

# TISP2072F3, TISP2082F3 DUAL SYMMETRICAL TRANSIENT VOLTAGE SUPPRESSORS

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## description (Continued)

The small-outline 8-pin assignment has been carefully chosen for the TISP series to maximise the inter-pin clearance and creepage distances which are used by standards (e.g. IEC950) to establish voltage withstand ratings.

## absolute maximum ratings

RATING		SYMBOL	VALUE	UNIT
Repetitive peak off-state voltage ( $0^{\circ}\text{C} < T_J < 70^{\circ}\text{C}$ )	'2072F3 '2082F3	$V_{\text{DRM}}$	$\pm 58$ $\pm 66$	V
Non-repetitive peak on-state pulse current (see Notes 1, 2 and 3)		$I_{\text{TSP}}$	120 80 70 60 50 38 50 50 45 35	A
1/2 $\mu\text{s}$ (Gas tube differential transient, open-circuit voltage wave shape 1/2 $\mu\text{s}$ )				
2/10 $\mu\text{s}$ (FCC Part 68, open-circuit voltage wave shape 2/10 $\mu\text{s}$ )				
8/20 $\mu\text{s}$ (ANSI C62.41, open-circuit voltage wave shape 1.2/50 $\mu\text{s}$ )				
10/160 $\mu\text{s}$ (FCC Part 68, open-circuit voltage wave shape 10/160 $\mu\text{s}$ )				
5/200 $\mu\text{s}$ (VDE 0433, open-circuit voltage wave shape 2 kV, 10/700 $\mu\text{s}$ )				
0.5/310 $\mu\text{s}$ (RLM 88, open-circuit voltage wave shape 1.5 kV, 0.5/700 $\mu\text{s}$ )				
5/310 $\mu\text{s}$ (CCITT IX K17/K20, open-circuit voltage wave shape 2 kV, 10/700 $\mu\text{s}$ )				
5/310 $\mu\text{s}$ (FTZ R12, open-circuit voltage wave shape 2 kV, 10/700 $\mu\text{s}$ )				
10/560 $\mu\text{s}$ (FCC Part 68, open-circuit voltage wave shape 10/560 $\mu\text{s}$ )				
10/1000 $\mu\text{s}$ (REA PE-60, open-circuit voltage wave shape 10/1000 $\mu\text{s}$ )				
Non-repetitive peak on-state current (see Notes 2 and 3)	D Package P Package SL Package	$I_{\text{TSM}}$	4 6 6	A rms
Initial rate of rise of on-state current, Linear current ramp, Maximum ramp value < 38 A		$di_F/dt$	250	A/ $\mu\text{s}$
Junction temperature		$T_J$	-40 to +150	$^{\circ}\text{C}$
Storage temperature range		$T_{\text{stg}}$	-40 to +150	$^{\circ}\text{C}$

- NOTES: 1. Further details on surge wave shapes are contained in the Applications Information section.  
2. Initially the TISP must be in thermal equilibrium with  $0^{\circ}\text{C} < T_J < 70^{\circ}\text{C}$ . The surge may be repeated after the TISP returns to its initial conditions.  
3. Above  $70^{\circ}\text{C}$ , derate linearly to zero at  $150^{\circ}\text{C}$  lead temperature.

## electrical characteristics for the T and R terminals, $T_J = 25^{\circ}\text{C}$

PARAMETER	TEST CONDITIONS	TISP2072F3		TISP2082F3		UNIT
		MIN	MAX	MIN	MAX	
$I_{\text{DRM}}$ Repetitive peak off-state current	$V_D = \pm V_{\text{DRM}}$ , $0^{\circ}\text{C} < T_J < 70^{\circ}\text{C}$		$\pm 10$		$\pm 10$	$\mu\text{A}$
$I_D$ Off-state current	$V_D = \pm 50\text{ V}$		$\pm 10$		$\pm 10$	$\mu\text{A}$
$C_{\text{off}}$ Off-state capacitance	$f = 100\text{ kHz}$ , $V_d = 100\text{ mV}$ , $V_D = 0$ , Third terminal voltage = 0 (see Notes 4 and 5)	32†	55	32†	55	pF

- NOTES: 4. These capacitance measurements employ a three terminal capacitance bridge incorporating a guard circuit. The third terminal is connected to the guard terminal of the bridge.  
5. Further details on capacitance are given in the Applications Information section.

† Typical value of the parameter, not a limit value.

## electrical characteristics for the T and G or the R and G terminals, $T_J = 25^{\circ}\text{C}$

PARAMETER	TEST CONDITIONS	TISP2072F3		TISP2082F3		UNIT
		MIN	MAX	MIN	MAX	
$I_{\text{DRM}}$ Repetitive peak off-state current	$V_D = \pm V_{\text{DRM}}$ , $0^{\circ}\text{C} < T_J < 70^{\circ}\text{C}$		$\pm 10$		$\pm 10$	$\mu\text{A}$

## PRODUCT INFORMATION

# TISP2072F3, TISP2082F3 DUAL SYMMETRICAL TRANSIENT VOLTAGE SUPPRESSORS

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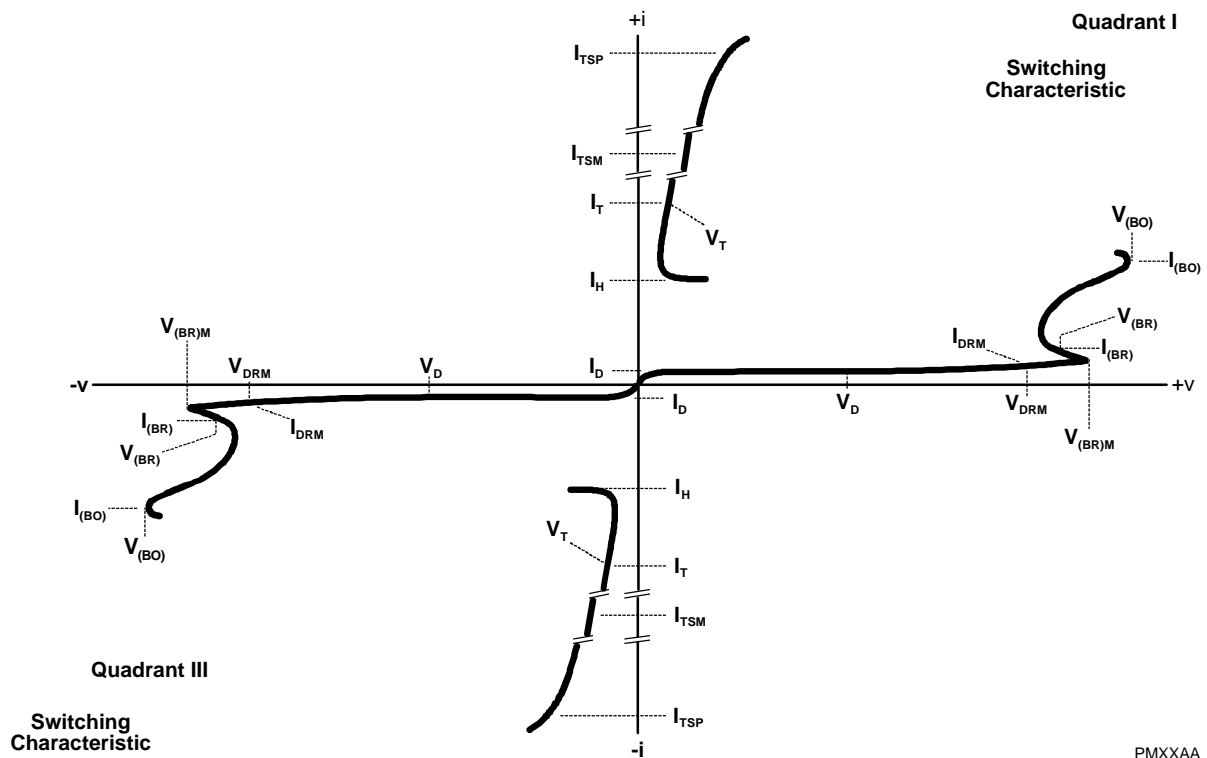
## electrical characteristics for the T and G or the R and G terminals, $T_J = 25^\circ\text{C}$ (Continued)

PARAMETER	TEST CONDITIONS	TISP2072F3		TISP2082F3		UNIT
		MIN	MAX	MIN	MAX	
$V_{(BO)}$ Breakover voltage	$dv/dt = \pm 250 \text{ V/ms}$ , Source Resistance = $300 \Omega$		$\pm 72$		$\pm 82$	V
$V_{(BO)}$ Impulse breakover voltage	$dv/dt = \pm 1000 \text{ V}/\mu\text{s}$ , $di/dt < 20 \text{ A}/\mu\text{s}$ Source Resistance = $50 \Omega$		$\pm 84^\dagger$		$\pm 94^\dagger$	V
$I_{(BO)}$ Breakover current	$dv/dt = \pm 250 \text{ V/ms}$ , Source Resistance = $300 \Omega$	$\pm 0.15$	$\pm 0.6$	$\pm 0.15$	$\pm 0.6$	A
$V_T$ On-state voltage	$I_T = \pm 5 \text{ A}$ , $t_W = 100 \mu\text{s}$		$\pm 3$		$\pm 3$	V
$I_H$ Holding current	$di/dt = -/+30 \text{ mA/ms}$	$\pm 0.15$		$\pm 0.15$		A
$dv/dt$ Critical rate of rise of off-state voltage	Linear voltage ramp, Maximum ramp value $< 0.85V_{(BR)MIN}$	$\pm 5$		$\pm 5$		$\text{kV}/\mu\text{s}$
$I_D$ Off-state current	$V_D = \pm 50 \text{ V}$		$\pm 10$		$\pm 10$	$\mu\text{A}$
$C_{off}$ Off-state capacitance	$f = 100 \text{ kHz}$ , $V_d = 100 \text{ mV}$ $V_D = 0$ ,	$77^\dagger$	130	$77^\dagger$	130	$\text{pF}$
	Third terminal voltage = 0 $V_D = -5 \text{ V}$	$42^\dagger$	70	$42^\dagger$	70	$\text{pF}$
	(see Notes 6 and 7) $V_D = -50 \text{ V}$	$19^\dagger$	30	$19^\dagger$	30	$\text{pF}$

NOTES: 6 These capacitance measurements employ a three terminal capacitance bridge incorporating a guard circuit. The third terminal is connected to the guard terminal of the bridge.

7. Further details on capacitance are given in the Applications Information section.

### PARAMETER MEASUREMENT INFORMATION



**Figure 1. VOLTAGE-CURRENT CHARACTERISTIC FOR ANY PAIR OF TERMINALS**

The high level characteristics for terminals R and T are not guaranteed.

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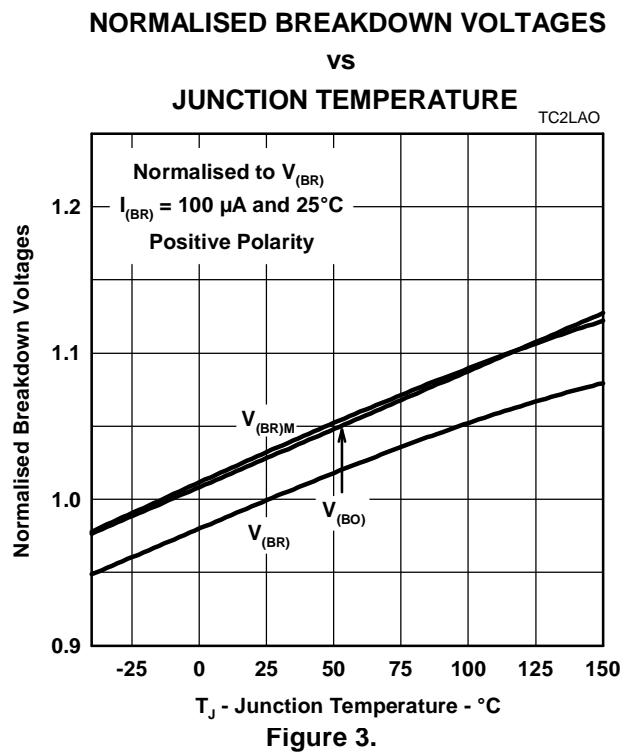
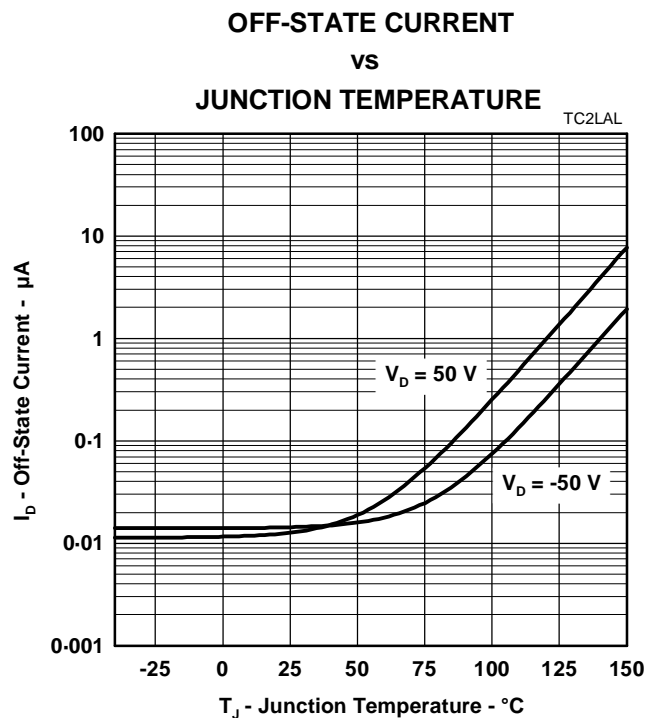
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† Typical value of the parameter, not a limit value.

## thermal characteristics

PARAMETER		MIN	TYP	MAX	UNIT
$R_{\theta JA}$ Junction to free air thermal resistance	D Package			160	°C/W
	P Package			100	
	SL Package			105	

## TYPICAL CHARACTERISTICS T and G, or R and G terminals



TYPICAL CHARACTERISTICS  
 T and G, or R and G terminals

NORMALISED BREAKDOWN VOLTAGES

VS

JUNCTION TEMPERATURE

TC2LAP

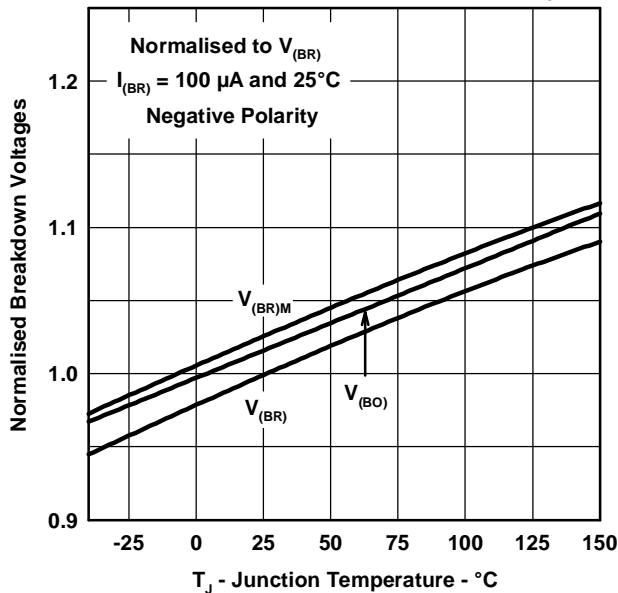


Figure 4.

ON-STATE CURRENT

VS

ON-STATE VOLTAGE

TC2MAQ

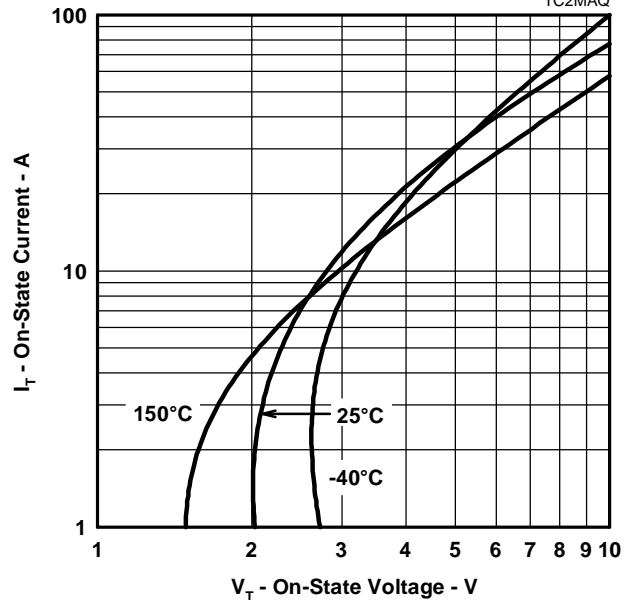


Figure 5.

HOLDING CURRENT & BREAKOVER CURRENT

VS

JUNCTION TEMPERATURE

TC2LAM

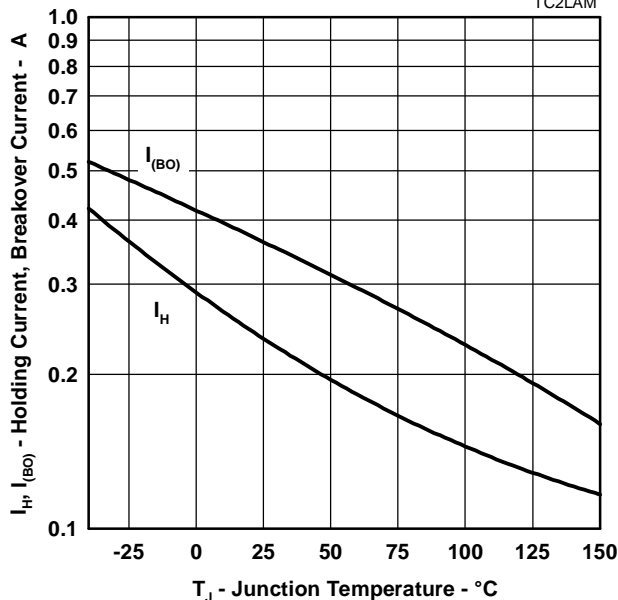


Figure 6.

NORMALISED BREAKOVER VOLTAGE

VS

RATE OF RISE OF PRINCIPLE CURRENT

TC2LAF

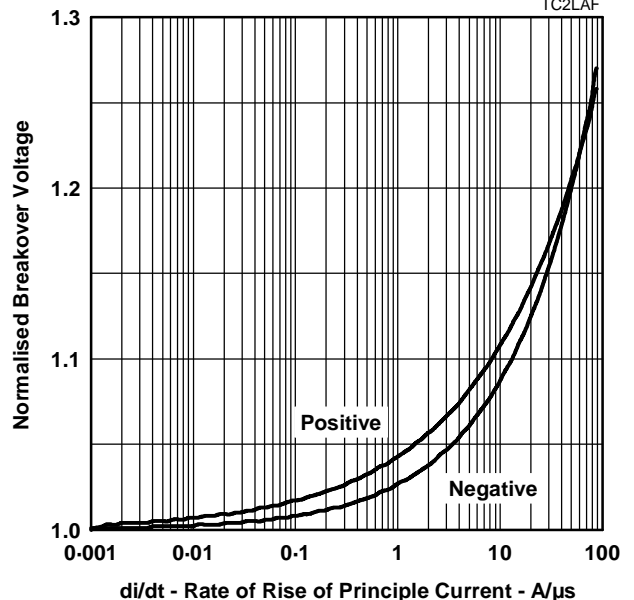


Figure 7.

TISP2072F3, TISP2082F3  
 DUAL SYMMETRICAL TRANSIENT  
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TYPICAL CHARACTERISTICS  
 T and G, or R and G terminals

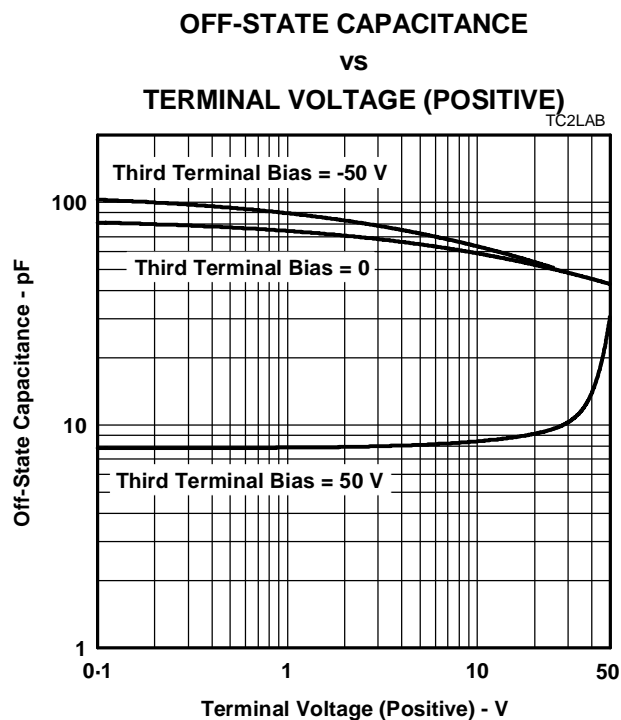


Figure 8.

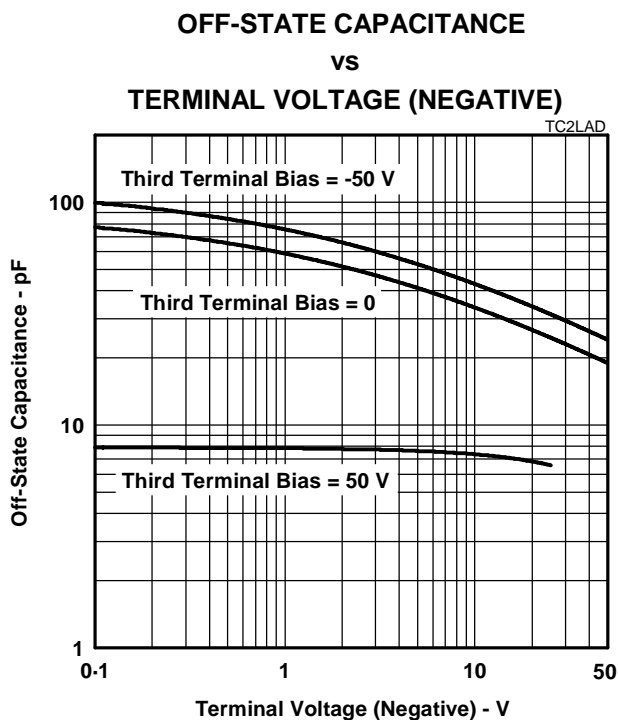


Figure 9.

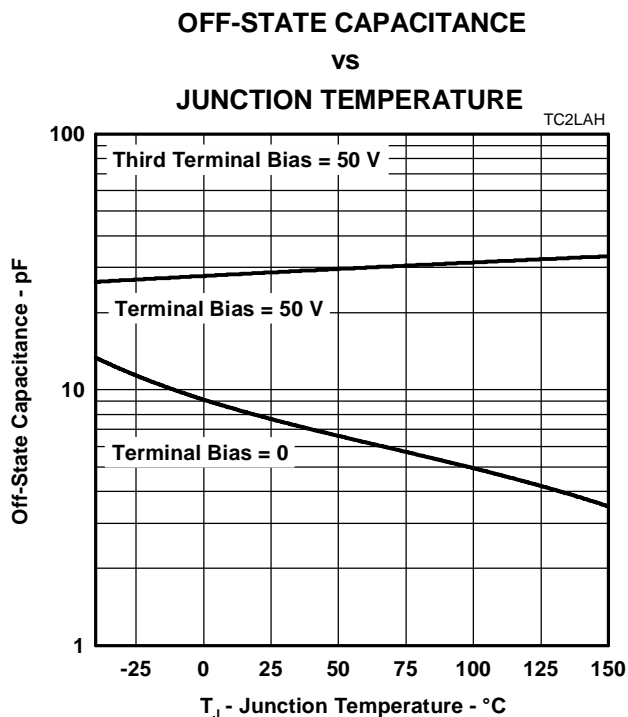


Figure 10.

TYPICAL CHARACTERISTICS  
 T and G, or R and G terminals

OFF-STATE CAPACITANCE  
 VS  
 JUNCTION TEMPERATURE

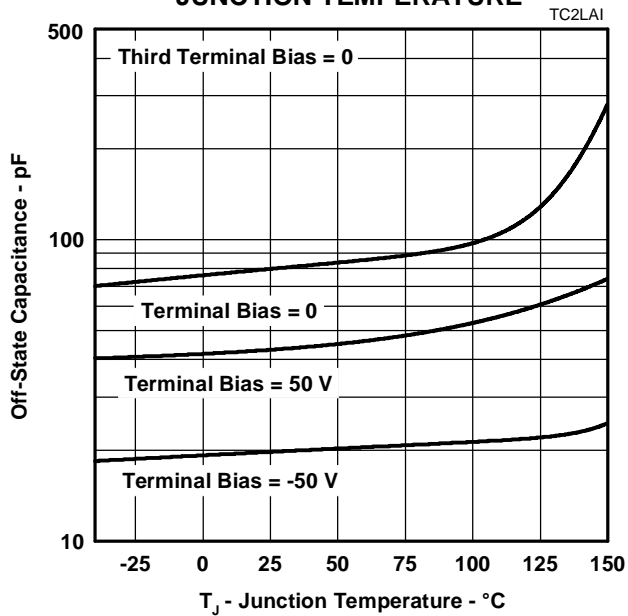


Figure 11.

OFF-STATE CAPACITANCE  
 VS  
 JUNCTION TEMPERATURE

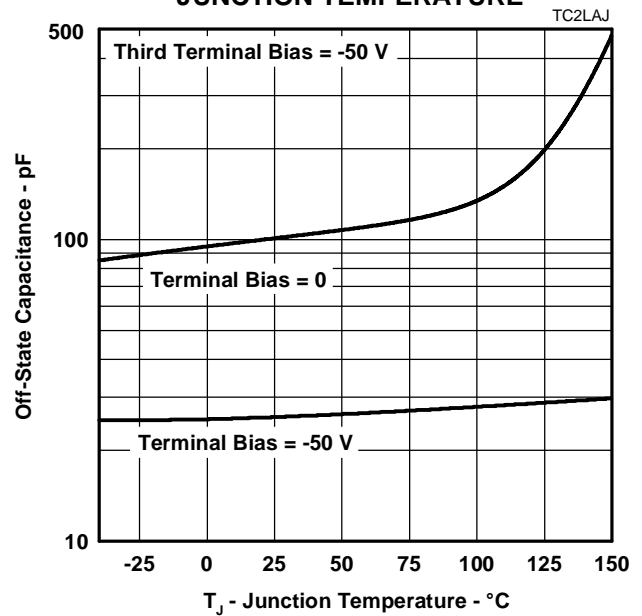


Figure 12.

SURGE CURRENT  
 VS  
 DECAY TIME

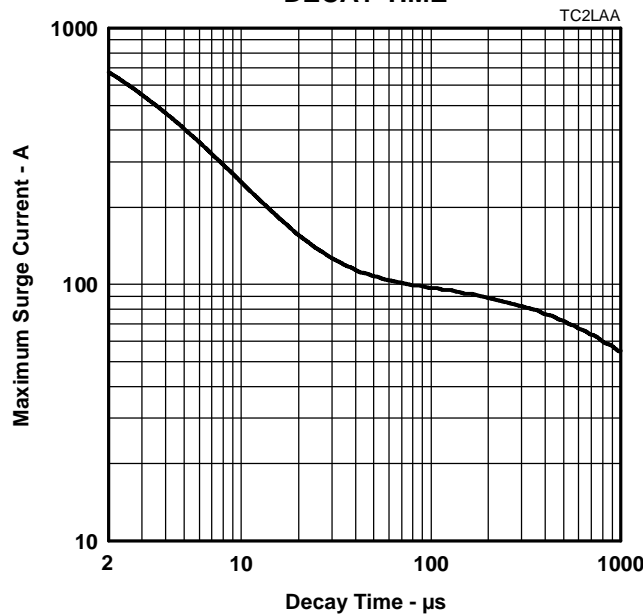


Figure 13.

TISP2072F3, TISP2082F3  
 DUAL SYMMETRICAL TRANSIENT  
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TYPICAL CHARACTERISTICS  
 T and R terminals

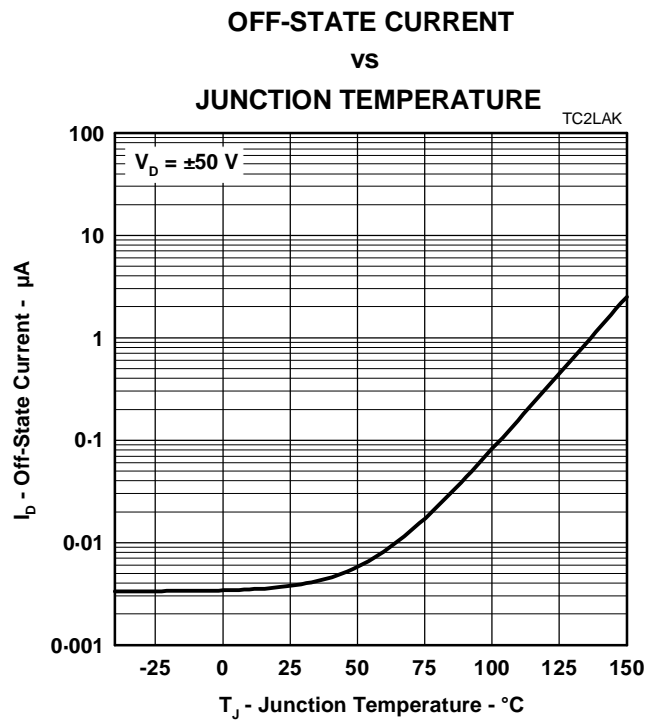


Figure 14.

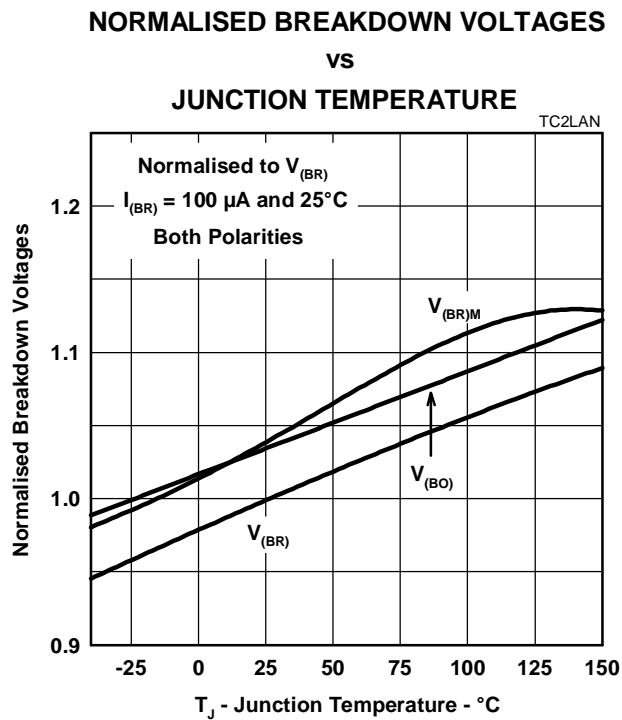


Figure 15.

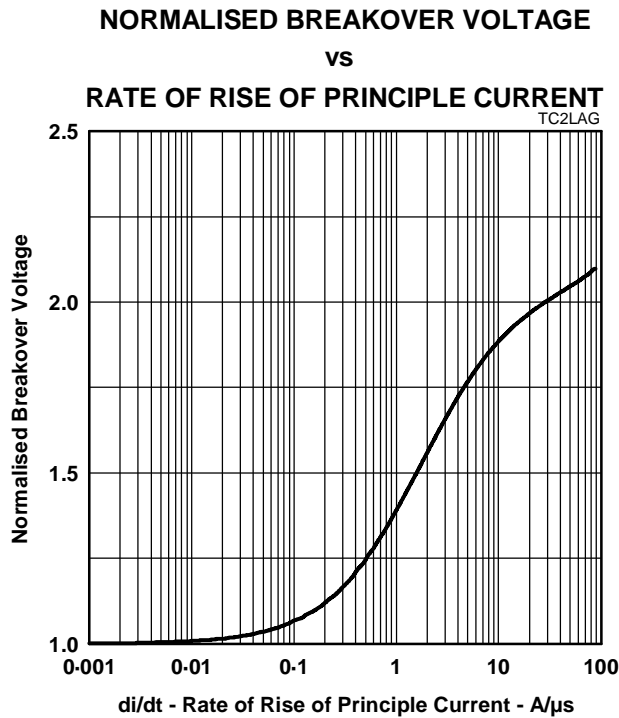


Figure 16.

TYPICAL CHARACTERISTICS  
 T and R terminals

OFF-STATE CAPACITANCE

VS

TERMINAL VOLTAGE (POSITIVE)

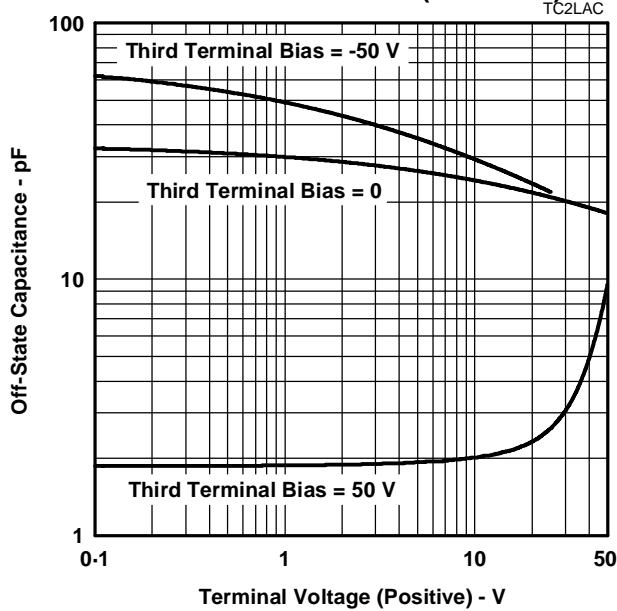


Figure 17.

OFF-STATE CAPACITANCE

VS

TERMINAL VOLTAGE (NEGATIVE)

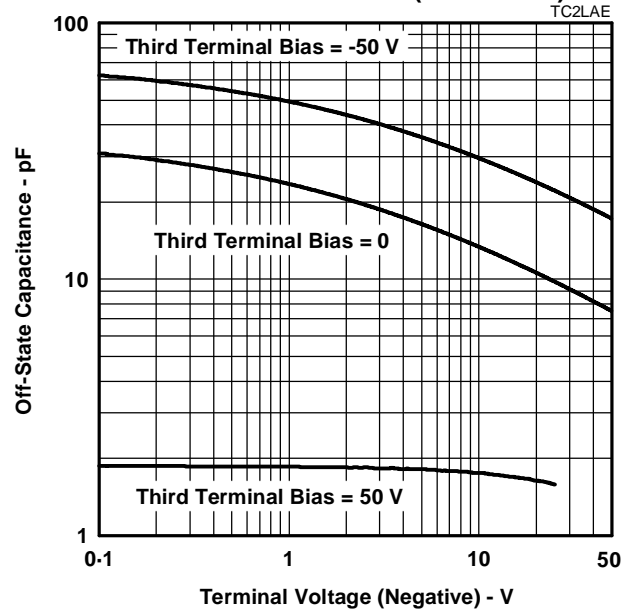


Figure 18.

THERMAL INFORMATION

MAXIMUM NON-RECURRING 50 Hz CURRENT

VS

CURRENT DURATION

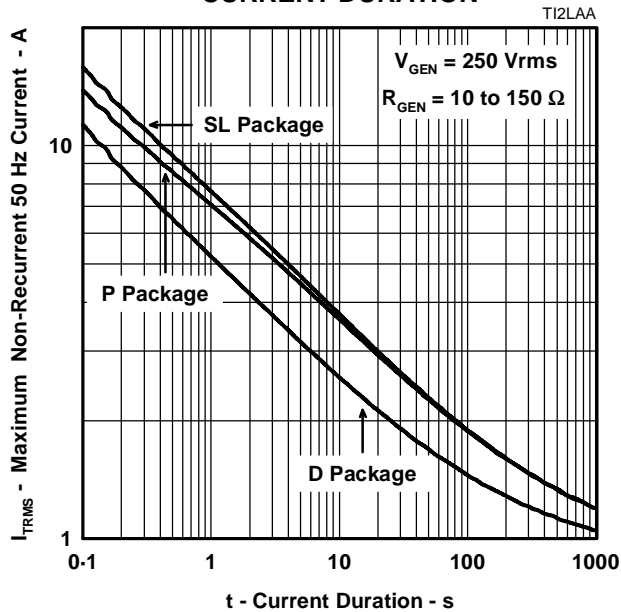


Figure 19.

THERMAL RESPONSE

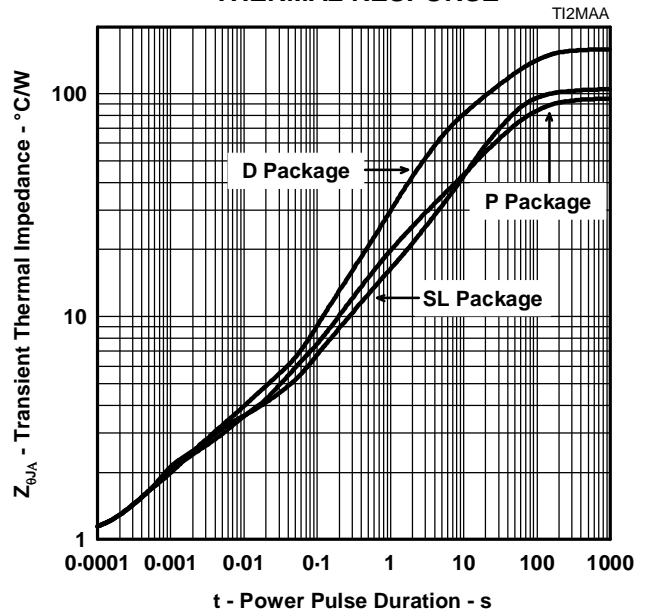


Figure 20.



# TISP2072F3, TISP2082F3 DUAL SYMMETRICAL TRANSIENT VOLTAGE SUPPRESSORS

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## APPLICATIONS INFORMATION

### electrical characteristics

The electrical characteristics of a TISP are strongly dependent on junction temperature,  $T_J$ . Hence a characteristic value will depend on the junction temperature at the instant of measurement. The values given in this data sheet were measured on commercial testers, which generally minimise the temperature rise caused by testing. Application values may be calculated from the parameters' temperature curves, the power dissipated and the thermal response curve ( $Z_{\theta}$ ).

### lightning surge

#### wave shape notation

Most lightning tests, used for equipment verification, specify a unidirectional sawtooth waveform which has an exponential rise and an exponential decay. Wave shapes are classified in terms of peak amplitude (voltage or current), rise time and a decay time to 50% of the maximum amplitude. The notation used for the wave shape is *amplitude, rise time/decay time*. A 50A, 5/310  $\mu$ s wave shape would have a peak current value of 50 A, a rise time of 5  $\mu$ s and a decay time of 310  $\mu$ s. The TISP surge current graph comprehends the wave shapes of commonly used surges.

### generators

There are three categories of surge generator type, single wave shape, combination wave shape and circuit defined. Single wave shape generators have essentially the same wave shape for the open circuit voltage and short circuit current (e.g. 10/1000  $\mu$ s open circuit voltage and short circuit current). Combination generators have two wave shapes, one for the open circuit voltage and the other for the short circuit current (e.g. 1.2/50  $\mu$ s open circuit voltage and 8/20  $\mu$ s short circuit current) Circuit specified generators usually equate to a combination generator, although typically only the open circuit voltage waveshape is referenced (e.g. a 10/700  $\mu$ s open circuit voltage generator typically produces a 5/310  $\mu$ s short circuit current). If the combination or circuit defined generators operate into a finite resistance the wave shape produced is intermediate between the open circuit and short circuit values.

### current rating

When the TISP switches into the on-state it has a very low impedance. As a result, although the surge wave shape may be defined in terms of open circuit voltage, it is the current wave shape that must be used to assess the required TISP surge capability. As an example, the CCITT IX K17 1.5 kV, 10/700  $\mu$ s surge is changed to a 38 A, 5/310  $\mu$ s waveshape when driving into a short circuit. Thus the TISP surge current capability, when directly connected to the generator, will be found for the CCITT IX K17 waveform at 310  $\mu$ s on the surge graph and not 700  $\mu$ s. Some common short circuit equivalents are tabulated below:

STANDARD	OPEN CIRCUIT VOLTAGE	SHORT CIRCUIT CURRENT
CCITT IX K17	1.5 kV, 10/700 $\mu$ s	38 A, 5/310 $\mu$ s
CCITT IX K20	1 kV, 10/700 $\mu$ s	25 A, 5/310 $\mu$ s
RLM88	1.5 kV, 0.5/700 $\mu$ s	38 A, 0.2/310 $\mu$ s
VDE 0433	2.0 kV, 10/700 $\mu$ s	50 A, 5/200 $\mu$ s
FTZ R12	2.0 kV, 10/700 $\mu$ s	50 A, 5/310 $\mu$ s

Any series resistance in the protected equipment will reduce the peak circuit current to less than the generators' short circuit value. A 2 kV open circuit voltage, 50 A short circuit current generator has an effective output impedance of 40  $\Omega$  (2000/50). If the equipment has a series resistance of 25  $\Omega$  then the surge current requirement of the TISP becomes 31 A (2000/65) and not 50 A.

## PRODUCT INFORMATION

## APPLICATIONS INFORMATION

### protection voltage

The protection voltage, ( $V_{(BO)}$ ), increases under lightning surge conditions due to thyristor regeneration. This increase is dependent on the rate of current rise,  $di/dt$ , when the TISP is clamping the voltage in its breakdown region. The  $V_{(BO)}$  value under surge conditions can be estimated by multiplying the 50 Hz rate  $V_{(BO)}$  (250 V/ms) value by the normalised increase at the surge's  $di/dt$  (Figure 7.) . An estimate of the  $di/dt$  can be made from the surge generator voltage rate of rise,  $dv/dt$ , and the circuit resistance.

As an example, the CCITT IX K17 1.5 kV, 10/700  $\mu$ s surge has an average  $dv/dt$  of 150 V/ $\mu$ s, but, as the rise is exponential, the initial  $dv/dt$  is higher, being in the region of 450 V/ $\mu$ s. The instantaneous generator output resistance is 25  $\Omega$ . If the equipment has an additional series resistance of 20  $\Omega$ , the total series resistance becomes 45  $\Omega$ . The maximum  $di/dt$  then can be estimated as 450/45 = 10 A/ $\mu$ s. In practice the measured  $di/dt$  and protection voltage increase will be lower due to inductive effects and the finite slope resistance of the TISP breakdown region.

### capacitance

#### off-state capacitance

The off-state capacitance of a TISP is sensitive to junction temperature,  $T_J$ , and the bias voltage, comprising of the dc voltage,  $V_D$ , and the ac voltage,  $V_d$ . All the capacitance values in this data sheet are measured with an ac voltage of 100 mV. The typical 25°C variation of capacitance value with ac bias is shown in Figure 21. When  $V_D \gg V_d$  the capacitance value is independent on the value of  $V_d$ . The capacitance is essentially constant over the range of normal telecommunication frequencies.

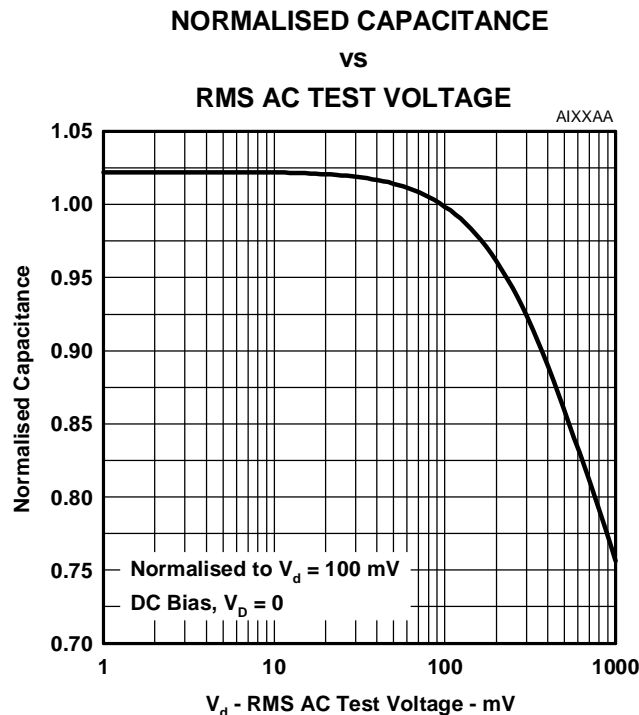


Figure 21.

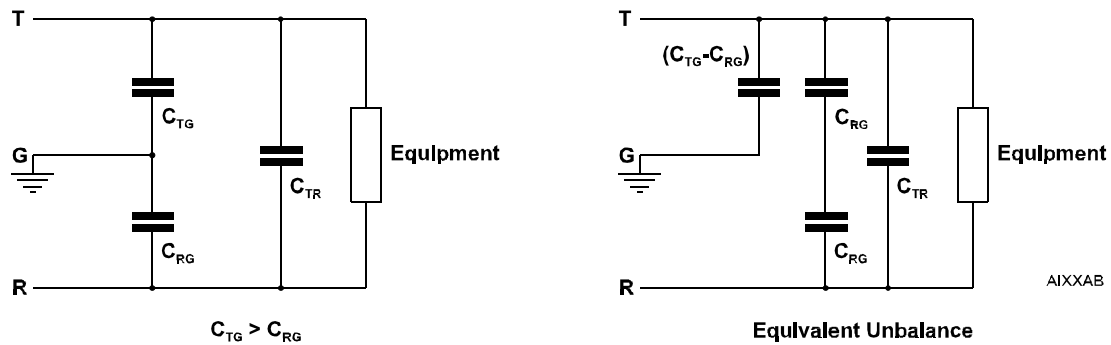
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**APPLICATIONS INFORMATION**

**longitudinal balance**

Figure 22 shows a three terminal TISP with its equivalent "delta" capacitance. Each capacitance,  $C_{TG}$ ,  $C_{RG}$  and  $C_{TR}$ , is the true terminal pair capacitance measured with a three terminal or guarded capacitance bridge. If wire R is biased at a larger potential than wire T then  $C_{TG} > C_{RG}$ . Capacitance  $C_{TG}$  is equivalent to a capacitance of  $C_{RG}$  in parallel with the capacitive difference of  $(C_{TG} - C_{RG})$ . The line capacitive unbalance is due to  $(C_{TG} - C_{RG})$  and the capacitance shunting the line is  $C_{TR} + C_{RG}/2$ .



**Figure 22.**

All capacitance measurements in this data sheet are three terminal guarded to allow the designer to accurately assess capacitive unbalance effects. Simple two terminal capacitance meters (unguarded third terminal) give false readings as the shunt capacitance via the third terminal is included.



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**VOLTAGE SUPPRESSORS**

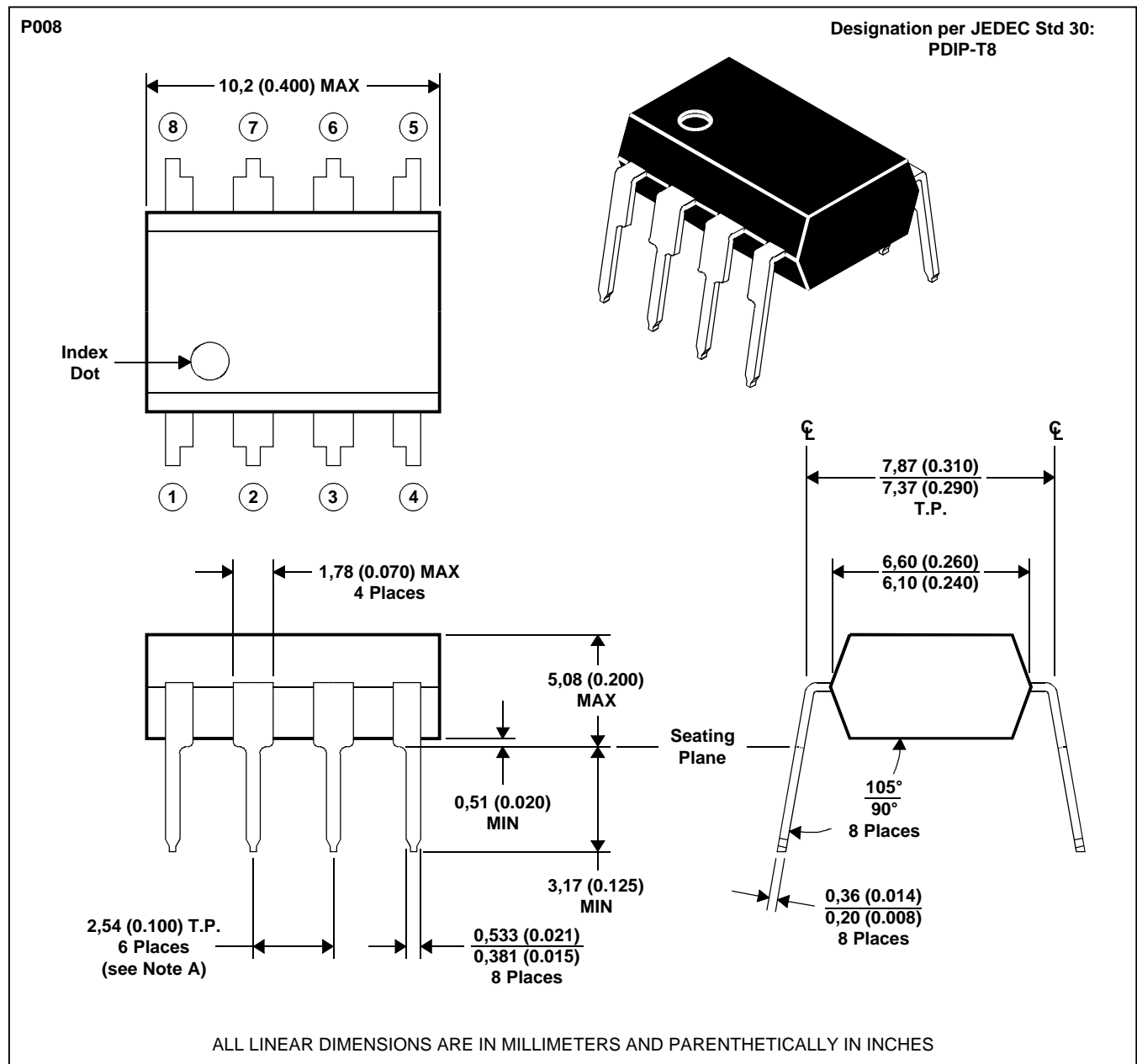
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**MECHANICAL DATA**

**P008**

**plastic dual-in-line package**

This dual-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. The package is intended for insertion in mounting-hole rows on 7,62 (0.300) centers. Once the leads are compressed and inserted, sufficient tension is provided to secure the package in the board during soldering. Leads require no additional cleaning or processing when used in soldered assembly.



NOTE A: Each pin centerline is located within 0,25 (0.010) of its true longitudinal position

MDXXABA

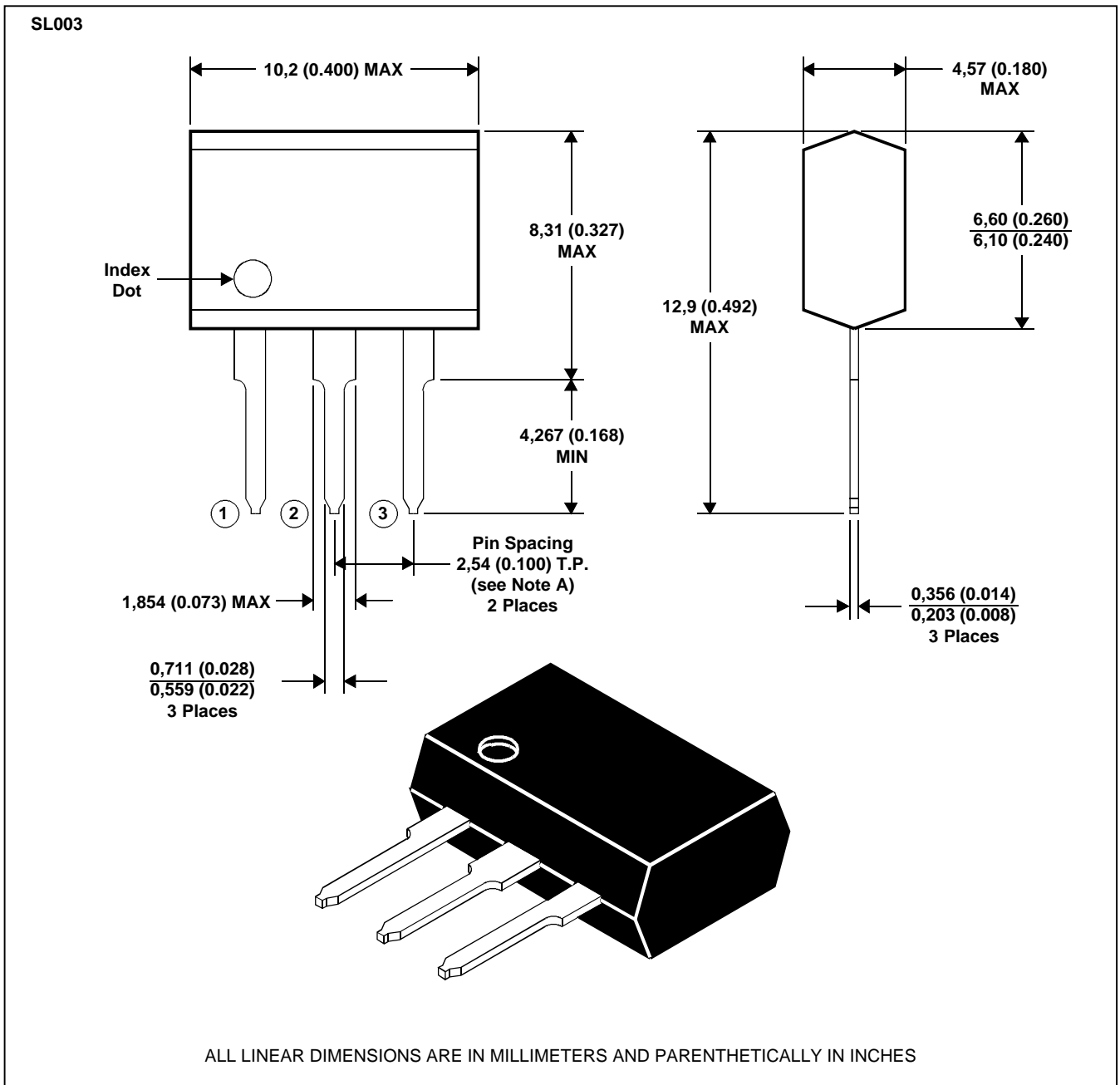
**PRODUCT INFORMATION**

MECHANICAL DATA

SL003

3-pin plastic single-in-line package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTES: A. Each pin centerline is located within 0,25 (0.010) of its true longitudinal position.  
 B. Body molding flash of up to 0,15 (0.006) may occur in the package lead plane.

MDXXAD

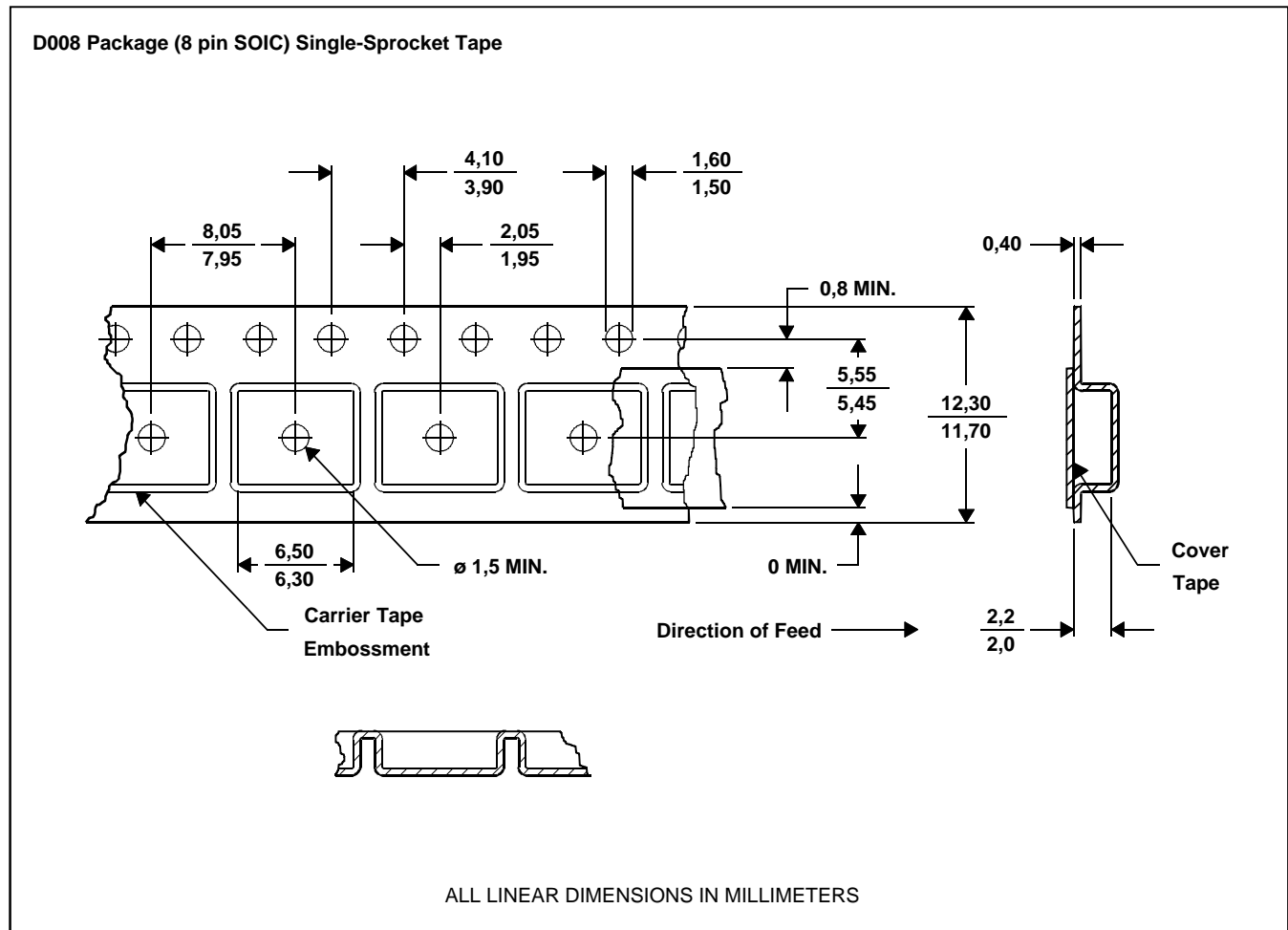
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DUAL SYMMETRICAL TRANSIENT  
VOLTAGE SUPPRESSORS**

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**MECHANICAL DATA**

**D008**

**tape dimensions**



NOTES: A. Taped devices are supplied on a reel of the following dimensions:-

MDXXAT

Reel diameter:	330 +0,0/-4,0 mm
Reel hub diameter:	100 ±2,0 mm
Reel axial hole:	13,0 ±0,2 mm

B. 2500 devices are on a reel.

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