

AP3700/A

Pin Configuration

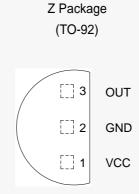


Figure 2. Pin Configuration of AP3700/A (Top View)

Pin Description

Pin Number TO-92	Pin Name	Function
1	VCC	The power supply of the IC, and is generally connected to opto-coupler's emitter
2	GND	Supply ground
3	OUT	The output pin, connected to the emitter of NPN transistor or the source of MOSFET



AP3700/A

Functional Block Diagram

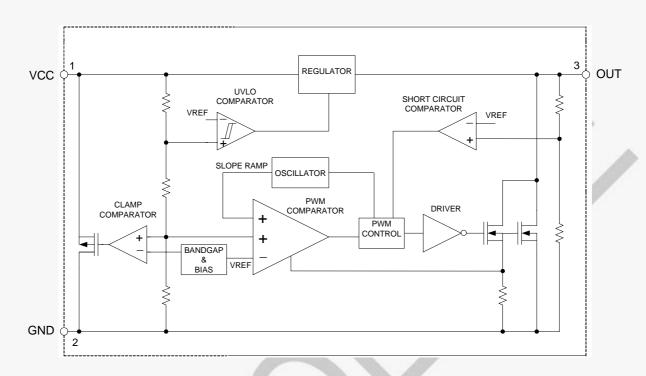
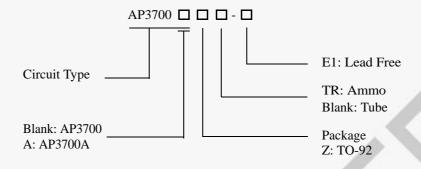


Figure 3. Functional Block Diagram of AP3700/A



AP3700/A

Ordering Information



Package	Switching Frequency	Temperature Range	Part Number	Marking ID	Packing Type	
TO-92	60kHz	-40 to 85°C	AP3700Z-E1	3700Z-E1	Bulk	
			AP3700ZTR-E1	3700Z-E1	Ammo	
			AP3700AZ-E1	3700AZ-E1	Bulk	
			AP3700AZTR-E1	3700AZ-E1	Ammo	

BCD Semiconductor's Pb-free products, as designated with "E1" suffix in the part number, are RoHS compliant.

Absolute Maximum Ratings (Note 1)

Parameter	Value	Unit
Supply Voltage	-0.3 to 6.0	V
Voltage at OUT (Note 2)	-0.3 to 40	V
Output Current at OUT	Internally limited	A
Power Dissipation	0.6	W
Operating Junction Temperature	150	°C
Storage Temperature	-65 to 150	°C
Lead Temperature (Soldering, 10s)	300	°C
ESD (Machine Model)	200	V

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Note 2: The Breadown Voltage. The actual operating voltage can not be greater than the value listed here.



AP3700/A

Electrical Characteristics

(V_{CC} =4V, T_J =25 o C, unless otherwise specified.)

Parameter	Symbol	Conditions		Min	Тур	Max	Unit	
UVLO SECTION		l			I			
Start-up Voltage	V _{TH (ST)}			5.00	5.25	5.50	V	
Minimal Operating Voltage	V _{OPR} (min)			3.4	3.65	3.9	V	
STANDBY CURRENT SEC	CTION	<u> </u>			- 4			
Start-up Current (Note 3)	I_{ST}	V _{CC} =4V			0.22	0.4	mA	
Operating Current	I _{CC(OPR)}			- 4	0.45	0.7	IIIA	
V _{CC} Zener Voltage	V_{Z}	I _{CC} =10mA		6	6.3		V	
Dynamic Impedance	R _{VCC}	V _{CC} =3.8 to 4.8V		-	18		kΩ	
INTERNAL OSCILLATOR							•	
Switching Frequency	F_{SW}			50	60	75	kHz	
Frequency Dither				±2	±2.5	±3	kHz	
Temperature Stability					5	8	%	
DRIVE OUTPUT SECTION	N	-						
OUT Start-up Voltage	V _{ST}				8.5	11	V	
Short Circuit Threashold Voltage (Note 4)	V _{SC}			•	6		V	
Rise Time	T_{R}	C_L =1nF, 15Ω pull-up			60		ns	
Fall Time	T_{F}	C_L =1nF, 15 Ω pull-up			30		115	
Maximum Duty Cycle	D _{MAX}	$V_{OPR}(min) + 0.2V$	1	67	75	84	%	
Minimum Duty Cycle		V _{CC} =V _{TH (ST)} -0.2V			3			
Driver OUT On-Resistance	R _{OUT}	I _{OUT} =0.06A			3		Ω	
Switch Off Current (OUT)		Driver off,			20	40	μΑ	
Effective Current Limit	I _{LIM}	$V_{CC} = V_{OPR} + 0.1V$	For AP3700	460			mA	
OUT Current Coefficient	G_{A}		For AP3700A	500	-0.3		A/V	

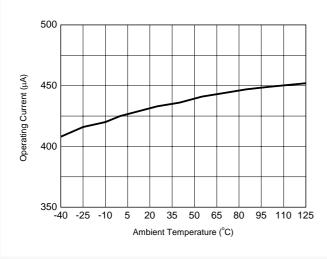
Note 3: AP3700/A is an emitter drive controller, the PWM controller will start up only when the voltage on the OUT pin rises up to its threshould value.

Note 4: AP3700/A performs its short circuit function by shutdown the OUT pulse when the OUT pin voltage drops below its short circuit threshold value.



AP3700/A

Typical Performance Characteristics



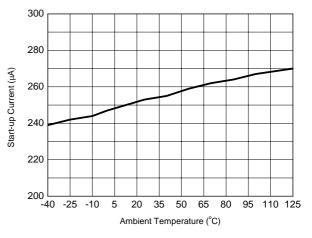
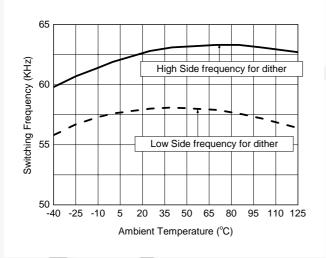


Figure 4. Operating Current vs. Ambient Temperature

Figure 5. Start-up Current vs. Ambient Temperature



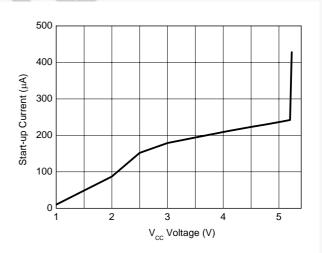


Figure 6. Switching Frequency vs. Ambient Temperature

Figure 7. Start-up Current vs. V_{CC} Voltage



AP3700/A

Function Description

1. Startup Circuit

Figure 3 is the functional block diagram of AP3700/A, and there are 3 external pins: the VCC pin, the OUT pin and GND pin. In typical application shown by Figure 10, the VCC pin is used for both bias supply and feedback control. The OUT pin directly drives external NPN transistor or MOSFET, and also provides initial bias power for UVLO comparator. When the IC works in PWM mode, the auxiliary winding will supply the $\rm V_{CC}$ enough operating current.

Figure 8 shows the start-up sequence of the V_{CC} and the $V_{OUT}\!\!$

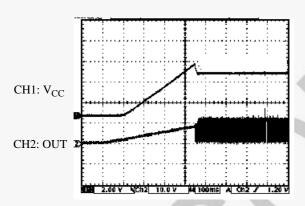


Figure 8. Start-up Sequence of V_{CC} and V_{OUT}

2. V_{CC}/Feedback Control

An opto-coupler and secondary constant voltage/current controller consists of voltage feedback network. When load is heavy, the voltage on VCC will be lower to enlarge duty cycle; on the contrary, if load drops, the voltage on VCC will rise to reduce duty cycle.

3. Frequency Dither

Frequency dither is performed by periodically spreading a single switching frequency into adjacent frequency band, so the peak energy is spread. This technique can improve EMI by reducing both quasipeak and average EMI emissions.

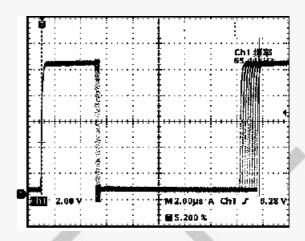


Figure 9. Frequency Dither Influences the Swithcing Cycle

AP3700/A has reference switching frequency of 60 kHz, and its frequency deviation is ± 2.5 kHz in period of 2ms. Figure 9 shows the frequency dither influence to the waveform .

4. Current Limit Control

The AP3700/A employs current mode control to improve transient response and voltage stability. In Figure 10, the external inductor current through the OUT pin is converted to a voltage by an internal resistor, and this voltage will participate to control duty cycle and peak inductor current.



AP3700/A

Typical Application

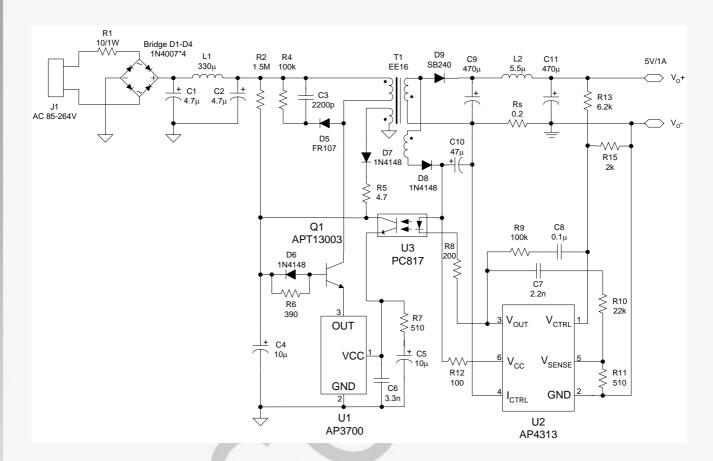


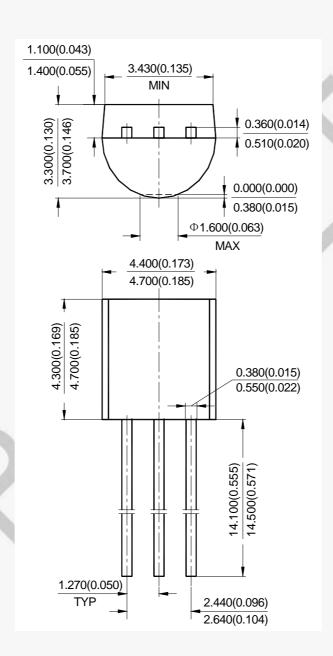
Figure 10. 5V/1A Output for Battery Charger of Mobile Phone

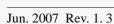


AP3700/A

Mechanical Dimensions

TO-92 Unit: mm(inch)



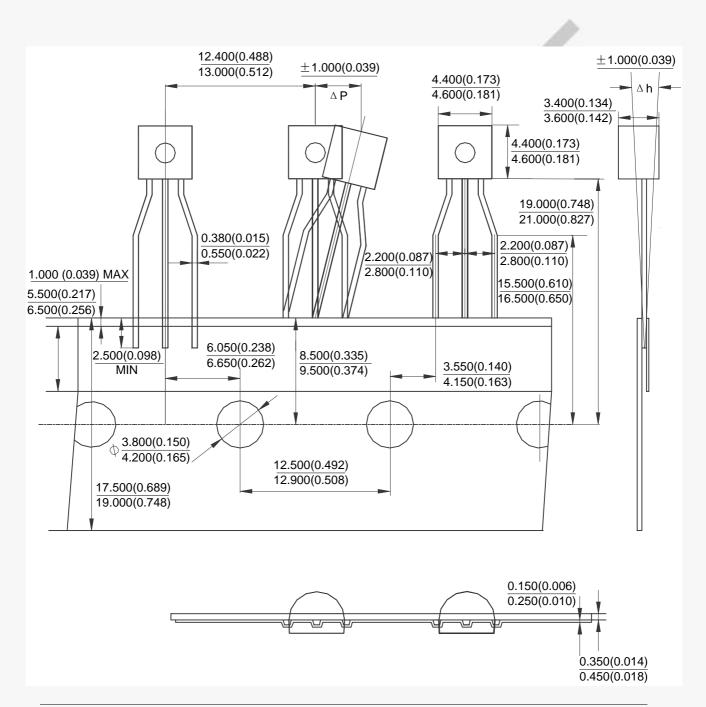




AP3700/A

Mechanical Dimensions

TO-92 Ammo Packing Unit: mm(inch)



Jun. 2007 Rev. 1. 3

BCD Semiconductor Manufacturing Limited





BCD Semiconductor Manufacturing Limited

http://www.bcdsemi.com

IMPORTANT NOTICE

BCD Semiconductor Manufacturing Limited reserves the right to make changes without further notice to any products or specifications herein. BCD Semiconductor Manufacturing Limited does not assume any responsibility for use of any its products for any particular purpose, nor does BCD Semiconductor Manufacturing Limited assume any liability arising out of the application or use of any its products or circuits. BCD Semiconductor Manufacturing Limited does not convey any license under its patent rights or other rights nor the rights of others.

MAIN SITE

BCD Semiconductor Manufacturing Limited
- Wafer Fab
Shanghai SIM-BCD Semiconductor Manufacturing Limited

Shanghai SIM-BCD Semiconductor Manufacturing Limited 800, Yi Shan Road, Shanghai 200233, China Tel: +86-21-6485 1491, Fax: +86-21-5450 0008

REGIONAL SALES OFFICE

Shenzhen Office

Shanghai SIM-BCD Semiconductor Manufacturing Co., Ltd. Shenzhen Office Advanced Analog Circuits (Shanghai) Corporation Shenzhen Office Room E. 5F, Noble Center, No.1006, 3rd Fuzhong Road, Futian District, Shenzhen 518026, China Tel: +86-755-8826 7951

Fax: +86-755-8826 7865

BCD Semiconductor Manufacturing Limited

- IC Design Group Advanced Analog Circuits (Shanghai) Corporation 8F, Zone B, 900, Yi Shan Road, Shanghai 200233, China Tel: +86-21-6495 9539, Fax: +86-21-6485 9673

Taiwan Office

BCD Semiconductor (Taiwan) Company Limited 4F, 298-1, Rui Guang Road, Nei-Hu District, Taipei, Taiwan

Tel: +886-2-2656 2808 Fax: +886-2-2656 2806 USA Office BCD Semiconductor Corporation 30920 Huntwood Ave. Hayward, CA 94544, U.S.A

CA 94544, U.S.A Tel: +1-510-324-2988 Fax: +1-510-324-2788