Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	60	V
Drain-Gate Voltage ($R_{GS} \leq 1.0M\Omega$)		V_{DGR}	60	V
Gate-Source Voltage	Continuous Pulsed	V_{GSS}	±20 ±40	V
Drain Current (Note 5)	Continuous Continuous @ +100°C Pulsed	I_D	115 73 800	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_D	150	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	833	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage		BV_{DSS}	60	—	—	V	V_{GS} = 0V, I_D = 10μA
Zero Gate Voltage Drain Current	@ T_C = +25°C @ T_C = +125°C	I_{DSS}	—	—	1.0 500	μA	V_{DS} = 60V, V_{GS} = 0V
Gate-Body Leakage		I_{GSS}	—	—	±10	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage		$V_{GS(TH)}$	1.0	—	2.0	V	V_{DS} = V_{GS}, I_D = 250μA
Static Drain-Source On-Resistance	@ T_J = +25°C	$R_{DS(ON)}$	—	2.0	7.5	Ω	V_{GS} = 5.0V, I_D = 0.05A
	@ T_J = +125°C			4.4	13.5		V_{GS} = 10V, I_D = 0.5A
On-State Drain Current		$I_{D(ON)}$	0.5	1.0	—	A	V_{GS} = 10V, V_{DS} = 7.5V
Forward Transconductance		g_{FS}	80	—	—	mS	V_{DS} =10V, I_D = 0.2A
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance		C_{iss}	—	22	50	pF	V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance		C_{oss}	—	11	25	pF	
Reverse Transfer Capacitance		C_{rss}	—	2.0	5.0	pF	
SWITCHING CHARACTERISTICS (Note 7)							
Turn-On Delay Time		$t_{D(ON)}$	—	7.0	20	ns	V_{DD} = 30V, I_D = 0.2A,
Turn-Off Delay Time		$t_{D(OFF)}$	—	11	20	ns	R_L = 150Ω, V_{GEN} = 10V, R_{GEN} = 25Ω

Notes: 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
6. Short duration pulse test used to minimize self-heating effect.
7. Guaranteed by design. Not subject to production testing.

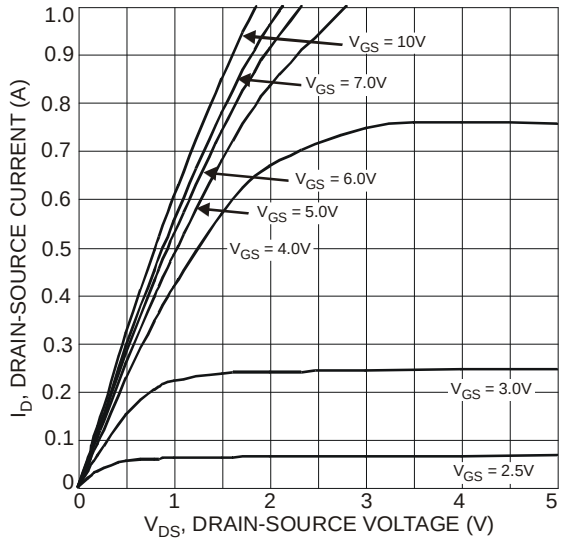


Fig. 1 On-Region Characteristics

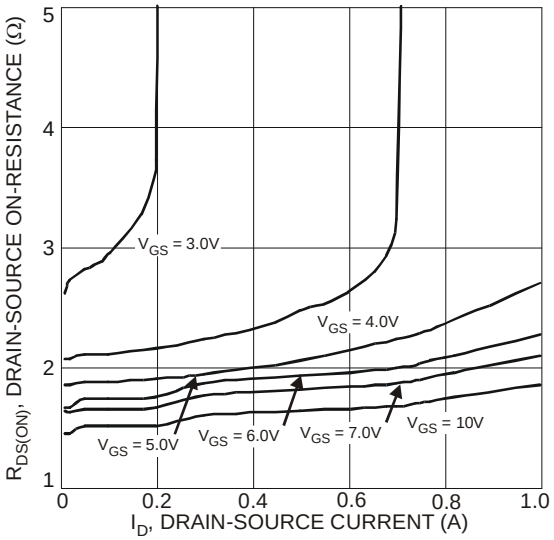


Fig. 2 On-Resistance Variation with Gate Voltage and Drain-Source Current

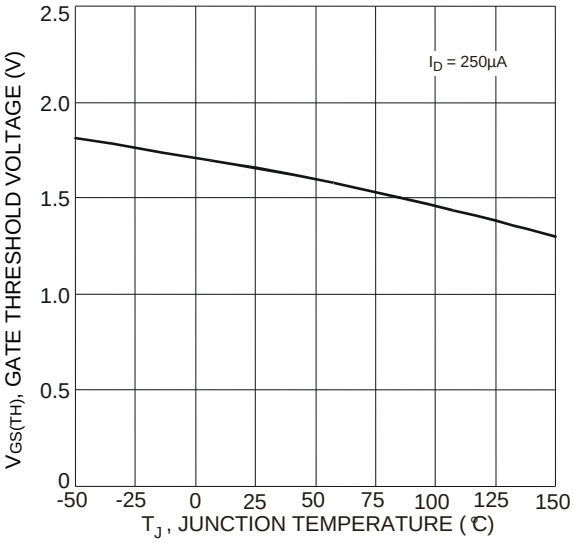


Fig. 3 Gate Threshold Variation with Temperature

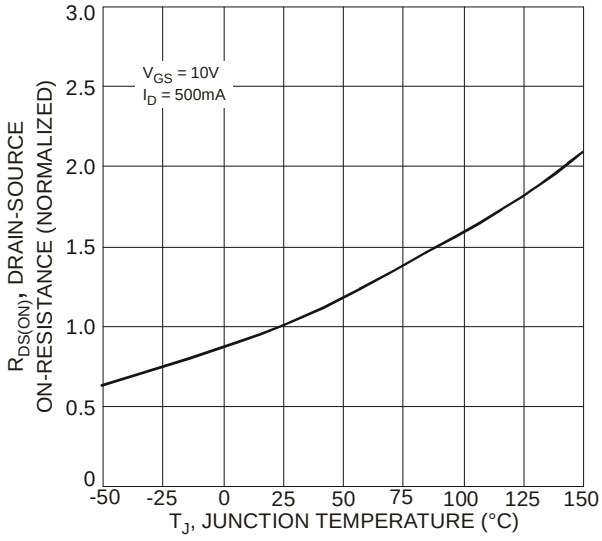


Fig. 4 On-Resistance Variation with Temperature

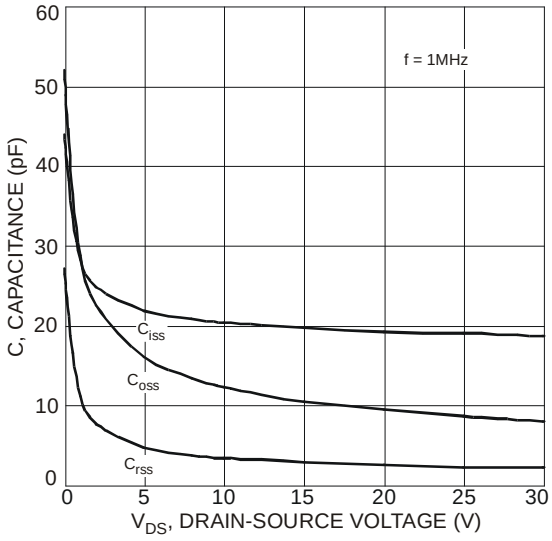


Fig. 5 Typical Capacitance

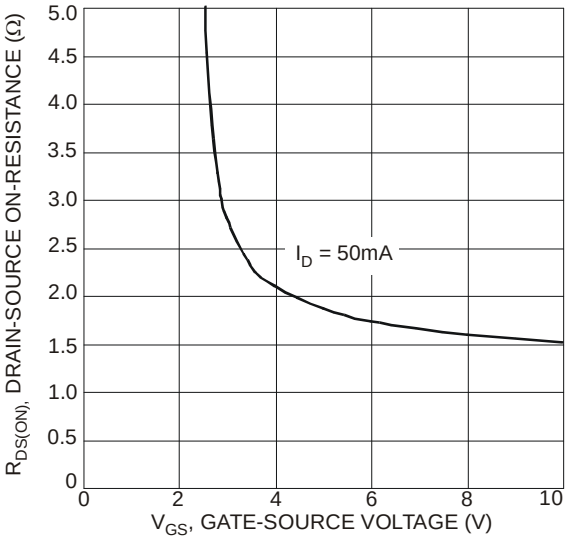
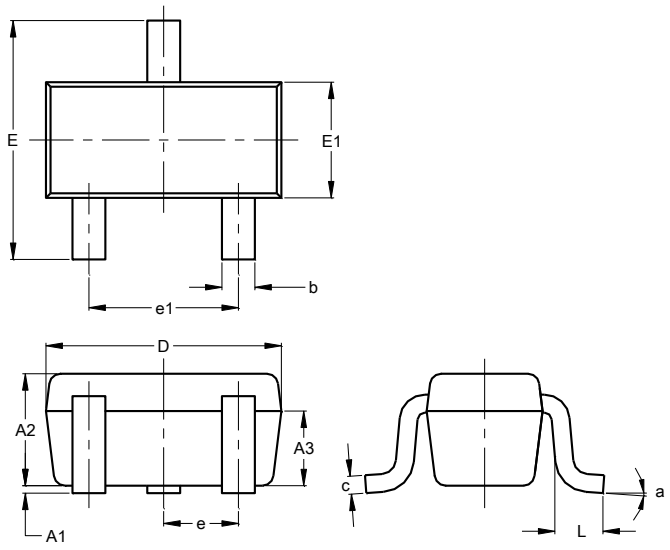


Fig. 6 On-Resistance vs. Gate-Source Voltage

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523

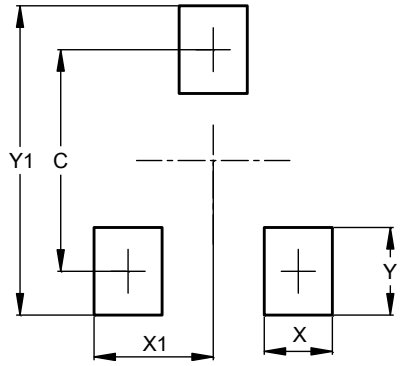


SOT523			
Dim	**Min**	**Max**	**Typ**
A1	0.00	0.10	0.05
A2	0.60	0.80	0.75
A3	0.45	0.65	0.50
b	0.15	0.30	0.22
c	0.10	0.20	0.12
D	1.50	1.70	1.60
E	1.45	1.75	1.60
E1	0.75	0.85	0.80
e	0.50 BSC		
e1	0.90	1.10	1.00
L	0.20	0.40	0.33
a	0°	--	8°
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523



Dimensions	Value (in mm)
C	1.29
X	0.40
X1	0.70
Y	0.51
Y1	1.80

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