

Low Noise Amplifier	
22 - 38 GHz	

Rev. V3

### Electrical Specifications: Freq: 22 - 38 GHz, V<sub>D</sub> = 3.3 V, T<sub>A</sub> = +25°C

Parameter	Units	Min.	Тур.	Max.
Small Signal Gain⁴	dB	17	19	—
Gain Flatness	dB	_	±2	_
Input Return Loss	dB	_	10	—
Output Return Loss	dB	_	13	_
Reverse isolation	dB	_	45	—
Noise Figure	dB	_	2.5	_
Output P1dB	dBm	_	5	
Supply Current (I <sub>D</sub> )	mA		55	65

4. Specified over 24-36 GHz

#### Absolute Maximum Ratings<sup>5,6</sup>

Parameter	Absolute Maximum		
Supply Voltage	7 VDC		
Supply Current	70 mA		
Input Power	12.0 dBm		
Storage Temperature	-65°C to +165°C		
Operating Temperature	-40°C to +85°C		
Channel Temperature <sup>7</sup>	+150°C		

5. Exceeding any one or combination of these limits may cause permanent damage to this device.

- MACOM does not recommend sustained operation near these survivability limits.
- Channel temperature directly affects a device's MTTF. It is recommended to keep channel temperature as low as possible to maximize lifetime.

#### **Handling Procedures**

Please observe the following precautions to avoid damage:

#### **Static Sensitivity**

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 1A devices.

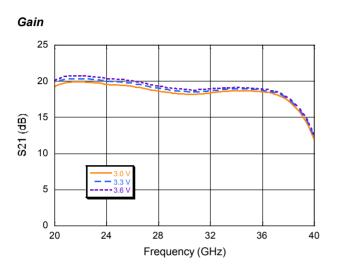
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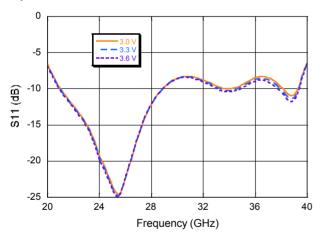


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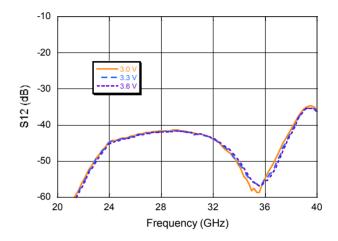
### **Typical Performance Curves**



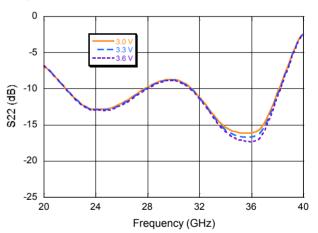
Input Return Loss



#### **Reverse Isolation**





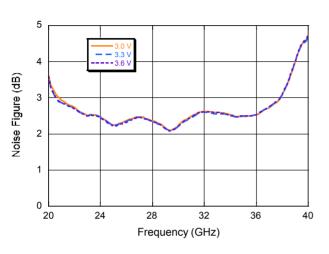


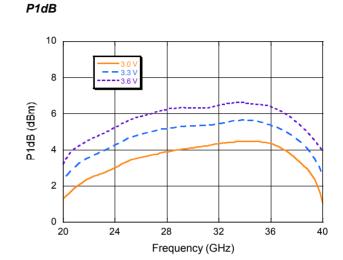
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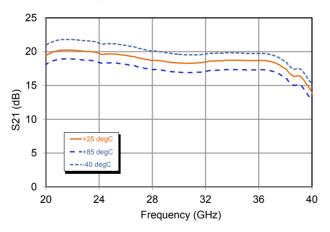
### **Typical Performance Curves**

#### Noise Figure





#### Gain Over Temperature

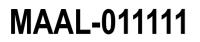


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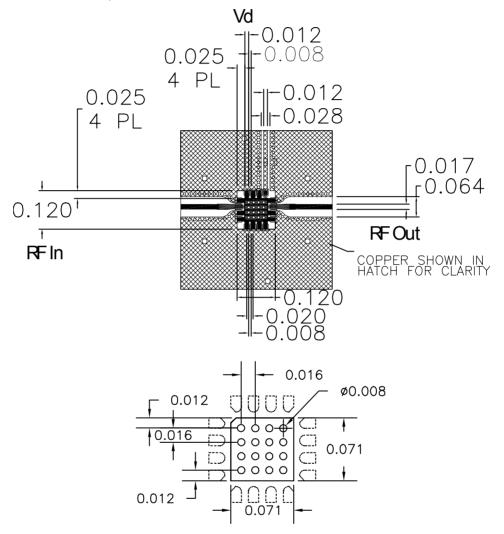
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### **Recommended Board Layout<sup>8</sup>**

(DXF file available from website)



8. Ground plane conductor should be removed under the corners of the package, as shown.

**Biasing** - The device is operated with a single, positive bias supply. The device performance is insensitive to changes in bias condition; however, gain and power handling can be slightly improved with higher bias conditions without significantly affecting the noise figure performance. Typical biasing conditions within the specified performance ranges are  $V_D = 3 V$ , 50 mA,  $V_D = 3.3 V$ , 55 mA,  $V_D = 3.6 V$ , 60 mA.

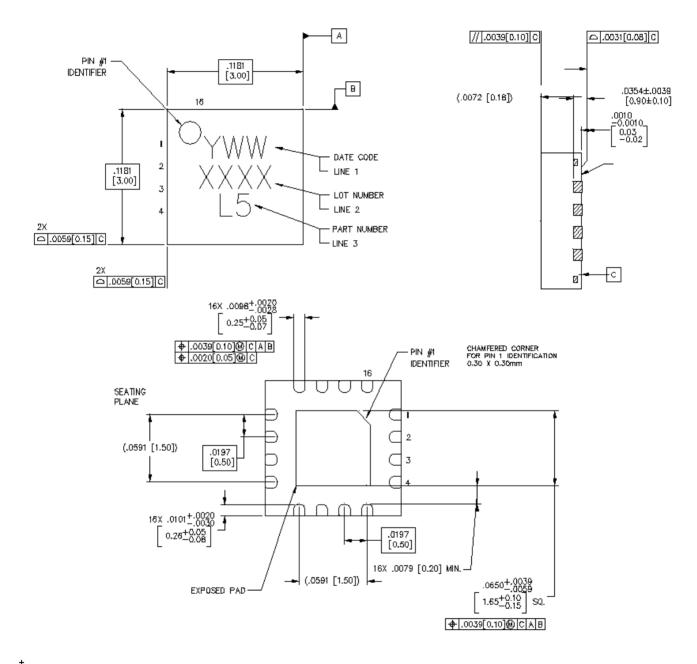
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## Lead-Free 3 mm 16-Lead PQFN<sup>†</sup>



<sup>†</sup> Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is NiPdAuAg

<sup>6</sup> 

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