

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_A = 25^\circ\text{C}$ unless otherwise noted)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	$BV_{DSS}$	30	--	--	V
Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250\mu\text{A}$	$V_{GS(TH)}$	1.2	1.6	2.5	V
Gate-Source Leakage Current	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	$I_{GSS}$	--	--	$\pm 100$	nA
Drain-Source Leakage Current	$V_{GS} = 0\text{V}, V_{DS} = 30\text{V}$	$I_{DSS}$	--	--	1	$\mu\text{A}$
	$V_{GS} = 0\text{V}, V_{DS} = 30\text{V}$ $T_J = 125^\circ\text{C}$		--	--	100	
Drain-Source On-State Resistance (Note 3)	$V_{GS} = 10\text{V}, I_D = 22\text{A}$	$R_{DS(on)}$	--	3	3.6	m $\Omega$
	$V_{GS} = 4.5\text{V}, I_D = 22\text{A}$		--	4	5.5	
Forward Transconductance (Note 3)	$V_{DS} = 5\text{V}, I_D = 22\text{A}$	$g_{fs}$	--	44	--	S
<b>Dynamic</b> (Note 4)						
Total Gate Charge	$V_{GS} = 10\text{V}, V_{DS} = 15\text{V}, I_D = 22\text{A}$	$Q_g$	--	50	--	nC
Total Gate Charge	$V_{GS} = 4.5\text{V}, V_{DS} = 15\text{V}, I_D = 22\text{A}$	$Q_g$	--	25	--	
Gate-Source Charge		$Q_{gs}$	--	7.3	--	
Gate-Drain Charge		$Q_{gd}$	--	12	--	
Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 15\text{V}$ $f = 1.0\text{MHz}$	$C_{iss}$	--	2530	--	pF
Output Capacitance		$C_{oss}$	--	376	--	
Reverse Transfer Capacitance		$C_{rss}$	--	249	--	
Gate Resistance	$f = 1.0\text{MHz}, \text{open drain}$	$R_g$	0.4	1.3	2.6	$\Omega$
<b>Switching</b> (Note 4)						
Turn-On Delay Time	$V_{GS} = 10\text{V}, V_{DS} = 15\text{V}, I_D = 22\text{A}, R_G = 2\Omega,$	$t_{d(on)}$	--	4.8	--	ns
Turn-On Rise Time		$t_r$	--	10.4	--	
Turn-Off Delay Time		$t_{d(off)}$	--	25.2	--	
Turn-Off Fall Time		$t_f$	--	10.6	--	
<b>Source-Drain Diode</b>						
Forward Voltage (Note 3)	$V_{GS} = 0\text{V}, I_S = 22\text{A}$	$V_{SD}$	--	--	1	V
Reverse Recovery Time	$I_S = 22\text{A},$ $di/dt = 100\text{A}/\mu\text{s}$	$t_{rr}$	--	30	--	ns
Reverse Recovery Charge		$Q_{rr}$	--	13	--	nC

**Notes:**

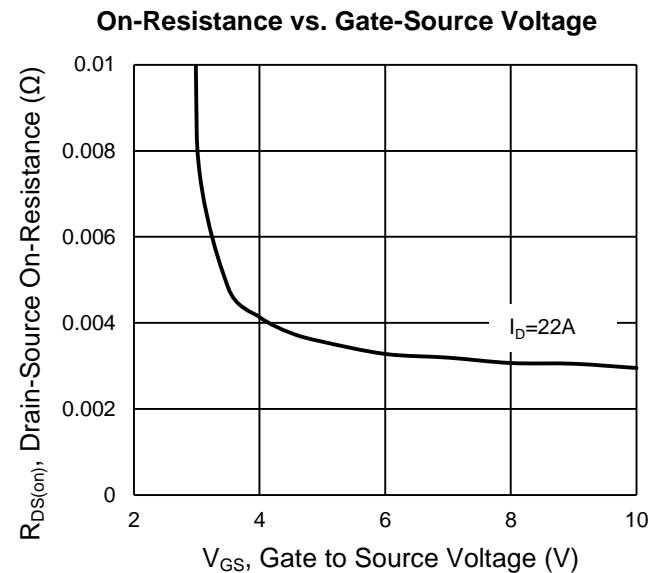
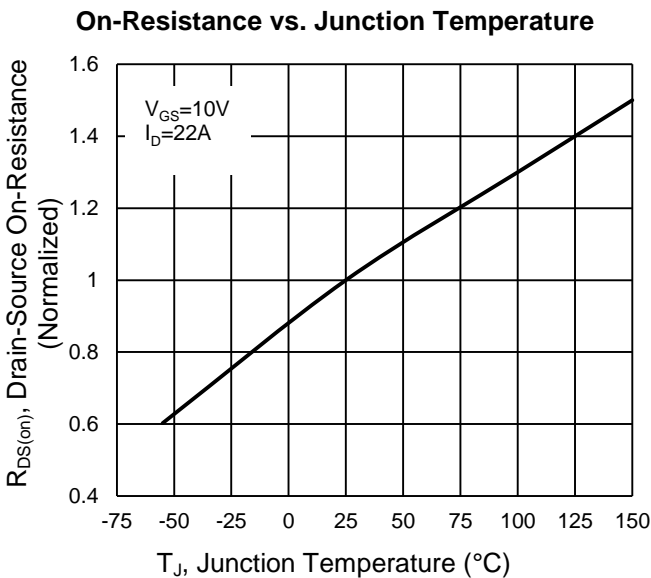
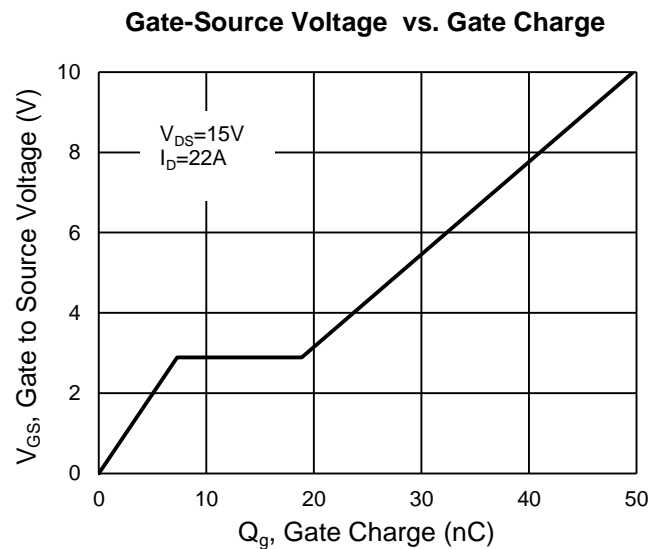
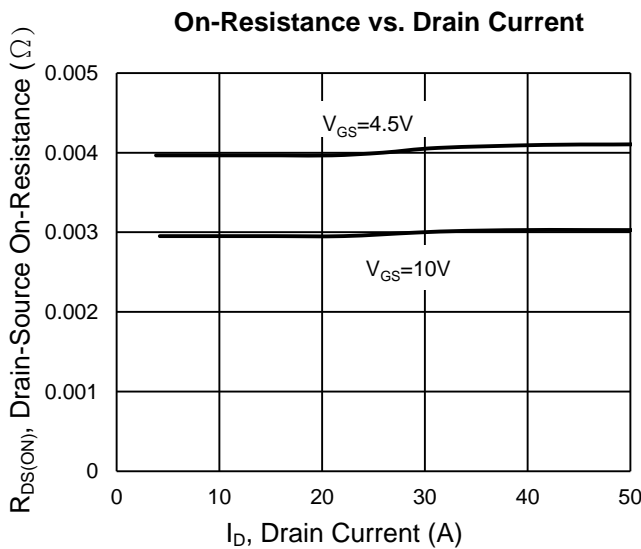
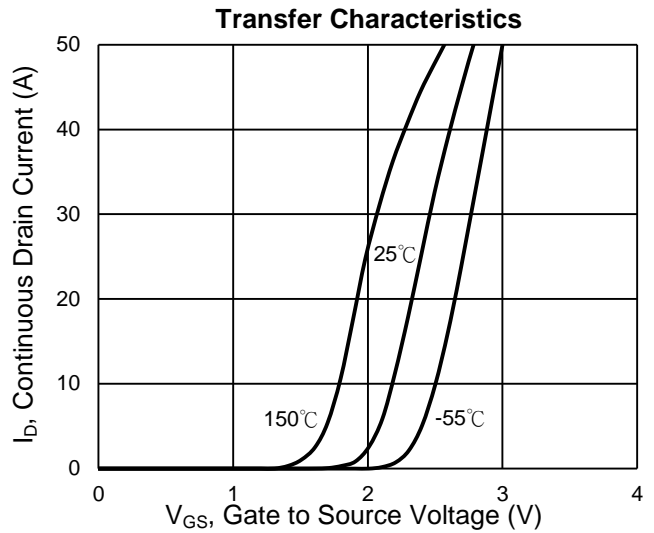
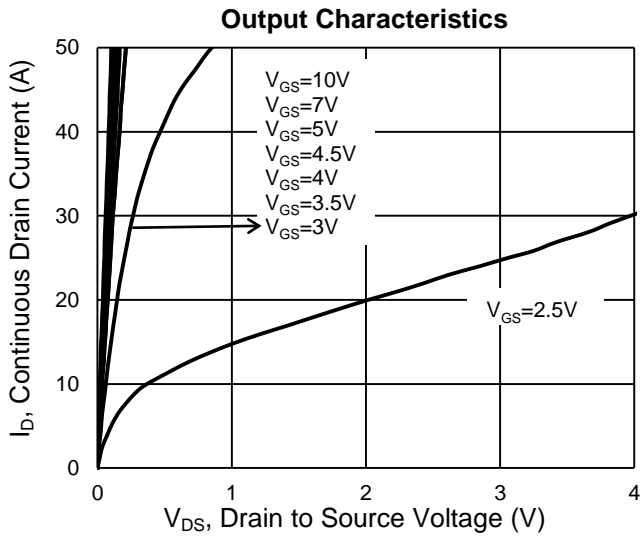
- Silicon limited current only.
- $L = 0.3\text{mH}, V_{GS} = 10\text{V}, V_{DD} = 25\text{V}, R_G = 25\Omega, I_{AS} = 27\text{A},$  Starting  $T_J = 25^\circ\text{C}$
- Pulse test: Pulse Width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- Switching time is essentially independent of operating temperature.

**ORDERING INFORMATION**

PART NO.	PACKAGE	PACKING
TSM036N03PQ56 RLG	PDFN56	2,500pcs / 13" Reel

**CHARACTERISTICS CURVES**

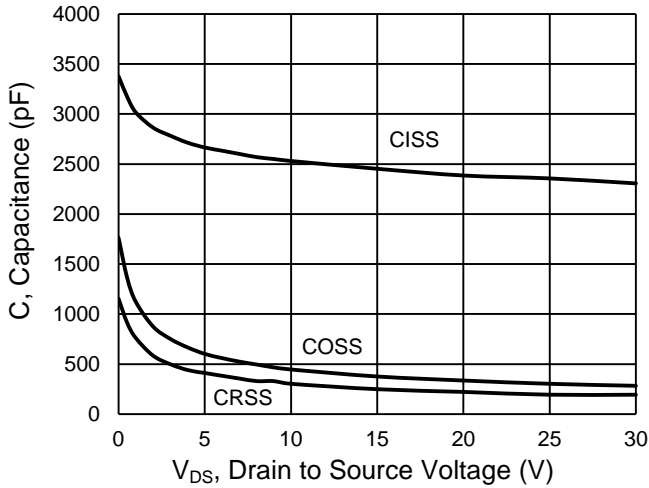
( $T_A = 25^\circ\text{C}$  unless otherwise noted)



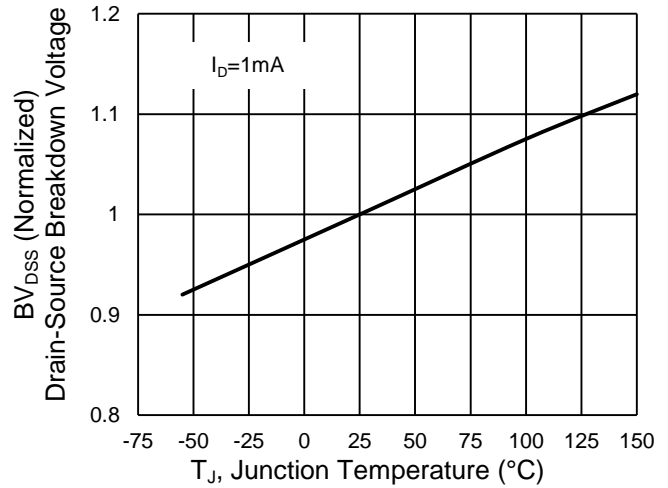
**CHARACTERISTICS CURVES**

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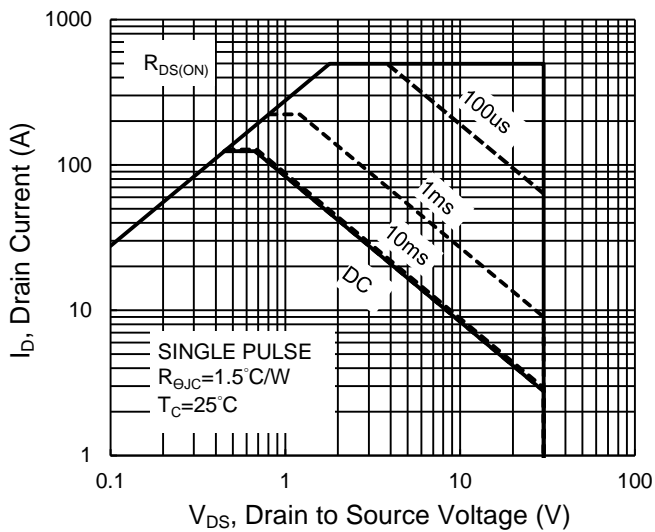
**Capacitance vs. Drain-Source Voltage**



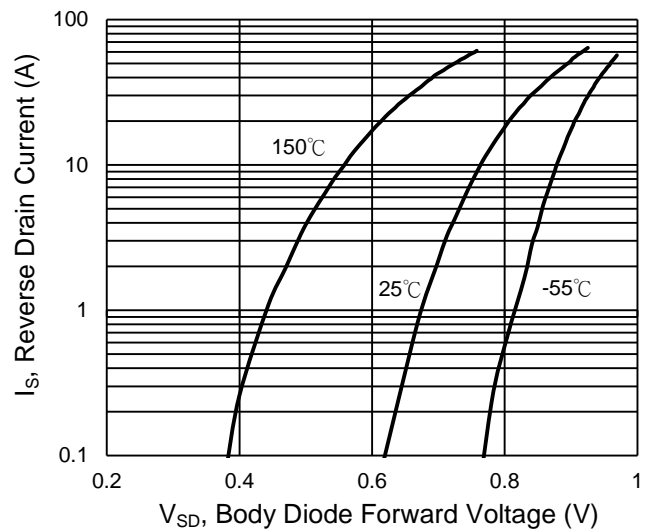
**$BV_{DSS}$  vs. Junction Temperature**



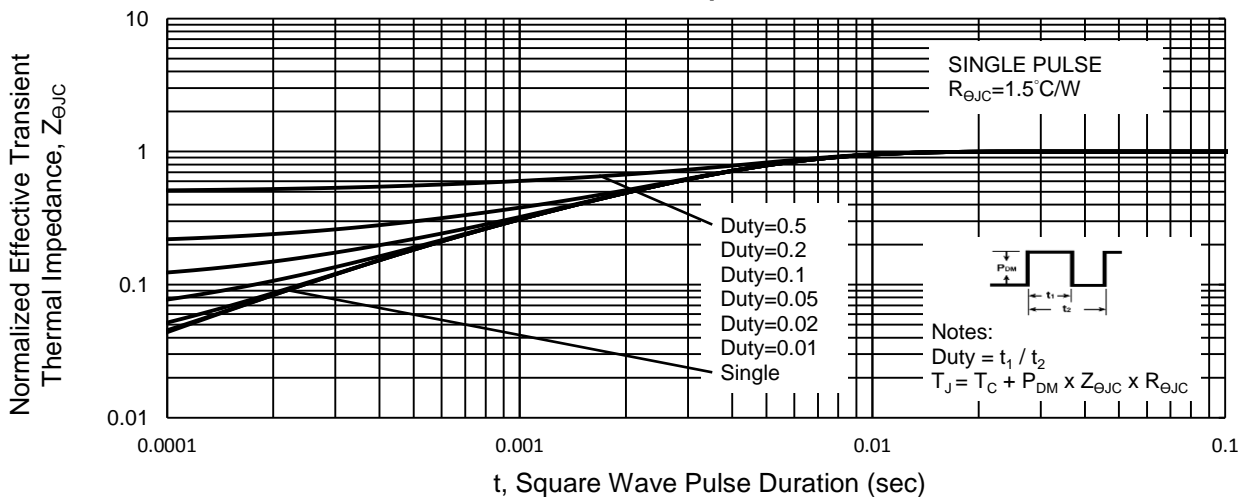
**Maximum Safe Operating Area, Junction-to-Case**



**Source-Drain Diode Forward Current vs. Voltage**

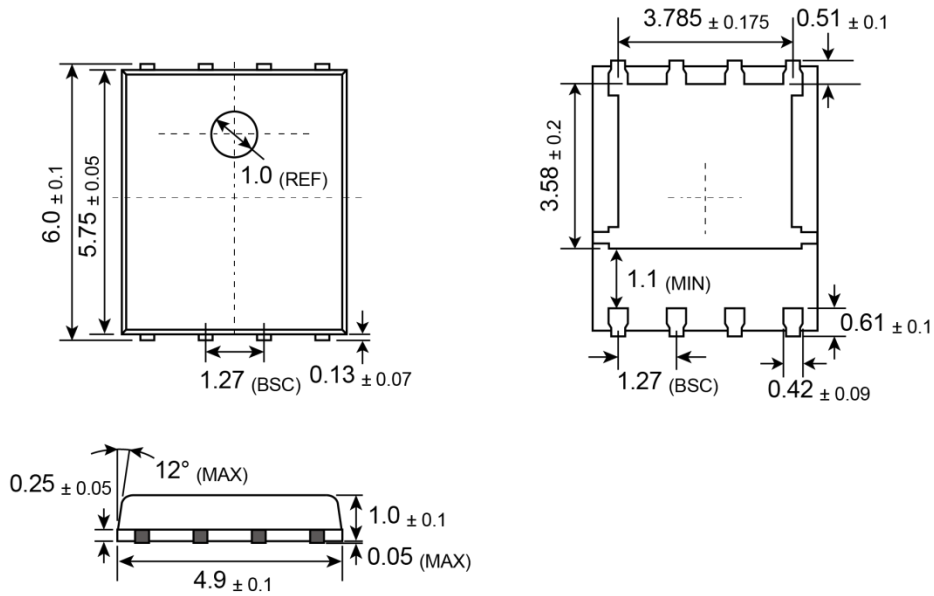


**Normalized Thermal Transient Impedance, Junction-to-Case**

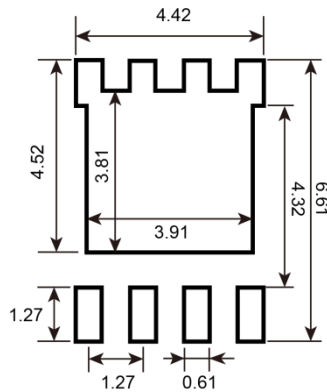


**PACKAGE OUTLINE DIMENSIONS** (Unit: Millimeters)

**PDFN56**



**SUGGESTED PAD LAYOUT** (Unit: Millimeters)



**MARKING DIAGRAM**



- G** = Halogen Free
- Y** = Year Code
- WW** = Week Code (01~52)
- F** = Factory Code

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