

THERMAL PERFORMANCE			
PARAMETER	SYMBOL	TYP	UNIT
Junction-to-lead thermal resistance	$R_{\theta JL}$	20	°C/W
Junction-to-ambient thermal resistance	$R_{\theta JA}$	75	°C/W

ELECTRICAL SPECIFICATIONS ($T_A = 25^\circ\text{C}$ unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage ⁽¹⁾	$I_F = 1\text{A}, T_J = 25^\circ\text{C}$	V_F	-	0.9	V
Reverse current @ rated V_R ⁽²⁾	$T_J = 25^\circ\text{C}$	I_R	-	1	μA
	$T_J = 100^\circ\text{C}$		-	10	μA
	$T_J = 125^\circ\text{C}$		-	50	μA
Junction capacitance	1MHz, $V_R = 4.0\text{V}$	C_J	25	-	pF
Reverse recovery time	$I_F = 0.5\text{A}, I_R = 1.0\text{A}, I_{rr} = 0.25\text{A}$	t_{rr}	-	25	ns

Notes:

1. Pulse test with $PW = 0.3\text{ms}$
2. Pulse test with $PW = 30\text{ms}$

ORDERING INFORMATION		
ORDERING CODE⁽¹⁾	PACKAGE	PACKING
ESH2xA	DO-214AC (SMA)	7,500 / Tape & Reel

Notes:

1. "x" defines voltage from 100V(ESH2BA) to 200V(ESH2DA)

CHARACTERISTICS CURVES

($T_A = 25^\circ\text{C}$ unless otherwise noted)

Fig.1 Forward Current Derating Curve

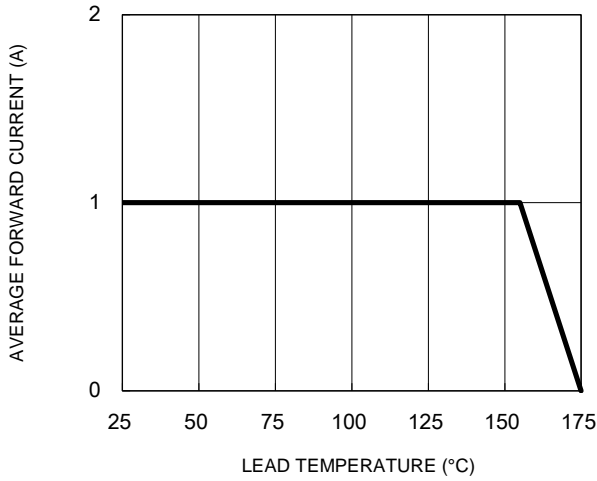


Fig.2 Typical Junction Capacitance

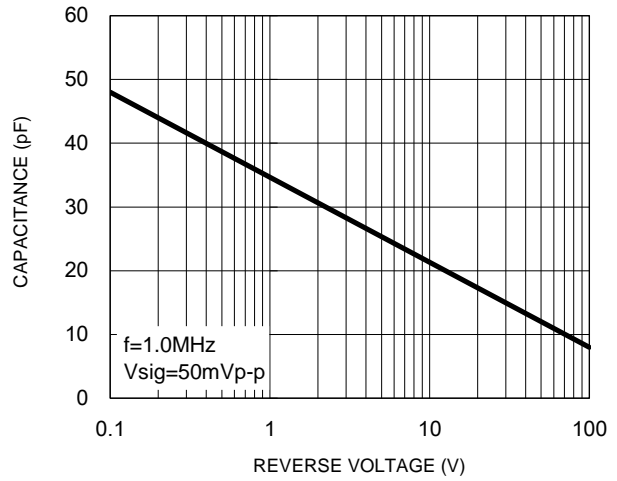


Fig.3 Typical Reverse Characteristics

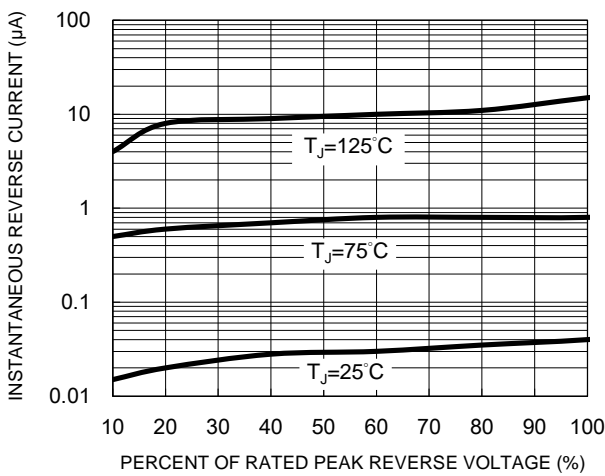


Fig.4 Typical Forward Characteristics

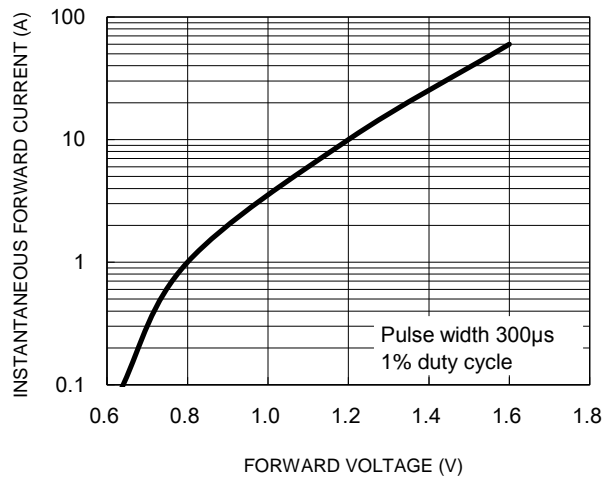
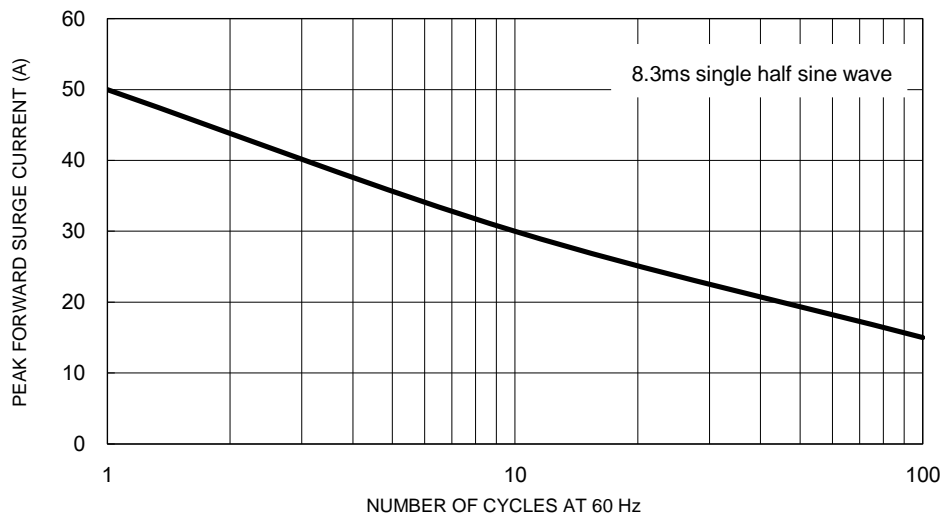


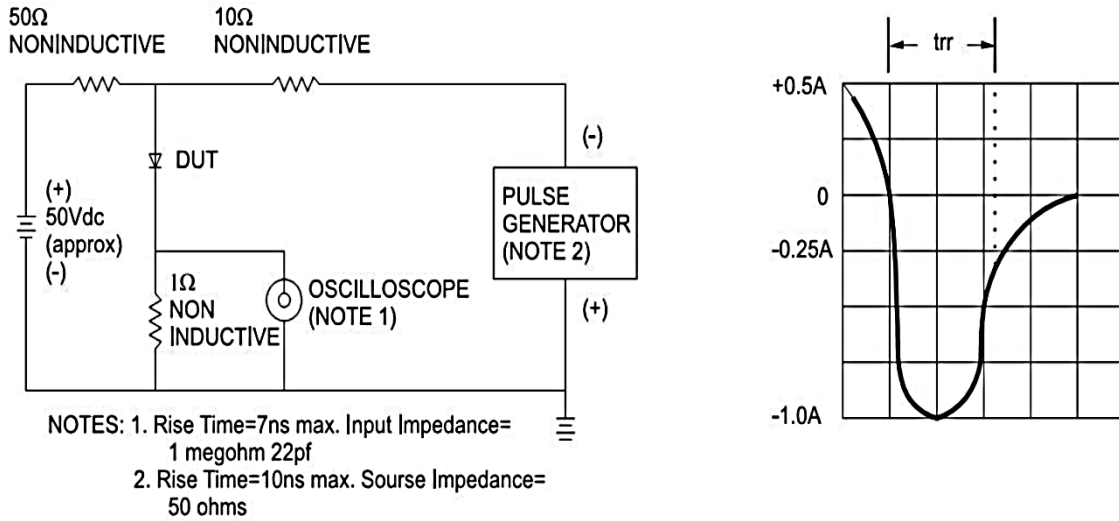
Fig.5 Maximum Non-Repetitive Forward Surge Current



CHARACTERISTICS CURVES

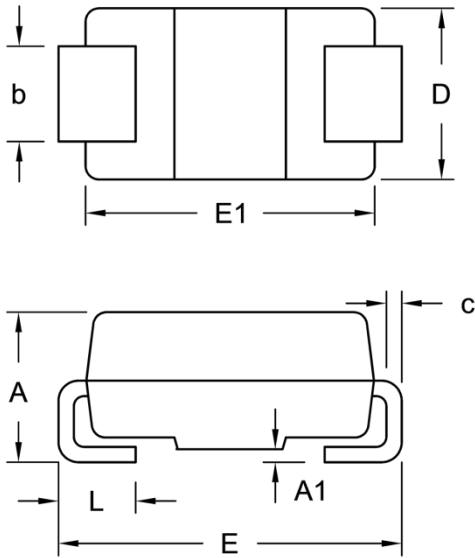
(T_A = 25°C unless otherwise noted)

Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram



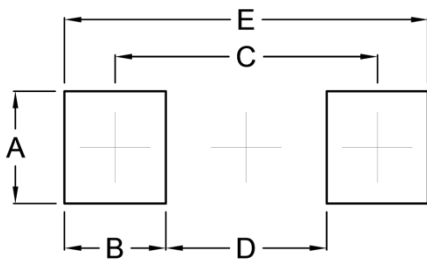
PACKAGE OUTLINE DIMENSIONS

DO-214AC (SMA)



DIM.	Unit (mm)		Unit (inch)	
	Min.	Max.	Min.	Max.
A	1.99	2.50	0.078	0.098
A1	0.10	0.20	0.004	0.008
b	1.27	1.58	0.050	0.062
c	0.15	0.31	0.006	0.012
D	2.29	2.83	0.090	0.111
E	4.95	5.33	0.195	0.210
E1	4.06	4.60	0.160	0.181
L	0.90	1.41	0.035	0.056

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	1.68	0.066
B	1.52	0.060
C	3.93	0.155
D	2.41	0.095
E	5.45	0.215

MARKING DIAGRAM



- P/N = Marking Code
- G = Green Compound
- YW = Date Code
- F = Factory Code

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