TIP29, A, B, C (NPN), TIP30, A, B, C (PNP)

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	<u>'</u>			U.
Collector–Emitter Sustaining Voltage (I _C = 30 mAdc, I _B = 0) (Note 2) TIP29G, TIP30G TIP29AG, TIP30AG TIP29BG, TIP30BG TIP29CG, TIP30CG	V _{CEO(sus)}	40 60 80 100	- - - -	Vdc
Collector Cutoff Current $(V_{CE} = 30 \text{ Vdc}, I_B = 0)$ TIP29G, TIP29AG, TIP30G, TIP30AG $(V_{CE} = 60 \text{ Vdc}, I_B = 0)$ TIP29BG, TIP29CG, TIP30BG, TIP30CG	ICEO	-	0.3 0.3	mAdc
Collector Cutoff Current $ (V_{CE} = 40 \text{ Vdc}, V_{EB} = 0) $ $ \text{TIP29G}, \text{TIP30G} $ $ (V_{CE} = 60 \text{ Vdc}, V_{EB} = 0) $ $ \text{TIP29AG}, \text{TIP30AG} $ $ (V_{CE} = 80 \text{ Vdc}, V_{EB} = 0) $ $ \text{TIP29BG}, \text{TIP30BG} $ $ (V_{CE} = 100 \text{ Vdc}, V_{EB} = 0) $ $ \text{TIP29CG}, \text{TIP30CG} $	I _{CES}	-	200 200 200 200	μAdc
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)	I _{EBO}	-	1.0	mAdc
ON CHARACTERISTICS (Note 2)	'		l	-I
DC Current Gain ($I_C = 0.2$ Adc, $V_{CE} = 4.0$ Vdc) ($I_C = 1.0$ Adc, $V_{CE} = 4.0$ Vdc)	h _{FE}	40 15	_ 75	-
Collector–Emitter Saturation Voltage (I _C = 1.0 Adc, I _B = 125 mAdc)	V _{CE(sat)}	-	0.7	Vdc
Base–Emitter On Voltage ($I_C = 1.0 \text{ Adc}$, $V_{CE} = 4.0 \text{ Vdc}$)	V _{BE(on)}	-	1.3	Vdc
DYNAMIC CHARACTERISTICS	.			•
Current-Gain - Bandwidth Product (Note 3) (I _C = 200 mAdc, V _{CE} = 10 Vdc, f _{test} = 1.0 MHz)	f⊤	3.0	-	MHz
Small–Signal Current Gain ($I_C = 0.2$ Adc, $V_{CE} = 10$ Vdc, $f = 1.0$ kHz)	h _{fe}	20	-	-

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

3. $f_T = |h_{fe}| \bullet f_{test}$

TIP29, A, B, C (NPN), TIP30, A, B, C (PNP)

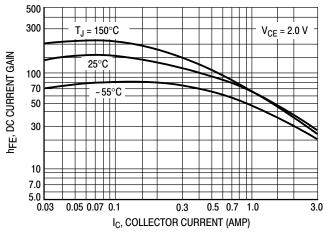


Figure 1. DC Current Gain

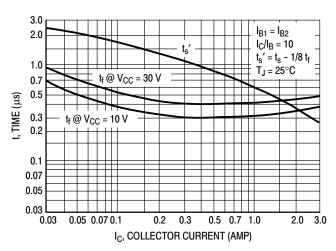


Figure 2. Turn-Off Time

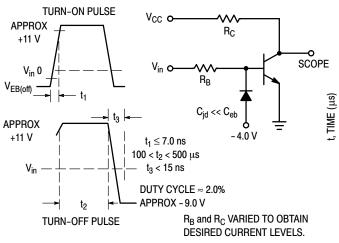


Figure 3. Switching Time Equivalent Circuit

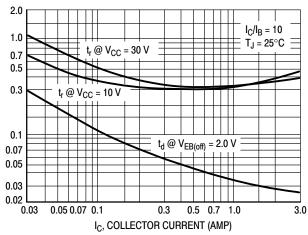


Figure 4. Turn-On Time

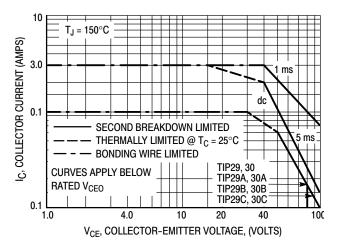


Figure 5. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate I_C – V_{CE} operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

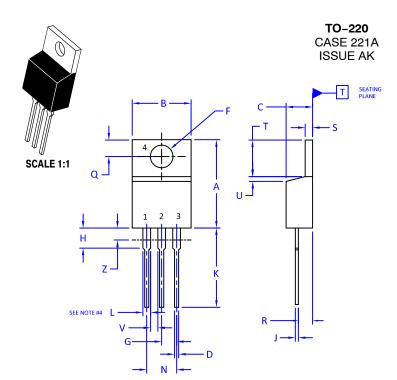
The data of Figure 5 is based on $T_{J(pk)} = 150^{\circ}C$; T_C is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \le 150^{\circ}C$. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

TIP29, A, B, C (NPN), TIP30, A, B, C (PNP)

ORDERING INFORMATION

Device	Package	Shipping
TIP29G	TO-220 (Pb-Free)	50 Units / Rail
TIP29AG	TO-220 (Pb-Free)	50 Units / Rail
TIP29BG	TO-220 (Pb-Free)	50 Units / Rail
TIP29CG	TO-220 (Pb-Free)	50 Units / Rail
TIP30G	TO-220 (Pb-Free)	50 Units / Rail
TIP30AG	TO-220 (Pb-Free)	50 Units / Rail
TIP30BG	TO-220 (Pb-Free)	50 Units / Rail
TIP30CG	TO-220 (Pb-Free)	50 Units / Rail





DATE 13 JAN 2022

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 2009.
- 2. CONTROLLING DIMENSION: INCHES
- 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

4. MAX WIDTH FOR F102 DEVICE = 1.35MM

	INCHES		MILLIMETERS	
DIM	MIN.	MAX.	MIN.	MAX.
Α	0.570	0.620	14.48	15.75
В	0.380	0.415	9.66	10.53
С	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.60	4.09
G	0.095	0.105	2.42	2.66
Н	0.110	0.161	2.80	4.10
J	0.014	0.024	0.36	0.61
К	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.41
Т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045		1.15	
Z		0.080		2.04

STYLE 1: PIN 1. 2. 3. 4.	COLLECTOR EMITTER	STYLE 2: PIN 1. 2. 3. 4.	COLLECTOR	STYLE 3: PIN 1. 2. 3. 4.	ANODE	2. 3.	MAIN TERMINAL 1 MAIN TERMINAL 2 GATE MAIN TERMINAL 2
STYLE 5: PIN 1. 2. 3. 4.	DRAIN SOURCE	STYLE 6: PIN 1. 2. 3. 4.	CATHODE ANODE	STYLE 7: PIN 1. 2. 3. 4.	ANODE	2. 3.	CATHODE ANODE EXTERNAL TRIP/DELAY ANODE
STYLE 9: PIN 1. 2. 3. 4.			GATE SOURCE DRAIN SOURCE	STYLE 11: PIN 1. 2. 3. 4.		STYLE 12: PIN 1. 2. 3. 4.	MAIN TERMINAL 1 MAIN TERMINAL 2 GATE NOT CONNECTED

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 $\frac{\text{TIP30ATU}}{\text{TIP30G}} \ \frac{\text{TIP29AG}}{\text{TIP30G}} \ \frac{\text{TIP29BG}}{\text{TIP29BG}} \ \frac{\text{TIP29CG}}{\text{TIP29CG}} \ \frac{\text{TIP30}}{\text{TIP30}} \ \frac{\text{TIP30AG}}{\text{TIP30AG}} \ \frac{\text{TIP30BG}}{\text{TIP30BG}} \ \frac{\text{TIP30BG}}{\text{TIP30CG}}$