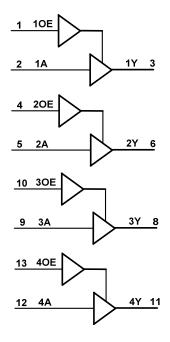


Pin Descriptions

Pin Number	Pin Name	Description
1	10E	Data Enable Input (active high)
2	1A	Data Input
3	1Y	Data Output
4	20E	Data Enable Input (active high)
5	2A	Data Input
6	2Y	Data Output
7	GND	Ground
8	3Y	Data Output
9	3A	Data Input
10	30E	Data Enable Input (active high)
11	4Y	Data Output
12	4A	Data Input
13	40E	Data Enable Input (active high)
14	Vcc	Supply Voltage

Logic Diagram



Function Table

Inp	Output	
OE	Α	Y
Н	Н	Н
Н	L	L
L	Х	Z



Absolute Maximum Ratings (Note 4) (@TA = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range (Note 5)	-0.5 to +7.0	V
I _{IK}	Input Clamp Current $V_I < -0.5V$ or $V_i > V_{CC} +0.5V$	±20	mA
lok	Output Clamp Current $V_O < -0.5V$ or $V_O > V_{CC} +0.5V$	±20	mA
Io	Continuous Output Current -0.5V < V _O V _{CC} +0.5V	+/-25	mA
Icc	Continuous Current Through V _{CC}	50	mA
I _{GND}	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
P _{TOT}	Total Power Dissipation	500	mW

Notes:

Recommended Operating Conditions (Note 6) (@TA = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	Supply Voltage		4.5	5.5	V
VI	Input Voltage		0	V _{CC}	V
Vo	Output Voltage		0	V _{CC}	V
Δt/ΔV	Input Transition Rise or Fall Rate	V _{CC} = 4.5V to 5.5V		500	ns/V
T _A	Operating Free-Air Temperature		-40	+125	°C

Note: 6. Unused inputs should be held at V_{CC} or Ground.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Cumbal	Dorometer	Test Conditions	V	T _A = -40°	$T_A = -40$ °C to +85°C		T _A = -40°C to +125°C		
Symbol	Parameter	rest Conditions	V _{CC}	Min	Max	Min	Max	Unit	
VIH	High-level Input Voltage		4.5V to 5.5V	2.0		2.0		V	
VIL	V _{IL} Low-level Input Voltage		4.5V to 5.5V		0.8		0.8	V	
V	High-level Output	I _{OH} = -20μA	4.5V	4.4		4.4		V	
V _{OH}	Voltage	I _{OH} = -4mA	4.5V	3.84		3.70		V	
Vol	Low-level Output	I _{OL} = 20μA	4.5V		0.1		0.1	V	
VOL	Voltage	I _{OL} = 4.0mA	4.5V		0.33		0.4		
loz	Z State Leakage Current	V _O = 0 to 5.5V	5.5V		± 5.0		± 10	μA	
l _l	Input Current	$V_I = GND$ to 6.0V	6.0V		± 1		± 1	μA	
I _{CC}	Supply Current	$V_I = GND \text{ or } V_{CC}, I_O = 0$	6.0V		20		40	μA	
ΔI _{CC}	Additional Supply One Input at Voc -2 1V		4.5V to 5.5V		675		735	μA	

^{4.} Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

^{5.} Input Voltage cannot exceed V_{CC} to the extent the Maximum clamp current is exceeded.



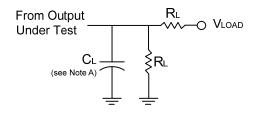
Switching Characteristics

Symbol	Parameter	Test Conditions	Vcc	•	Γ _A = +25°0	;	-40°C to +85°C	-40°C to +125°C	Unit
Symbol	Farameter	rest conditions	VCC	Min	Тур	Max	Max	Max	Oill
4	Propagation	Figure 1	4 51/		15	25	31	38	ns
t _{PD}	Delay A _N to Y _N			_	13	23	31	36	115
4	Enable Time				15	28	35	42	ns
t _{EN}	OE_N to Y_N			4.5V		13	20	35	42
	DisableTime C _L = 50pF			15	25	31	38	no	
t _{DIS}	OE_N to Y_N			_	15	25	31	36	ns
t _t	Transition Time			_	5	12	15	18	ns

Operating Characteristics (@T_A = +25°C, unless otherwise specified.)

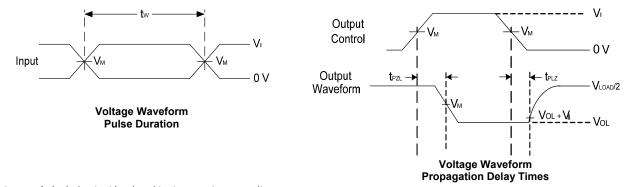
Parameter		Test Conditions	V _{CC} = 5.5V Typ	Unit
C _{pd}	Power dissipation capacitance per gate	f = 1MHz	24	pF
Cı	Input Capacitance	$V_I = V_{CC} - \text{or GND}$	3.5	pF

Parameter Measurement Information



TEST	Condition
t _{PLZ} (see Notes D and E)	Vload
t _{PZL} (see Notes D and F)	Vload

V	Inp	uts	V	V	•		V/A
V _{CC}	VI	t _r /t _f	VM	V _{LOAD}	C _L	KL	V Δ
4.5V	1.5V	≤6ns	3.0V	2 X V _{CC}	50pF	2ΚΩ	10% of V _{CC}



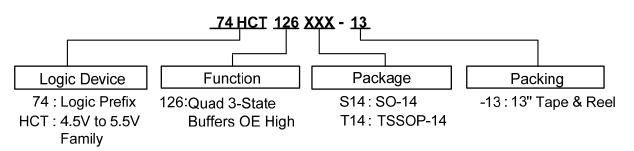
Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.
- C. The inputs are measured one at a time with one transition per measurement.
- D. For the open drain device t_{PLZ} and t_{PZL} are the same as $t_{PD.}$
- E. t_{PZL} is measured at V_{M} .
- D. $t_{PLZ}\,$ is measured at V_{OL} +V $_{\!\Delta}$
- F. A Thevenin equivalent load may be used in place of $V_{\text{CC}}\,X\,2$ and resistor divider.

Figure 1 Load Circuit and Voltage Waveforms



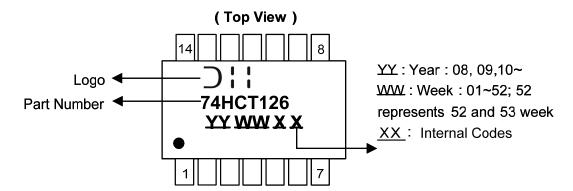
Ordering Information



	Device	Package Code	Packaging	7" Tape	and Reel
	Device	Fackage Code	rackaging	Quantity	Part Number Suffix
Pb. Lead-free Green	74HCT126S14-13	S14	SO-14	2500/Tape & Reel	-13
Pb) Lead-free Green	74HCT126T14-13	T14	TSSOP-14	2500/Tape & Reel	-13

Marking Information

(1) SO-14, TSSOP-14



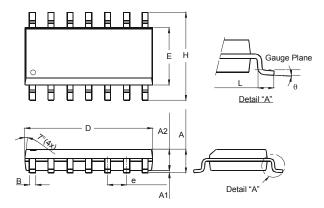
Part Number	Package
74HCT126S14	SO-14
74HCT126T14	TSSOP-14



Package Outline Dimensions (All dimensions in mm.)

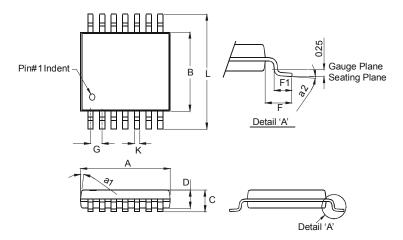
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Package Type: SO-14



	SO-14				
Dim	Min	Max			
Α	1.47	1.73			
A1	0.10	0.25			
A2	1.45	Тур			
В	0.33	0.51			
D	8.53	8.74			
Е	3.80	3.99			
е	1.27	Тур			
Н	5.80	6.20			
L	0.38	1.27			
θ	0°	8°			
All Dimensions in mm					

Package Type: TSSOP-14



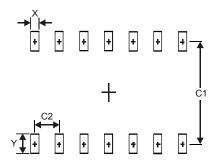
TSSOP-14		
Dim	Min	Max
a1	7° (4X)	
a2	0°	8°
Α	4.9	5.10
В	4.30	4.50
C		1.2
D	0.8	1.05
F	1.00 Typ	
F1	0.45	0.75
G	0.65 Typ	
K	0.19	0.30
L	6.40 Typ	
All Dimensions in mm		



Suggested Pad Layout

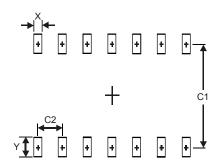
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.

Package Type: SO-14



Dimensions	Value (in mm)
X	0.60
Υ	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Y	1.45
C1	5.9
C2	0.65



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