1 Absolute maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Uni
V _{CER}	Collector-emitter voltage (R _{BE} = 100Ω)	70	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	60	V
V _{EBO}	Emitter-base voltage (I _C = 0)	5	V
I _C	Collector current	3	Α
I _{CM}	Collector peak current (t _p < ms)	5	.1
I _B	Base current	1	Α
P _{TOT}	Total dissipation at T _{case} = 25°C	47	W
T _{stg}	Storage temperature	-5E to 150	°C
TJ	Max. operating junction temperature	150	°C
	Obsoleti		
	Storage temperature Max. operating junction temperature		

2 Electrical characteristics

(T_{case} = 25°C; unless otherwise specified)

Table 3. Electrical characteristics

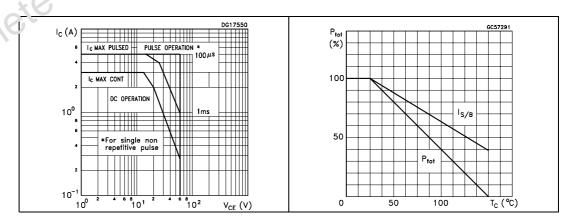
Symbol	Parameter	Test Conditions		Тур.	Max.	Unit
I _{CES}	Collector cut-off current (V _{BE} = 0)	V _{CE} = rated V _{CEO}			0.2	mA
I _{CEO}	Collector cut-off current (I _B = 0)	for BD241A $V_{CE} = 30V$ for BD241C $V_{CE} = 60V$			0.3 0.3	mA mA
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} =5V		.10		mA
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage (I _B = 0)	I _C =30mA for BD241A for BD241C	cc 100).		V V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C =3A I _B =0.6A			1.2	V
V _{BE} ⁽¹⁾	Base-emitter voltage	I _C =3A '' ₎₌ =4V			1.8	V
h _{FE} ⁽¹⁾	DC current gain	$I_{C} = 1 \text{A} V_{CE} = 4V$ $I_{C} = 3A V_{CE} = 4V$	25 10			

^{1.} Pulsed duration = 300 ms, duty cycle ≥1.5%.

2.1 Electrical characteristic (curves)

Figure ?. Safe operating area

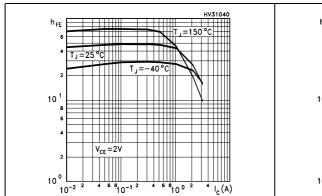
Figure 3. Derating curve



Electrical characteristics BD241A-A

Figure 4. DC current gain

Figure 5. DC current gain



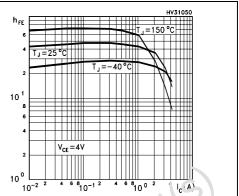
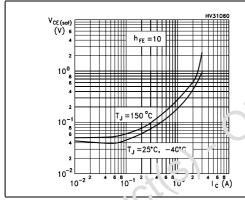


Figure 6. Collector-emitter saturation voltage

Figure 7. Base-emitter savuration voltage



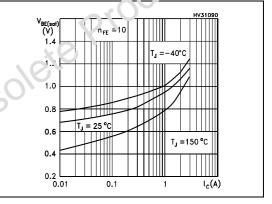
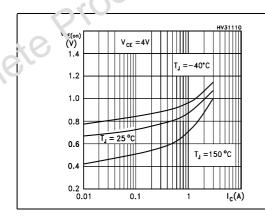
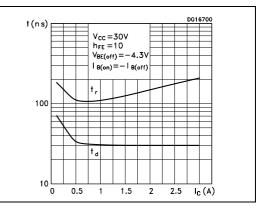


Figure 8. Base-conitter on voltage

Figure 9. Resistive load switching time





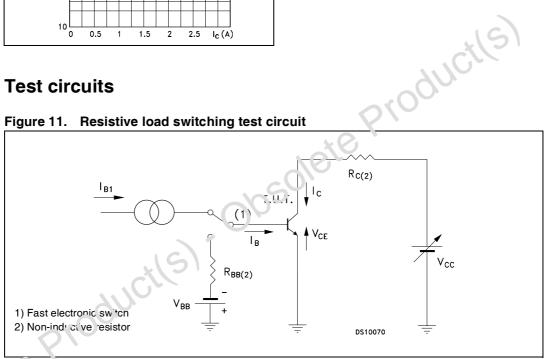
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DG16710 t (n s) V_{CC} = 30V h_{FE} = 10 $V_{BE(off)} = -4.3V$ $I_{B(on)} = -I_{B(off)}$ 1000 100 10 L I_C(A)

Figure 10. Resistive load switching time

2.2 **Test circuits**

Figure 11. Resistive load switching test circuit



3 Package mechanical data

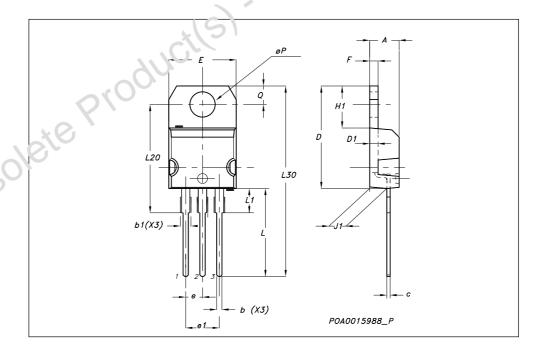
In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

Obsolete Product(s).

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TO-220 Mechanical data

DIM.	mm.				
DIWI.	MIN.	TYP	MAX.		
Α	4.40		4.60		
b	0.61		0.88		
b1	1.14		1.70		
С	0.49		0.70		
D	15.25		15.75		
D1		1.27			
E	10		10.40		
е	2.40		2.70		
e1	4.95		5 15		
F	1.23		1.3;		
H1	6.20		6.60		
J1	2.40		2.72		
L	13	- 1	14		
L1	3.50	101	3.93		
L20		16.40			
L30		28 90			
øΡ	3.75		3.85		
Q	2.65		2.95		



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Revision history BD241A-A

4 Revision history

Table 4. Revision history

Date	Revision	Changes
10-Jul-2007	1	Initial Release

Obsolete Product(s).

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