

Table 3. ESD Protection Characteristics

Test Methodology	Class
Human Body Model (per JESD22-A114)	1C (Minimum)
Machine Model (per EIA/JESD22-A115)	A (Minimum)
Charge Device Model (per JESD22-C101)	IV (Minimum)

Table 4. Electrical Characteristics ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Off Characteristics					
Zero Gate Voltage Drain Leakage Current ($V_{DS} = 65\text{ Vdc}$, $V_{GS} = 0\text{ Vdc}$)	I_{DSS}	—	—	10	μAdc
Zero Gate Voltage Drain Leakage Current ($V_{DS} = 28\text{ Vdc}$, $V_{GS} = 0\text{ Vdc}$)	I_{DSS}	—	—	1	μAdc
Gate-Source Leakage Current ($V_{GS} = 5\text{ Vdc}$, $V_{DS} = 0\text{ Vdc}$)	I_{GSS}	—	—	1	μAdc

On Characteristics

Gate Threshold Voltage ($V_{DS} = 10\text{ Vdc}$, $I_D = 248\ \mu\text{Adc}$)	$V_{GS(th)}$	1.2	2	2.7	Vdc
Gate Quiescent Voltage ($V_{DD} = 30\text{ Vdc}$, $I_D = 900\text{ mAdc}$, Measured in Functional Test)	$V_{GS(Q)}$	2	2.7	3.5	Vdc
Drain-Source On-Voltage ($V_{GS} = 10\text{ Vdc}$, $I_D = 2.3\text{ Adc}$)	$V_{DS(on)}$	0.1	0.21	0.3	Vdc

Dynamic Characteristics ⁽¹⁾

Reverse Transfer Capacitance ($V_{DS} = 28\text{ Vdc} \pm 30\text{ mV(rms)ac}$ @ 1 MHz, $V_{GS} = 0\text{ Vdc}$)	C_{rss}	—	0.77	—	pF
Output Capacitance ($V_{DS} = 28\text{ Vdc} \pm 30\text{ mV(rms)ac}$ @ 1 MHz, $V_{GS} = 0\text{ Vdc}$)	C_{oss}	—	464	—	pF
Input Capacitance ($V_{DS} = 28\text{ Vdc}$, $V_{GS} = 0\text{ Vdc} \pm 30\text{ mV(rms)ac}$ @ 1 MHz)	C_{iss}	—	214	—	pF

Functional Tests (In Freescale Test Fixture, 50 ohm system) $V_{DD} = 30\text{ Vdc}$, $I_{DQ} = 900\text{ mA}$, $P_{out} = 12\text{ W Avg.}$, $f = 3400\text{ MHz}$ and $f = 3600\text{ MHz}$, WiMAX Signal, 802.16d, 7 MHz Channel Bandwidth, 64 QAM $^{3/4}$, 4 Bursts, PAR = 9.5 dB @ 0.01% Probability on CCDF. ACPR measured in 0.5 MHz Channel Bandwidth @ $\pm 5.25\text{ MHz}$ Offset.

Power Gain	G_{ps}	12	14	17	dB
Drain Efficiency	η_D	12	14	24	%
Output Peak-to-Average Ratio @ 0.01% Probability on CCDF	PAR	7.5	8.7	—	dB
Adjacent Channel Power Ratio	ACPR	—	-49	-46	dBc
Input Return Loss	IRL	—	-12	-5	dB

1. Part internally matched both on input and output.

(continued)

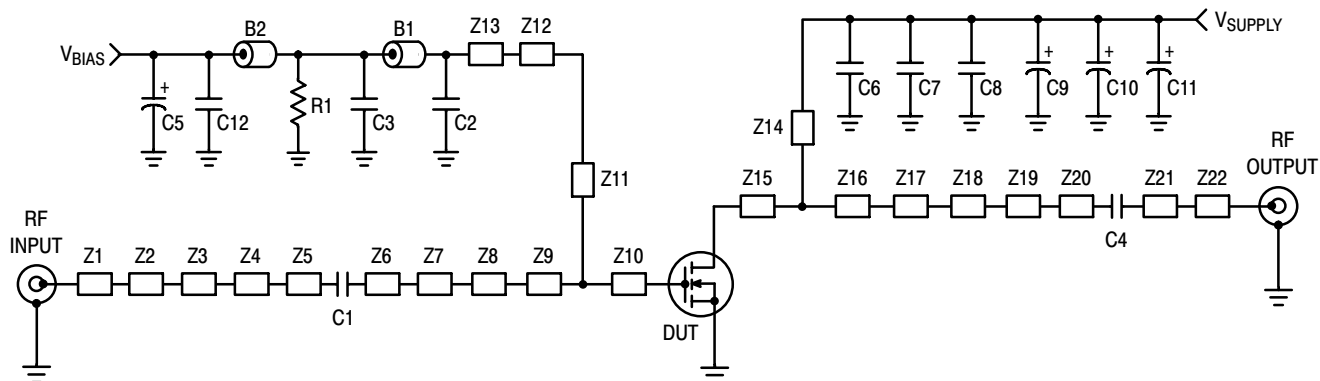
Table 4. Electrical Characteristics ($T_C = 25^\circ\text{C}$ unless otherwise noted) (continued)

Characteristic	Symbol	Min	Typ	Max	Unit
Typical Performances OFDM Signal (In Freescale Test Fixture, 50 ohm system) $V_{DD} = 30\text{ Vdc}$, $I_{DQ} = 900\text{ mA}$, $P_{out} = 12\text{ W Avg.}$, $f = 3400\text{ MHz}$ and $f = 3600\text{ MHz}$, WiMAX Signal, OFDM Single-Carrier, 7 MHz Channel Bandwidth, 64 QAM $3/4$, 4 Bursts, PAR = 9.5 dB @ 0.01% Probability on CCDF.					
Mask System Type G @ $P_{out} = 32\text{ W Avg.}$ Point B at 3.5 MHz Offset Point C at 5 MHz Offset Point D at 7.4 MHz Offset Point E at 14 MHz Offset Point F at 17.5 MHz Offset	Mask	—	-27 -38 -42 -60 -60	—	dBc
Relative Constellation Error @ $P_{out} = 12\text{ W Avg.}$ ⁽¹⁾	RCE	—	-34	—	dB
Error Vector Magnitude ⁽¹⁾ (Typical EVM Performance @ $P_{out} = 12\text{ W Avg.}$ with OFDM 802.16d Signal Call)	EVM	—	2.1	—	% rms

Typical Performances (In Freescale Test Fixture, 50 ohm system) $V_{DD} = 30\text{ Vdc}$, $I_{DQ} = 900\text{ mA}$, 3400-3600 MHz Bandwidth

Video Bandwidth @ 84 W PEP P_{out} where $IM3 = -30\text{ dBc}$ (Tone Spacing from 100 kHz to VBW) $\Delta IMD3 = IMD3 @ \text{VBW frequency} - IMD3 @ 100\text{ kHz} < 1\text{ dBc}$ (both sidebands)	VBW	—	20	—	MHz
Gain Flatness in 200 MHz Bandwidth @ $P_{out} = 12\text{ W Avg.}$	G_F	—	0.36	—	dB
Average Deviation from Linear Phase in 200 MHz Bandwidth @ $P_{out} = 75\text{ W CW}$	Φ	—	3.21	—	°
Average Group Delay @ $P_{out} = 75\text{ W CW}$, $f = 3500\text{ MHz}$	Delay	—	2.38	—	ns
Part-to-Part Insertion Phase Variation @ $P_{out} = 75\text{ W CW}$, $f = 3500\text{ MHz}$, Six Sigma Window	$\Delta\Phi$	—	63.4	—	°
Gain Variation over Temperature (-30°C to $+85^\circ\text{C}$)	ΔG	—	0.025	—	dB/°C
Output Power Variation over Temperature (-30°C to $+85^\circ\text{C}$)	ΔP_{1dB}	—	0.026	—	dBm/°C

1. $RCE = 20\text{Log}(EVM/100)$



Z1	0.427" x 0.084" Microstrip	Z13	0.358" x 0.150" Microstrip
Z2	0.066" x 0.192" x 0.084" Taper	Z14	0.541" x 0.070" Microstrip
Z3	0.045" x 0.192" Microstrip	Z15	0.911" x 0.560" Microstrip
Z4	0.044" x 0.310" Microstrip	Z16	0.379" x 0.560" Microstrip
Z5	0.150" x 0.430" Microstrip	Z17	0.300" x 0.084" Microstrip
Z6	0.107" x 0.240" Microstrip	Z18	0.200" x 0.240" Microstrip
Z7	0.155" x 0.400" Microstrip	Z19	0.047" x 0.240" x 0.140" Taper
Z8	0.943" x 0.084" Microstrip	Z20	0.463" x 0.084" Microstrip
Z9	0.158" x 0.600" Microstrip	Z21	0.089" x 0.142" Microstrip
Z10	0.110" x 0.600" Microstrip	Z22	0.657" x 0.084" Microstrip
Z11	0.802" x 0.150" Microstrip	PCB	Arlon CuClad 250GX-0300-55-22, 0.030", $\epsilon_r = 2.55$
Z12	0.150" x 0.155" Microstrip		

Figure 1. MRF7S38075HR3(HSR3) Test Circuit Schematic

Table 5. MRF7S38075HR3(HSR3) Test Circuit Component Designations and Values

Part	Description	Part Number	Manufacturer
B1, B2	Small Ferrite Beads	2743019447	Fair Rite
C1, C2, C4, C6	2.7 pF Chip Capacitors	ATC100B2R7BT500XT	ATC
C3, C7	100 pF Chip Capacitors	ATC100B101FT500XT	ATC
C5	22 μ F, 35 V Electrolytic Capacitor	EMVY350ADA221MHA0G	Nippon Chemi-Con
C9	100 μ F, 50 V Electrolytic Capacitor	MCHT101M1HB-1017-RF	Multicomp
C10, C11	470 μ F, 63 V Electrolytic Capacitors	EKME630ELL471MK25S	Multicomp
C12, C8	0.01 μ F, 50 V Chip Capacitors	C1825C103J5RAC	Kemet
R1	180 k Ω , 1/4 W Chip Resistor	CRCW12061803FKEA	Vishay

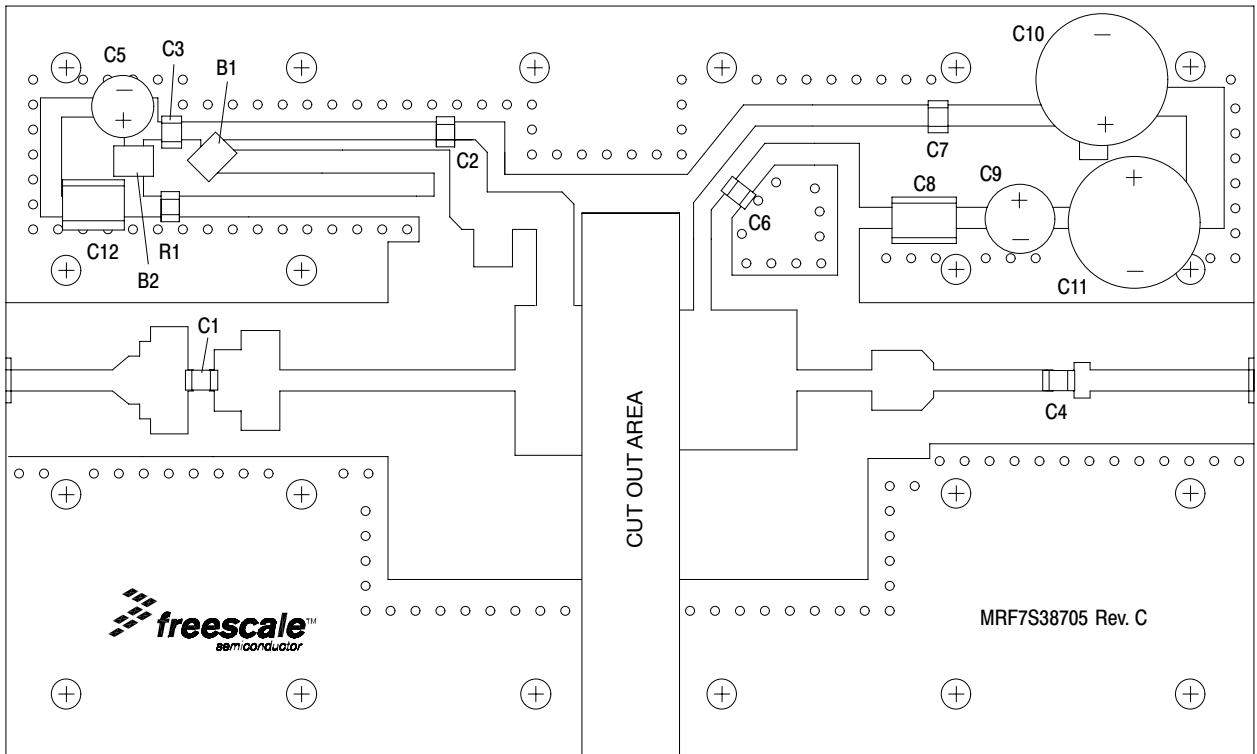


Figure 2. MRF7S38075HR3(HSR3) Test Circuit Component Layout

TYPICAL CHARACTERISTICS

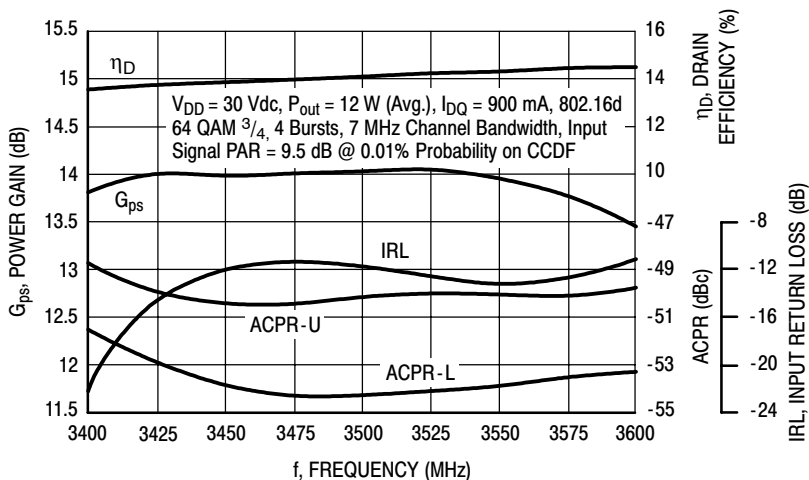


Figure 3. WiMAX Broadband Performance @ $P_{out} = 12$ Watts Avg.

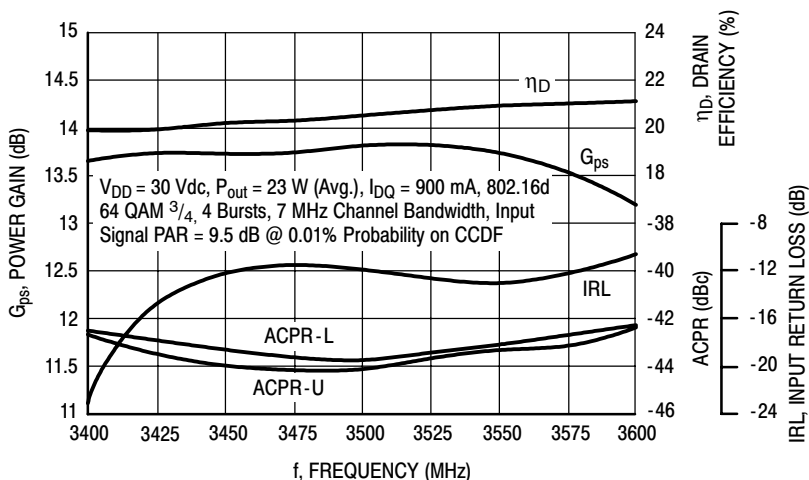


Figure 4. WiMAX Broadband Performance @ $P_{out} = 23$ Watts Avg.

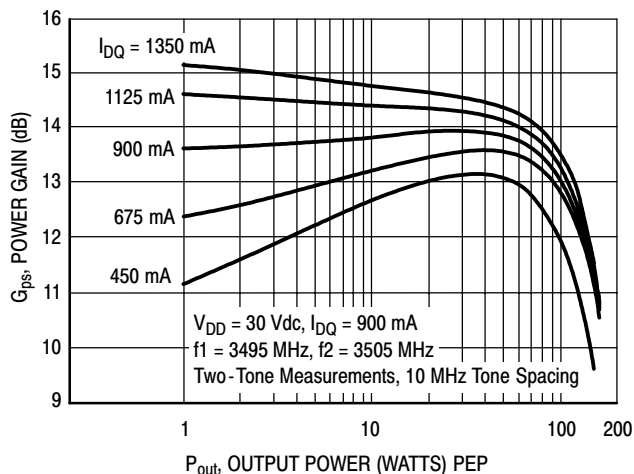


Figure 5. Two-Tone Power Gain versus Output Power

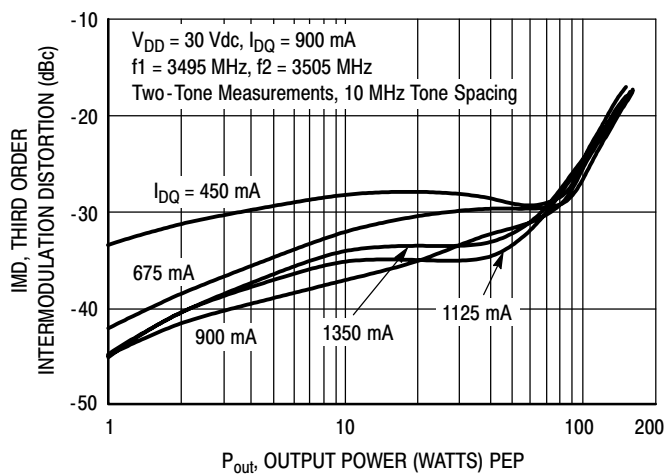


Figure 6. Third Order Intermodulation Distortion versus Output Power

TYPICAL CHARACTERISTICS

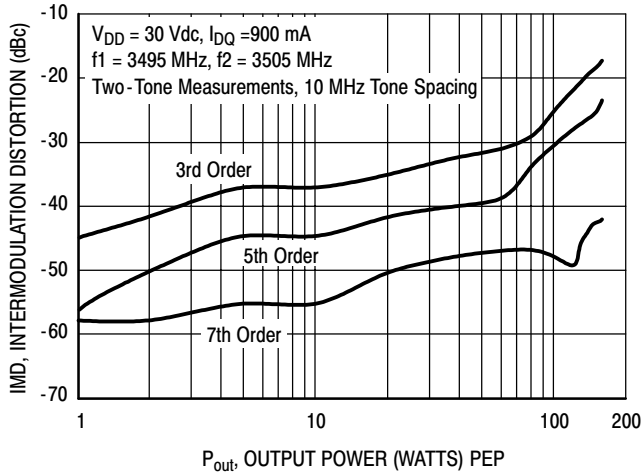


Figure 7. Intermodulation Distortion Products versus Output Power

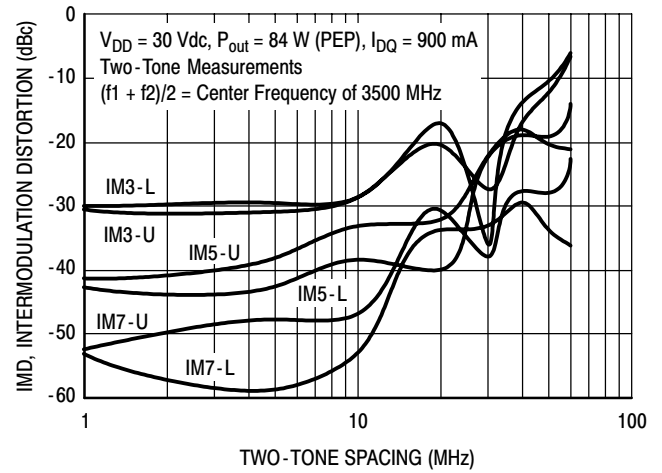


Figure 8. Intermodulation Distortion Products versus Tone Spacing

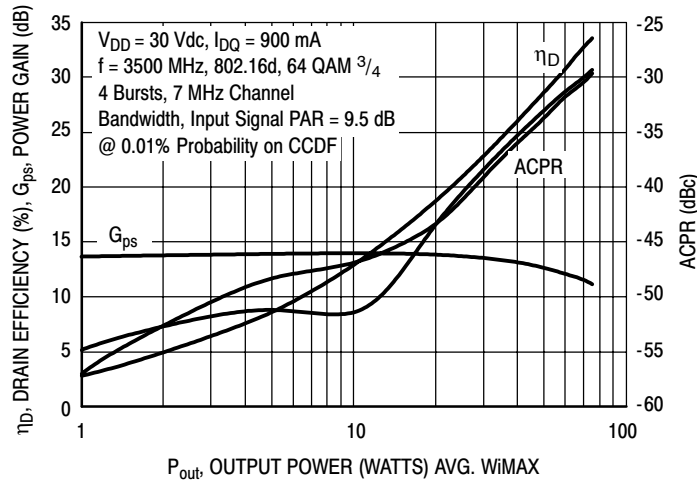


Figure 9. WiMAX, ACPR, Power Gain and Drain Efficiency versus Output Power

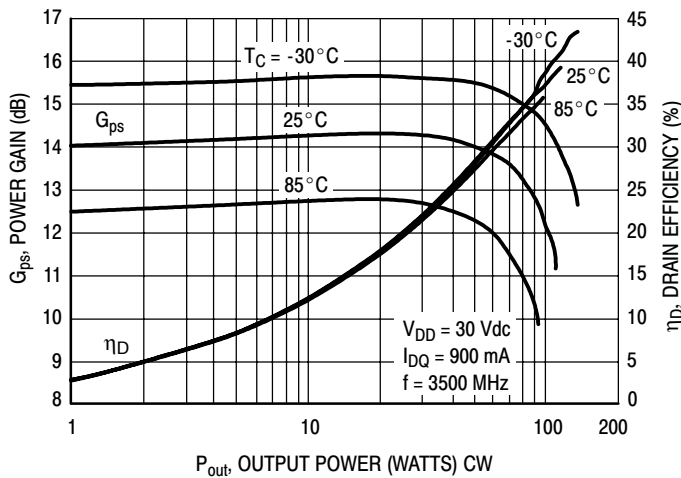


Figure 10. Power Gain and Drain Efficiency versus CW Output Power

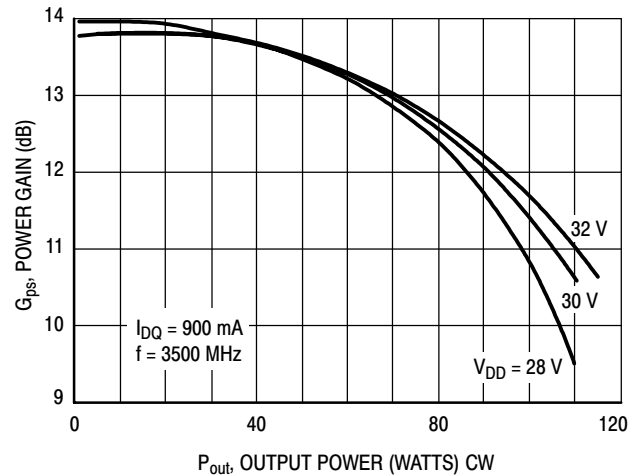
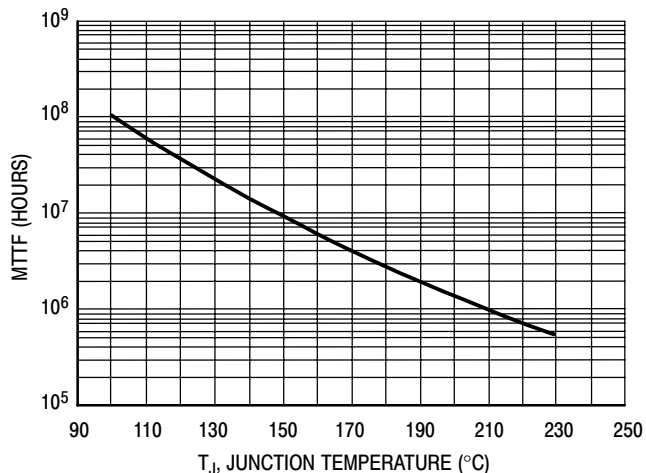


Figure 11. Power Gain versus Output Power

TYPICAL CHARACTERISTICS



This above graph displays calculated MTTF in hours when the device is operated at $V_{DD} = 30$ Vdc, $P_{out} = 12$ W Avg., and $\eta_D = 14\%$.

MTTF calculator available at <http://www.freescale.com/rf>. Select Tools/Software/Application Software/Calculators to access the MTTF calculators by product.

Figure 12. MTTF versus Junction Temperature

WiMAX TEST SIGNAL

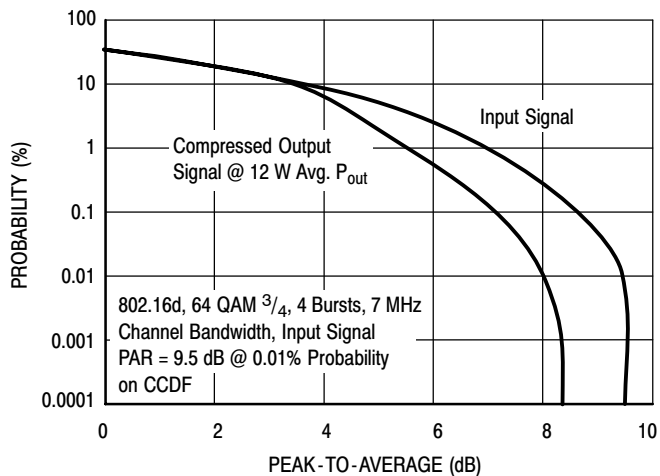


Figure 13. OFDM 802.16d Test Signal

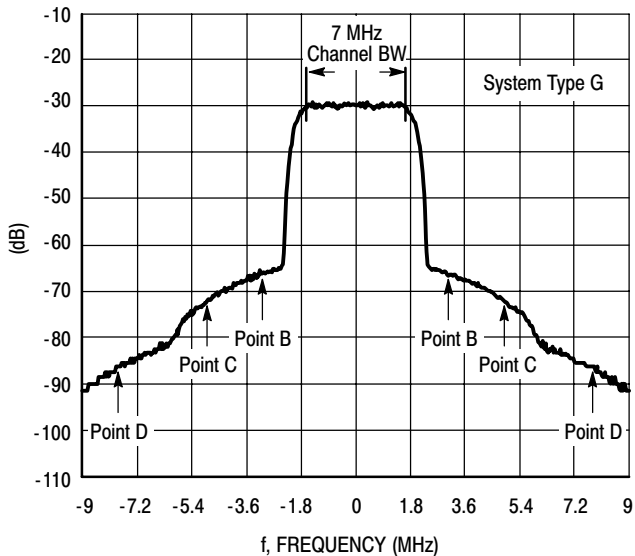
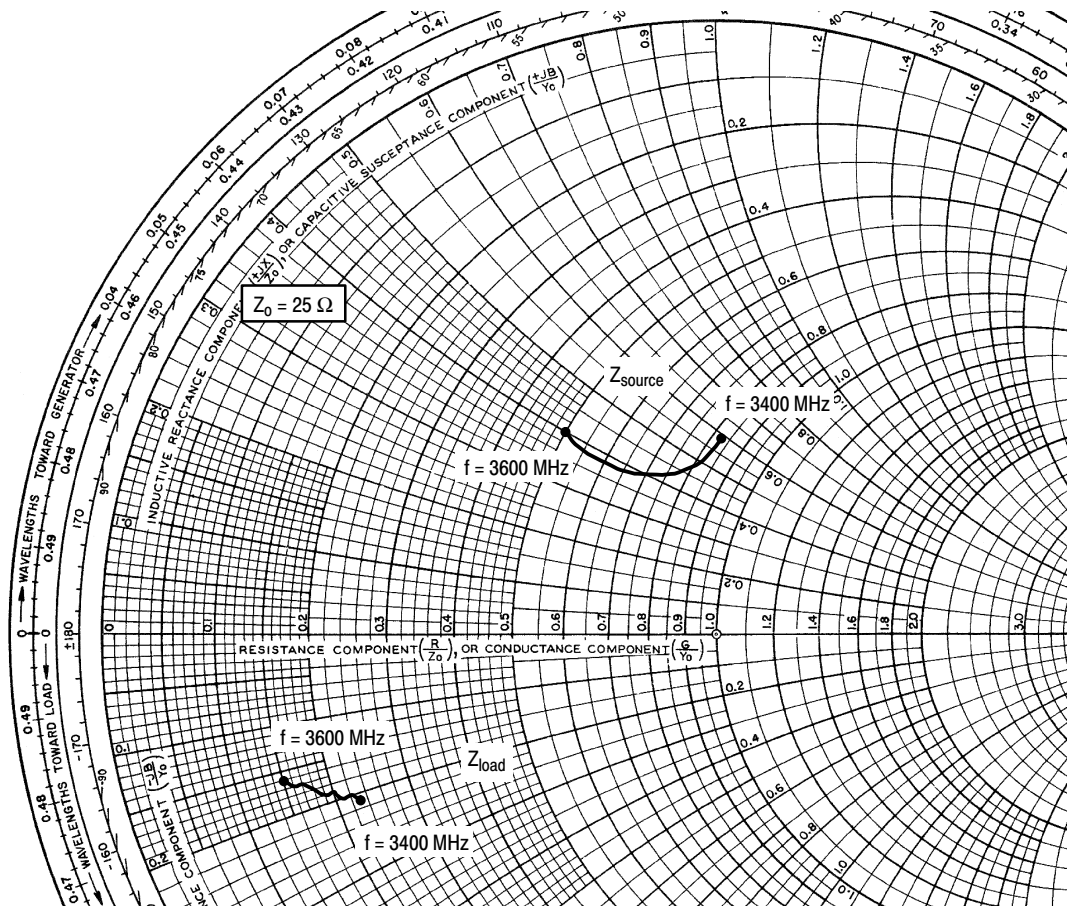


Figure 14. WiMAX Spectrum Mask Specifications



$V_{DD} = 30 \text{ Vdc}$, $I_{DQ} = 900 \text{ mA}$, $P_{out} = 12 \text{ W Avg.}$

f MHz	Z_{source} Ω	Z_{load} Ω
3400	$20.70 + j14.63$	$5.63 - j5.17$
3425	$20.22 + j12.38$	$5.44 - j5.10$
3450	$19.02 + j10.82$	$5.23 - j4.97$
3475	$17.58 + j9.95$	$4.98 - j4.83$
3500	$16.28 + j9.46$	$4.73 - j4.66$
3525	$14.97 + j9.47$	$4.50 - j4.50$
3550	$13.94 + j9.49$	$4.22 - j4.33$
3575	$13.11 + j9.66$	$3.97 - j4.13$
3600	$12.45 + j9.98$	$3.73 - j3.89$

Z_{source} = Test circuit impedance as measured from gate to ground.

Z_{load} = Test circuit impedance as measured from drain to ground.

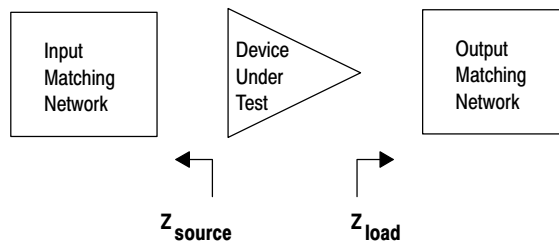
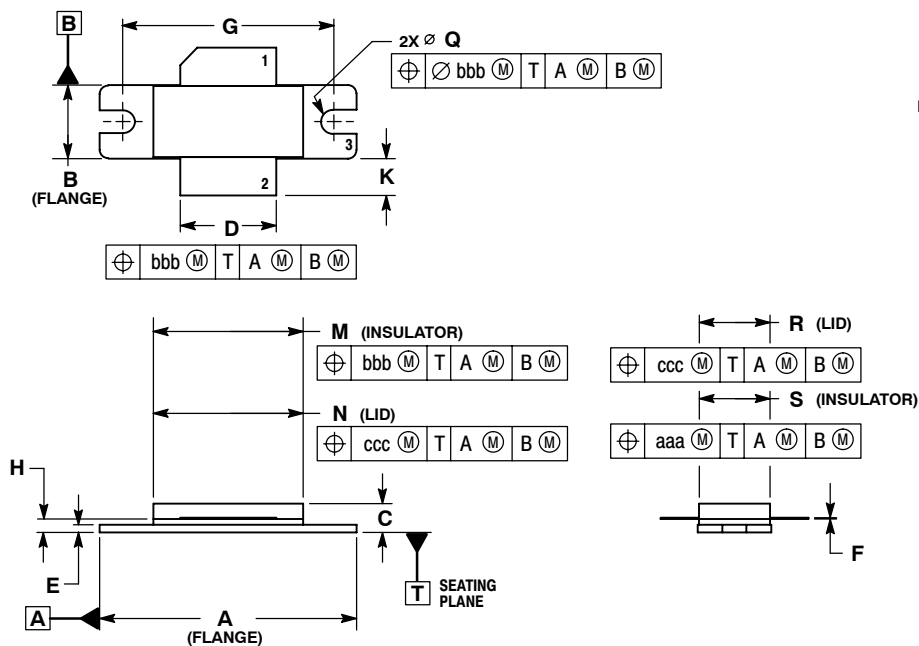


Figure 15. Series Equivalent Source and Load Impedance

PACKAGE DIMENSIONS

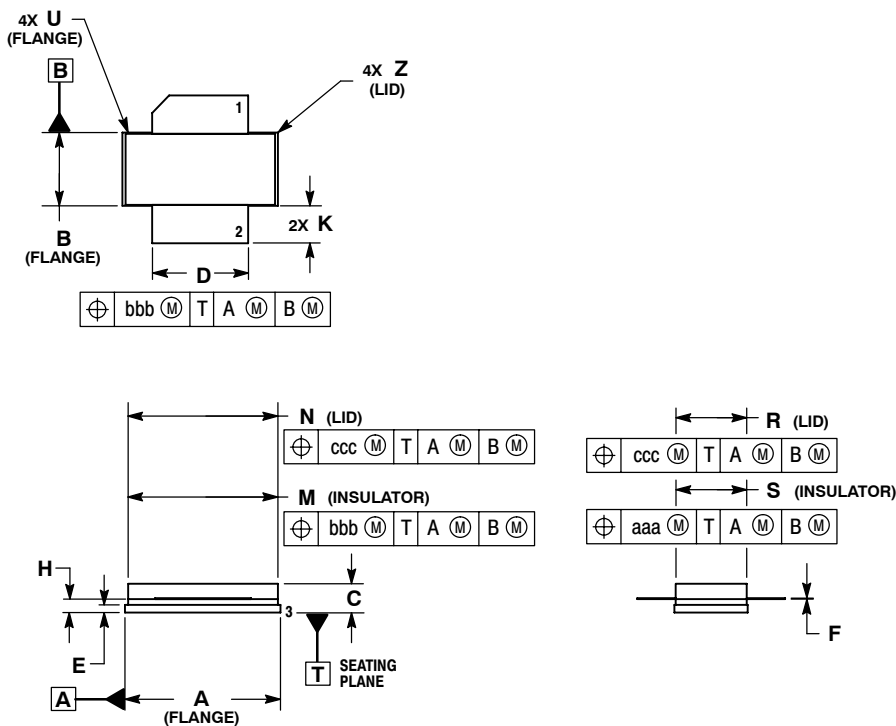


- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M-1994.
 2. CONTROLLING DIMENSION: INCH.
 3. DELETED
 4. DIMENSION H IS MEASURED 0.030 (0.762) AWAY FROM PACKAGE BODY.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	1.335	1.345	33.91	34.16
B	0.380	0.390	9.65	9.91
C	0.125	0.170	3.18	4.32
D	0.495	0.505	12.57	12.83
E	0.035	0.045	0.89	1.14
F	0.003	0.006	0.08	0.15
G	1.100 BSC		27.94 BSC	
H	0.057	0.067	1.45	1.70
K	0.170	0.210	4.32	5.33
M	0.774	0.786	19.66	19.96
N	0.772	0.788	19.60	20.00
Q	$\varnothing .118$	$\varnothing .138$	$\varnothing 3.00$	$\varnothing 3.51$
R	0.365	0.375	9.27	9.53
S	0.365	0.375	9.27	9.52
aaa	0.005 REF		0.127 REF	
bbb	0.010 REF		0.254 REF	
ccc	0.015 REF		0.381 REF	

- STYLE 1:
 PIN 1. DRAIN
 2. GATE
 3. SOURCE

**CASE 465-06
 ISSUE G
 NI-780
 MRF7S38075H**



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M-1994.
 2. CONTROLLING DIMENSION: INCH.
 3. DELETED
 4. DIMENSION H IS MEASURED 0.030 (0.762) AWAY FROM PACKAGE BODY.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.805	0.815	20.45	20.70
B	0.380	0.390	9.65	9.91
C	0.125	0.170	3.18	4.32
D	0.495	0.505	12.57	12.83
E	0.035	0.045	0.89	1.14
F	0.003	0.006	0.08	0.15
H	0.057	0.067	1.45	1.70
K	0.170	0.210	4.32	5.33
M	0.774	0.786	19.61	20.02
N	0.772	0.788	19.61	20.02
R	0.365	0.375	9.27	9.53
S	0.365	0.375	9.27	9.52
U	---	0.040	---	1.02
Z	---	0.030	---	0.76
aaa	0.005 REF		0.127 REF	
bbb	0.010 REF		0.254 REF	
ccc	0.015 REF		0.381 REF	

- STYLE 1:
 PIN 1. DRAIN
 2. GATE
 5. SOURCE

**CASE 465A-06
 ISSUE H
 NI-780S
 MRF7S38075HS**

MRF7S38075HR3 MRF7S38075HSR3

PRODUCT DOCUMENTATION

Refer to the following documents to aid your design process.

Application Notes

- AN1955: Thermal Measurement Methodology of RF Power Amplifiers

Engineering Bulletins

- EB212: Using Data Sheet Impedances for RF LDMOS Devices

REVISION HISTORY

The following table summarizes revisions to this document.

Revision	Date	Description
0	Aug. 2007	<ul style="list-style-type: none">• Initial Release of Data Sheet

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