DYNAMIC CHARACTERISTICS

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C _{iss}	Input Capacitance	V _{GS} = 0V		6965	8355	
C _{oss}	Output Capacitance	$V_{GS} = 25V$		2100	2940	pF
C _{rss}	Reverse Transfer Capacitance	f = 1 MHz		85	127	Į.
Q _g	Total Gate Charge ³	V _{GS} = 10V		250	375	
Q _{gs}	Gate-Source Charge	V _{DD} = 300V		30	45	nC
Q _{gd}	Gate-Drain ("Miller") Charge	I _D = 47A @ 25°C		105	157	
t _{d(on)}	Turn-on Delay Time			18	36	
t	Rise Time	V _{GS} = 13V V _{DD} = 380V		28	56	
t _{d(off)}	Turn-off Delay Time	I _D = 47A @ 125°C		295	442	ns
t _f	Fall Time	R _G = 5Ω		84	168	
E _{on}	Turn-on Switching Energy ⁶	INDUCTIVE SWITCHING @ 25°C $V_{DD} = 400V, V_{GS} = 15V$		810	1620	
E _{off}	Turn-off Switching Energy	$I_D = 47A, R_G = 5\Omega$		840	1680	
E _{on}	Turn-on Switching Energy ⁶	INDUCTIVE SWITCHING @ 125°C $V_{DD} = 400VV_{GS} = 15V$		1172	1758	μJ
E _{off}	Turn-off Switching Energy	$I_D = 47A, R_G = 5\Omega$		985	1970	
	-DRAIN DIODE RATINGS AND CHARA	CTERISTICS	1	1		

Symbol	Characteristic / Test Conditions	MIN	ТҮР	MAX	UNIT
۱ _s	Continuous Source Current (Body Diode)			47	Amps
I _{SM}	Pulsed Source Current ¹ (Body Diode)			141	, anpo
V _{SD}	Diode Forward Voltage 2 (V _{GS} = 0V, I _S = -47A)			1.2	Volts
t _{rr}	Reverse Recovery Time (I _S = -47A, dI _S /dt = 100A/ μ s, V _R = 350V)		580	650	ns
Q _{rr}	Reverse Recovery Charge (I _S = -47A, dI _S /dt = 100A/ μ s, V _R = 350V)		23	16.5	μC
^{dv} / _{dt}	Peak Diode Recovery ^{dv} / _{dt} ⁵			6	V/ns

THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	ТҮР	MAX	UNIT
R _{θJC}	Junction to Case			0.30	°C/W
$R_{_{\thetaJA}}$	Junction to Ambient			62	

① Repetitive Rating: Pulse width limited by maximum junction temperature

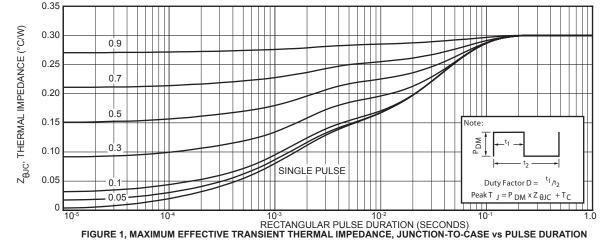
2 Pulse Test: Pulse width < 380 µs, Duty Cycle < 2%

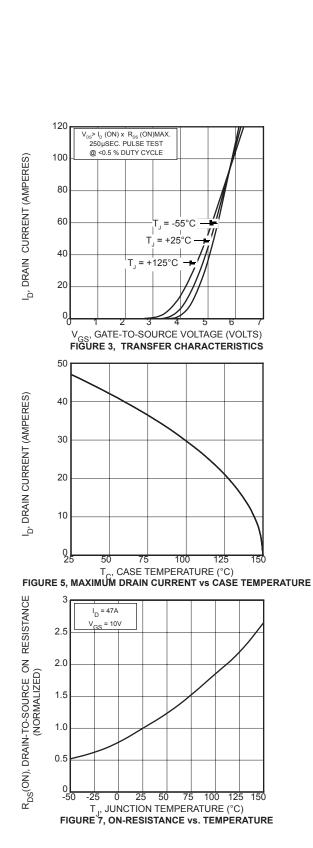
3 See MIL-STD-750 Method 3471

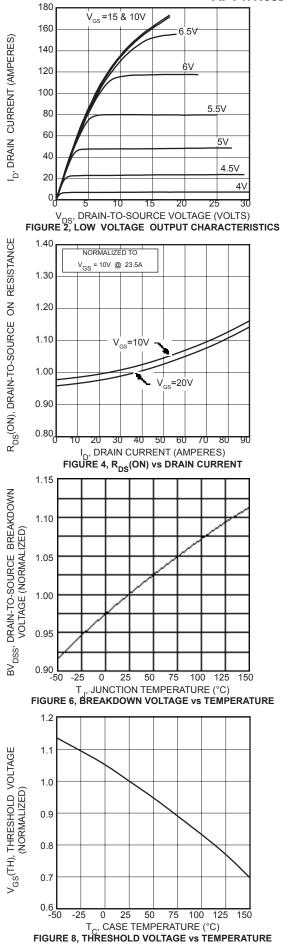
④ Starting T_j = +25°C, L = 36.0mH, R_G = 25Ω, Peak I_L = 10A
⑤ ^{dv}/_{dt} numbers reflect the limitations of the test circuit rather than the device itself. I_S = -I_D47A, ^{di}/_{dt} = 700A/µs v_R = v_{DSS}, τ_J = 150°C
⑥ Eon includes diode reverse recovery. See figures 18, 20.

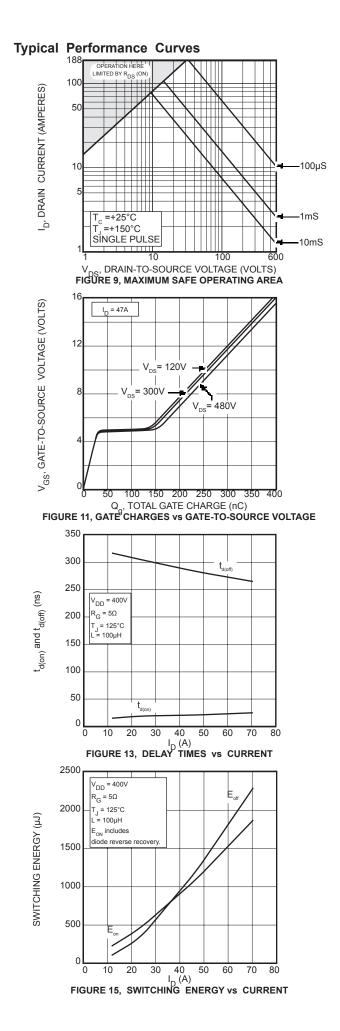
 $\ensuremath{\overline{\mathcal{O}}}$ Repetitve avalanche causes additional power losses that can be

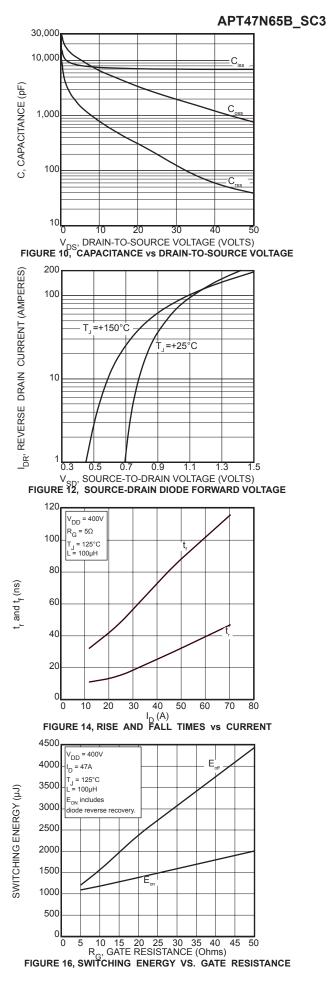
calculated as $P_{AV} = E_{AR}^* f$ Microsemi Reserves the right to change, without notice, the specifications and information contained herein.

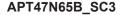












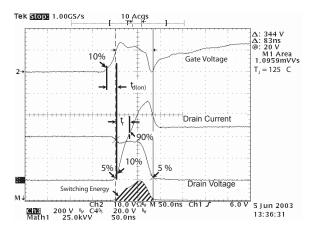


Figure 18, Turn-on Switching Waveforms and Definitions

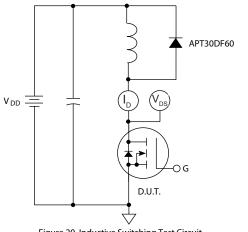


Figure 20, Inductive Switching Test Circuit

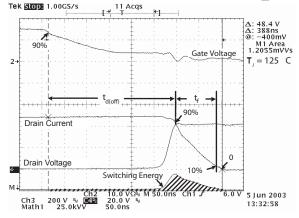
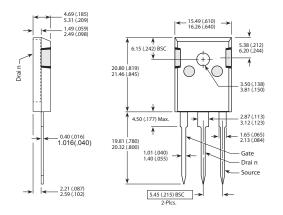
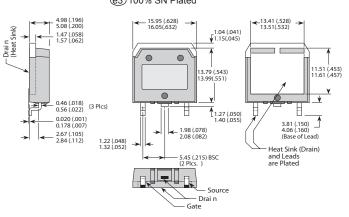


Figure 19, Turn-off Switching Waveforms and Definitions





D³PAK (S) Package Outline (e3) 100% SN Plated



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