1 Characteristics

Symbol	Par	Value	Unit			
V _{RRM}	Repetitive peak reverse voltage	9		40	V	
١ _F	Continuous forward current			350	mA	
	Surge non repetitive forward t _p = 10 ms current sinusoida	$t_{p} = 10 \text{ ms}$	SOD-123, SOD-323	2	A	
IFSM		sinusoidal	DO-35	7.5		
T _{stg}	Storage temperature range			-65 to +150	°C	
т.	Maximum operating junction temperature		SOD-123, SOD-323	-40 to +150	°C	
'j	range	DO-35	-40 to +125			
TL	Maximum temperature for soldering during 10 s		SOD-123, SOD-323	260		
			DO-35 at 4 mm from case	230	°C	

Table 2. Absolute ratings (limiting values at $T_i = 25$ °C, unless otherwise specified)

Table 3.Thermal parameters

Symbol	Parameter	Value	Unit		
R _{th(j-a)}	lunction to ambient ⁽¹⁾	SOD-123	500	°C/M	
		SOD-323	550	0/11	
R _{th(j-l)}	Junction to lead ⁽²⁾	DO-35	300	°C/W	

1. Epoxy printed circuit board with recommended pad layout

2. On infinite heatsink with 4 mm lead length



Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit	
V _{BR}	Breakdown reverse voltage	T _j = 25 °C	Ι _r = 25 μΑ	40			V	
	Reverse leakage current	T _j = 25 °C	V _R = 1.5 V			1		
			V _R = 10 V			2	μΑ	
			V _R = 20 V			5		
I _R ⁽¹⁾			V _R = 40 V			25		
		T _j = 60 °C	V _R = 1.5 V			10		
			V _R = 10 V			15		
			V _R = 20 V			25		
			V _R = 40 V			50		
V _F ⁽²⁾	Forward voltage drop	T _j = 25 °C	I _F = 0.1 mA			0.25	- V	
			I _F = 1 mA			0.3		
			I _F = 10 mA			0.4		
			I _F = 50 mA			0.5		
			I _F = 200 mA			0.75		
			I _F = 500 mA			0.9		

Table 4. Static electrical characteristics

1. Pulse test: t_p = 5 ms, δ < 2 %

2. Pulse test: t_p = 380 μ s, δ < 2 %

Table 5. **Dynamic characteristics**

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C Diode capacitance	V _R = 0 V, F = 1 MHz		30		2 5	
	Didde capacitance	V _R = 1 V, F = 1 MHz		18		μr

Average forward power dissipation Figure 2. Figure 1. versus average forward current

Average forward current versus ambient temperature ($\delta = 1$)





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Figure 3. Reverse leakage current versus reverse applied voltage (typical values)



Figure 5. Junction capacitance versus reverse applied voltage (typical values)



Figure 7. Relative variation of thermal impedance junction to ambient versus pulse duration (SOD-323)





Figure 6. Forward voltage drop versus forward current (typical values)



Figure 8. Thermal resistance junction to ambient versus copper surface under each lead (SOD-323)



2 Package information

- Epoxy meets UL94,V0
- Lead-free packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: <u>www.st.com</u>. ECOPACK[®] is an ST trademark.

Table 6. SOD-123 dimensions













Figure 10. SOD-323 footprint (dimensions in mm)



Table 8. DO-35 dimensions

			Dimer	isions	
	Ref.	Millim	neters	Incl	nes
		Min.	Max.	Min.	Max.
ØD	Α	3.05	4.50	0.120	0.177
	В	1.53	2.00	0.060	0.079
	С	12.7		0.500	
	D	0.458	0.558	0.018	0.022



3 Ordering information

Table 9. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
BAT48ZFILM	Z48	SOD-123 Single	10 mg	3000	Tape and reel
BAT48JFILM	48	SOD-323 Single	5 mg	3000	Tape and reel
BAT48RL	BAT48	DO-35	15 mg	4000	Tape and reel

4 Revision history

Table 10. Document revision history

Date	Revision	Changes
08-Aug-2006	1	Initial release.
07-Jul-2011	2	Updated package information for SOD-123. Added DO-35 package.



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