

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	40	V
Gate-Source Voltage			V _{GS}	±20	V
		(Note 6)		7	
Continuous Drain Current	$V_{GS} = 10V$	$T_{A} = +70^{\circ}C$ (Note 6)	ID	5.6	А
		(Note 5)		5	
Pulsed Drain Current	V _{GS} = 10V	(Note 7)	I _{DM}	22	А
Continuous Source Current (Body Diode) (Note 6)		(Note 6)	ls	5.4	А
Pulsed Source Current (Body Diode) (Note 7)		I _{SM}	22	А	

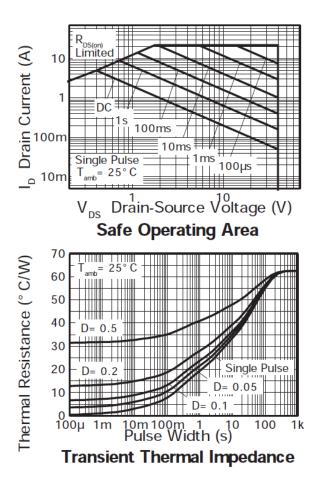
Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

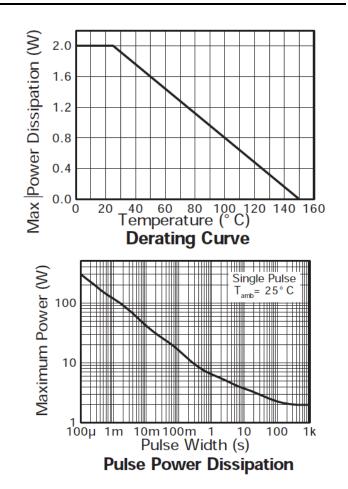
Characteristic	Symbol	Value	Unit		
Power Dissipation	(Note 5)	6	2 16	₩ mW/°C	
Linear Derating Factor	(Note 6)		3.9 31		
Thermal Resistance, Junction to Ambient	(Note 7)	Devi	62.5	•C/W	
Thermal Resistance, Junction to Ambient	(Note 6)	R _{0JA}	32.2		
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C	

Notes: 5. For a device surface mounted on 25mm x 25mm FR-4 PCB with high coverage of single sided 1oz copper, in still air conditions. 6. For a device surface mounted on FR-4 PCB measured at $t \le 5$ seconds.

7. Repetitive rating 25mm x 25mm FR-4 PCB, D = 0.05, pulse width 10µs - pulse width limited by maximum junction temperature.

Thermal Characteristics







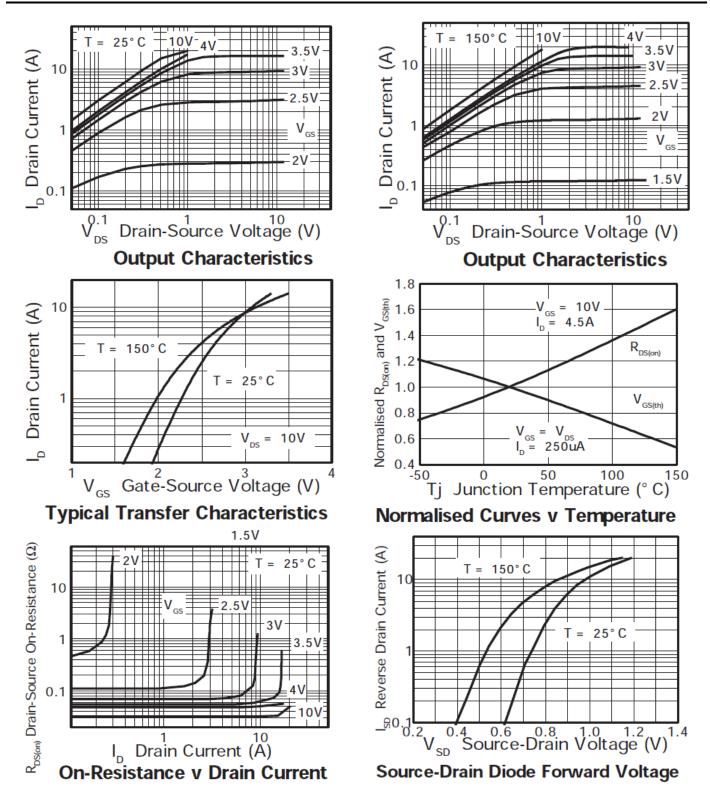
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	40			V	$I_D = 250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	$V_{DS} = 40V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V _{GS(TH)}	1		2	V	$I_D = 250 \mu A, V_{DS} = V_{GS}$	
Statia Drain Source On Registence (Note 9)	Deserve		_	0.05	Ω	$V_{GS} = 10V, I_D = 4.5A$	
Static Drain-Source On-Resistance (Note 8)	R _{DS(ON)}			0.075		$V_{GS} = 4.5V, I_D = 3.2A$	
Forward Transconductance	g fs	_	8.7		S	$V_{DS} = 15V, I_D = 2.5A$	
Diode Forward Voltage (Note 8)	V _{SD}	_	0.8	0.95	V	$I_{S} = 2.5A, V_{GS} = 0V, T_{J} = +25^{\circ}C$	
Reverse Recovery Time (Note 9)	t _{RR}	_	19.86		ns	I _F = 2.5A, di/dt = 100A/µs,	
Reverse Recovery Charge (Note 9)	Q _{RR}	_	16.36		nC	T _J = +25°C	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	_	770	_	pF	V _{DS} = 40V, V _{GS} = 0V f = 1MHz	
Output Capacitance	Coss	_	92	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	61		pF		
Total Gate Charge	Qg	_	18.2		nC	$V_{DS} = 30V, V_{GS} = 10V,$ $I_D = 2.5A$ (Refer to test circuit)	
Gate-Source Charge	Q _{gs}	_	2.1		nC		
Gate-Drain Charge	Q _{gd}		4.5		nC		
Turn-On Delay Time	t _{D(ON)}		2.55		ns	$V_{DD} = 30V, V_{GS} = 10V$ $I_D = 2.5A, R_G \cong 6\Omega$ (Refer to test circuit)	
Turn-On Rise Time	tr	_	4.45		ns		
Turn-Off Delay Time	t _{D(OFF)}	_	28.61		ns		
Turn-Off Fall Time	t _f	_	7.35		ns		

Notes:8. Short duration pulse test used to minimize self-heating effect.9. Guaranteed by design. Not subject to product testing.

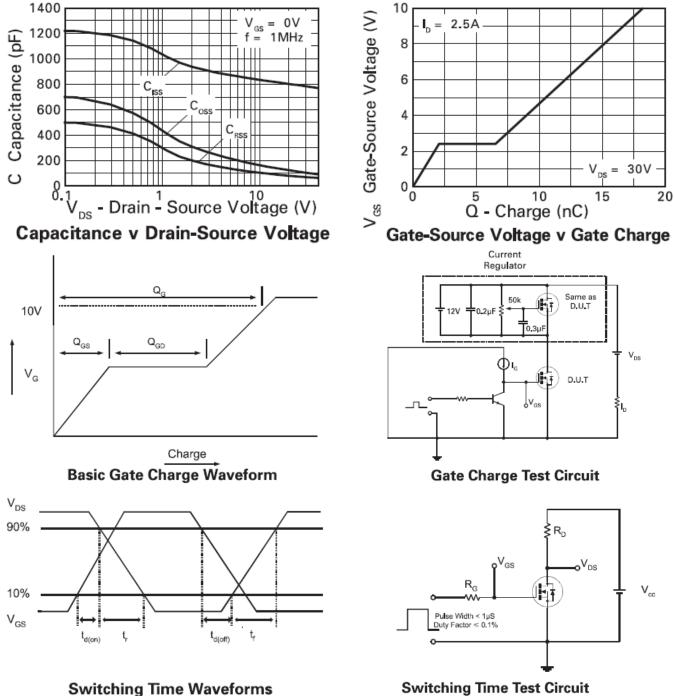


Typical Characteristics





Typical Characteristics (Cont.)



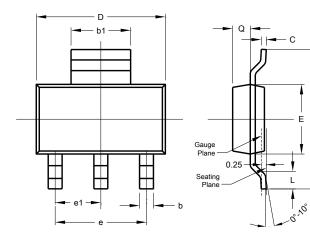
Switching Time Test Circuit

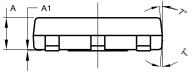


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

E1

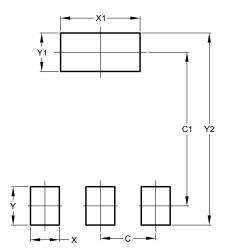




SOT223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
ш	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
e	-	-	4.60		
e1	-	-	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)		
С	2.30		
C1	6.40		
Х	1.20		
X1	3.30		
Y	1.60		
Y1	1.60		
Y2	8.00		



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