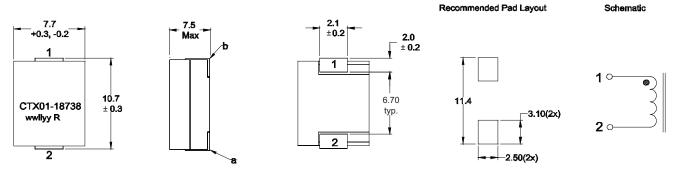
#### **Product Specifications**

Part Number <sup>6</sup>	OCL <sup>1</sup>	FLL <sup>2</sup>	I <sub>rms</sub> ³	l <sub>sat</sub> 1 <sup>4</sup>	I <sub>sat</sub> 2 <sup>5</sup>	DCR (mΩ)
	(nH) ±10%	(nH) minimum	(amps)	(amps)	(amps)	@20°C
CTX01-18738-R	210	151	50	55	45	0.29 ± 5%

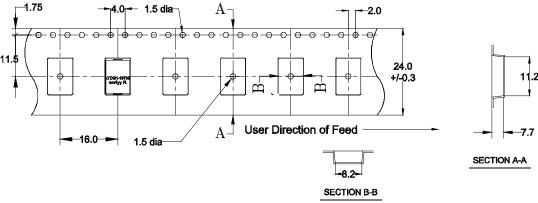
- 1.0pen Circuit Inductance (OCL) Test Parameters: 300kHz, 0.10V $_{\rm ms}$ , 0.0Adc @ 25°C.
- 2. Full Load Inductance (FLL) Test Parameters: 300kHz, 0.10V<sub>ms</sub>, I<sub>sat</sub> 1 @ 25°C.
- 3.1<sub>ma</sub>: DC current for an approximate temperature rise of 20°C without core loss. Derating is necessary for AC currents. PCB layout, trace thickness and width, air-flow and proximity of other heat generating components will affect the temperature rise. It is recommended that the temperature of the part not exceed 125°C under worst case operating conditions verified in the end application.
- 4. I<sub>sat</sub>1: Peak current for approximately 20% rolloff at +25°C.
- 5. I<sub>sat</sub>2: Peak current for approximately 20% rolloff at +125°C.
- 6. Part Number Definition: CTX01-18738-R
- CTX01-18738 = Product code and size
- "-R" suffix = RoHS compliant

#### **Dimensions (mm)**

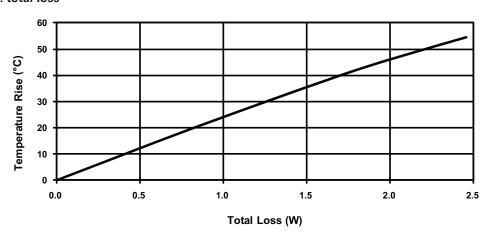


Part marking: CTX01-18738, wwllyy = Date Code, R = Revision Level All soldering surfaces must be coplanar within 0.102 millimeters. Tolerances are  $\pm 0.1$  millimeters unless stated otherwise. The DCR is measured from point "a" to point "b"

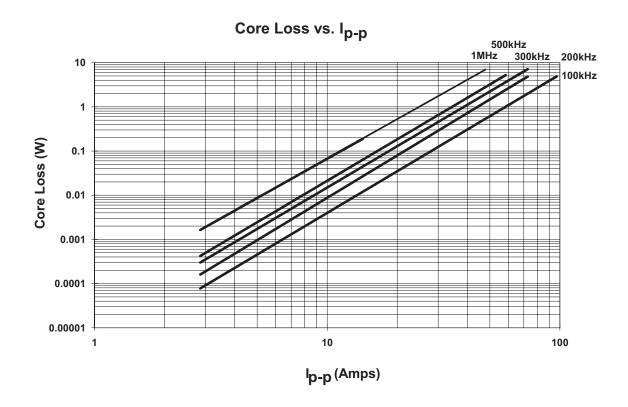
## Packaging information (mm)



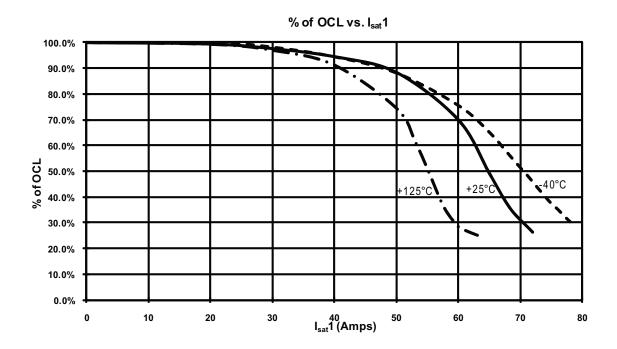
## Temperature rise vs. total loss



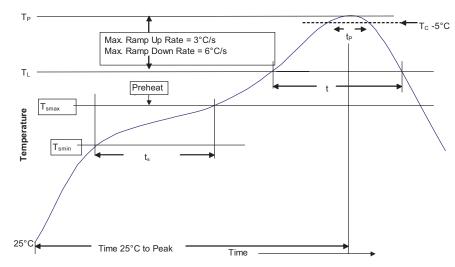
#### **Core loss**



## **Inductance characteristics**



## Solder reflow profile



-<sub>Tc</sub>-5°C Table 1 - Standard SnPb Solder (T<sub>C</sub>)

Package Thickness	Volume mm3 <350	Volume mm3 ≥350
<2.5mm)	235°C	220°C
≥2.5mm	220°C	220°C

Table 2 - Lead (Pb) Free Solder (T<sub>C</sub>)

Package Thickness	Volume mm³ <350	Volume mm³ 350 - 2000	Volume mm³ >2000
<1.6mm	260°C	260°C	260°C
1.6 – 2.5mm	260°C	250°C	245°C
>2.5mm	250°C	245°C	245°C

#### **Reference JDEC J-STD-020D**

Profile Feature	Standard SnPb Solder	Lead (Pb) Free Solder	
Preheat and Soak • Temperature min. (T <sub>smin</sub> )	100°C	150°C	
Temperature max. (T <sub>smax</sub> )	150°C	200°C	
• Time (T <sub>smin</sub> to T <sub>smax</sub> ) (t <sub>s</sub> )	60-120 Seconds	60-120 Seconds	
Average ramp up rate $T_{Smax}$ to $T_p$	3°C/ Second Max.	3°C/ Second Max.	
Liquidous temperature (TL) Time at liquidous (tL)	183°C 60-150 Seconds	217°C 60-150 Seconds	
Peak package body temperature (Tp)*	Table 1	Table 2	
$\overline{\text{Time } (t_p)^{**} \text{ within 5 °C of the specified classification temperature } (T_c)}$	20 Seconds**	30 Seconds**	
Average ramp-down rate (T <sub>p</sub> to T <sub>Smax</sub> )	6°C/ Second Max.	6°C/ Second Max.	
Time 25°C to Peak Temperature	6 Minutes Max.	8 Minutes Max.	

 $<sup>^{*}</sup>$  Tolerance for peak profile temperature (T<sub>p</sub>) is defined as a supplier minimum and a user maximum.

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<sup>\*\*</sup> Tolerance for time at peak profile temperature (t<sub>p</sub>) is defined as a supplier minimum and a user maximum.

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