

**Taiwan Semiconductor** 

ORDERING INFORMATION					
AEC-Q101	PACKING CODE	GREEN COMPOUND	PACKAGE	PACKING	
QUALIFIED		CODE			
	R3	Suffix "G"	SMA	1,800 / 7" Plastic reel	
	R2		SMA	7,500 / 13" Paper reel	
Drofix "LI"	M2		SMA	7,500 / 13" Plastic reel	
	F3		Folded SMA	1,800 / 7" Plastic reel	
	F2		Folded SMA	7,500 / 13" Paper reel	
	F4		Folded SMA	7,500 / 13" Plastic reel	
N/A	E3		Clip SMA	1,800 / 7" Plastic reel	
	E2		Clip SMA	7,500 / 13" Plastic reel	
	AEC-Q101 QUALIFIED Prefix "H" N/A	AEC-Q101 QUALIFIED PACKING CODE   Prefix "H" R3   R2 R2   M2 F3   F2 F4   N/A E3	AEC-Q101 QUALIFIEDPACKING CODE GREEN COMPOUND CODEPrefix "H"R3R2M2M2F3F2Suffix "G"F4E3	AEC-Q101 QUALIFIEDPACKING CODEGREEN COMPOUND CODEPACKAGER3R3SMAR2SMAM2SMAF3Suffix "G"F0ded SMAF0ded SMAF0ded SMAF0ded SMAF4Clip SMAN/AE3Clip SMA	

Note 1: "x" defines voltage from 50V (RS2AA) to 1000V (RS2MA)

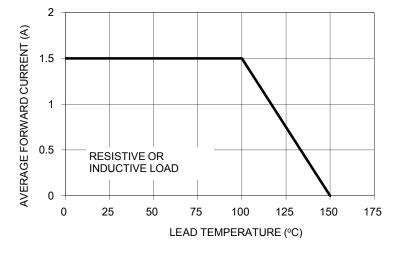
### EXAMPLE

PREFERRED P/N	PART NO.	AEC-Q101 QUALIFIED	PACKING CODE	GREEN COMPOUND CODE	DESCRIPTION	
RS2MA R3	RS2MA		R3			
RS2MA R3G	RS2MA		R3	G	Green compound	
RS2MAHR3	RS2MA	Н	R3		AEC-Q101 qualified	

#### **RATINGS AND CHARACTERISTICS CURVES**

(TA=25°C unless otherwise noted)





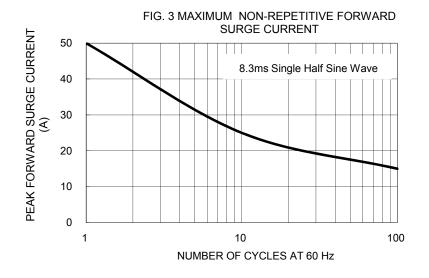
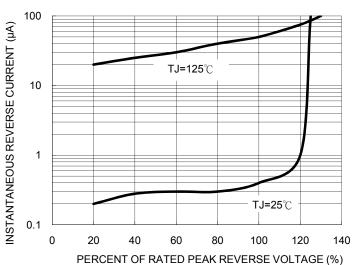
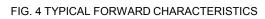
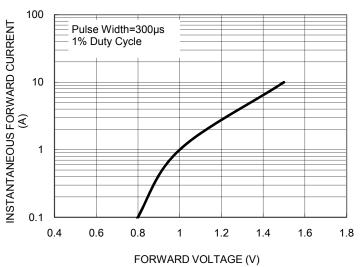


FIG. 2 TYPICAL REVERSE CHARACTERISTICS







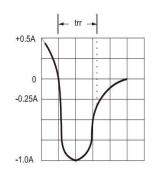


## 100 f=1.0MHz Vsig=50mVp-p CAPACITANCE (pF) 10 10 100 1 REVERSE VOLTAGE (V)

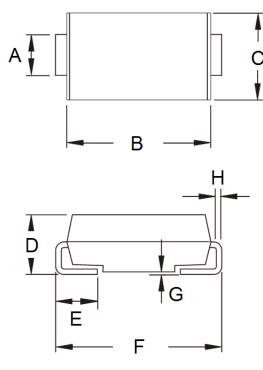
FIG. 5 TYPICAL JUNCTION CAPACITANCE

FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

#### 50Ω NONINDUCTIVE 10Ω NONINDUCTIVE 111 (-) DUT (+) 50Vdc (approx) (-) PULSE GENERATOR (NOTE 2) OSCILLOSCOPE (NOTE 1) 6 (+) NOTES: 1. Rise Time=7ns max. Input Impedance= 1 megohm 22pf 2. Rise Time=10ns max. Sourse Impedance= 50 ohms Ŧ

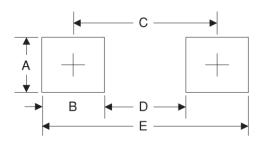


#### PACKAGE OUTLINE DIMENSIONS



DIM.	Unit	(mm)	Unit (inch)	
	Min	Max	Min	Max
Α	1.27	1.58	0.050	0.062
В	4.06	4.60	0.160	0.181
С	2.29	2.83	0.090	0.111
D	1.99	2.50	0.078	0.098
E	0.90	1.41	0.035	0.056
F	4.95	5.33	0.195	0.210
G	0.10	0.20	0.004	0.008
Н	0.15	0.31	0.006	0.012

#### SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
А	1.68	0.066
В	1.52	0.060
С	3.93	0.155
D	2.41	0.095
E	5.45	0.215

#### **MARKING DIAGRAM**



- P/N =Specific Device Code
  - Green Compound
- YW = Date Code F =

G =

Factory Code

#### Document Number: DS\_D1405078



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RS2BA RS2DA RS2GA RS2KA RS2GA R2 RS2MA R2 RS2GAHR2G RS2KA F2 RS2KA R2G RS2KAHF3G RS2JAHF2G RS2JA R2G RS2MAHF2G RS2KAHF2G RS2MAHR2G RS2MAHR3G RS2GAHR3G RS2GA R2G RS2GA R3 RS2MA R2G RS2JA R3 RS2DA R3 RS2DA R3 RS2DAHR3G RS2KA F2G RS2JA F2G RS2JAHR2G RS2JAHR3G RS2GA F2G RS2DAHF2G RS2MA F2G RS2MAHF3G RS2KA R2 RS2MA F2 RS2DAHR2G RS2JAHR3G RS2GA F2G RS2DAHF2G RS2DAHF3G RS2KA R3 RS2MA R3 RS2KA R2 RS2MA F2 RS2DAHR2G RS2JAHF3G RS2GA F2G RS2DAHF2G RS2DAHF3G RS2KA R3 RS2MA R3 RS2KAHR2G RS2GAHF3G RS2JA R2 RS2AAHF2G RS2DAHF3G RS2DAHF3G RS2MAHF3G RS2MA R3 RS2KAHR2G RS2GAHF3G RS2DAHF3G RS2DAHF3G RS2AAHF3G RS2AAHF3G RS2DA R2G RS2DA F2G RS2DA R3 RS2AA R3 RS2MA R32 RAM R32 RAM R32 RAM R32 RAM R32 RS2MA R32 RAM R32