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ESH2B, ESH2C, ESH2D

Vishay General Semiconductor

ELECTRICAL CHARACTERISTICS ($T_A = 25 \degree C$ unless otherwise noted)								
PARAMETER	TEST CONDITIONS		SYMBOL	VALUE	UNIT			
Maximum instantaneous forward voltage	I _F = 2 A		V _F ⁽¹⁾	0.93	V			
Maximum DC reverse current		T _A = 25 °C	I	2.0				
at rated DC blocking voltage	Γ	T _A = 125 °C	IR	50	μΑ			
Maximum reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		t _{rr}	25	ns			
Typical reverse recovery time	$ I_F = 2 \text{ A}, V_R = 30 \text{ V}, \\ dI/dt = 50 \text{ A}/\mu\text{s}, I_{rr} = 10 \text{ \% } I_{RM} $	T _J = 25 °C	- t _{rr}	35	ns			
		T _J = 100 °C		55				
Typical stored charge	I _F = 2 A, V _B = 30 V,	T _J = 25 °C	Q _{rr}	20	nC			
	dl/dt = 50 A/µs, I _{rr} = 10 % I _{RM}	T _J = 100 °C		35				
Typical junction capacitance	4.0 V, 1 MHz		CJ	30	pF			

Note

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	ESH2B	ESH2C	ESH2D	UNIT	
Typical thermal resistance	R _{0JA} ⁽¹⁾	65			°C/W	
	R _{θJL} ⁽¹⁾	20				

Note

 $^{(1)}\,$ Units mounted on PCB with 8.0 mm x 8.0 mm land areas

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
ESH2D-E3/52T	0.096	52T	750	7" diameter plastic tape and reel			
ESH2D-E3/5BT	0.096	5BT	3200	13" diameter plastic tape and reel			
ESH2DHE3_A/H ⁽¹⁾	0.096	Н	750	7" diameter plastic tape and reel			
ESH2DHE3_A/I (1)	0.096	I	3200	13" diameter plastic tape and reel			

Note

⁽¹⁾ AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

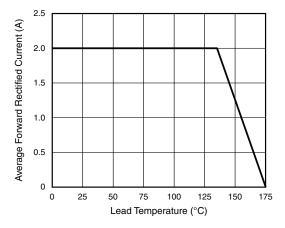


Fig. 1 - Maximum Forward Current Derating Curve

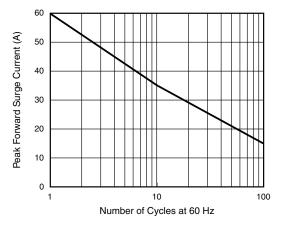


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

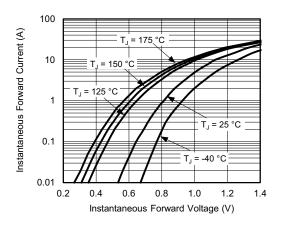


Fig. 3 - Typical Instantaneous Forward Characteristics

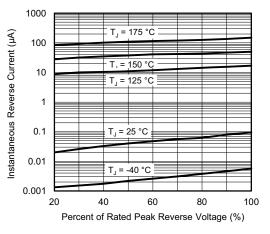


Fig. 4 - Typical Reverse Leakage Characteristics

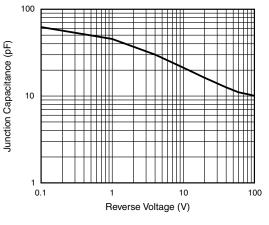


Fig. 5 - Typical Junction Capacitance

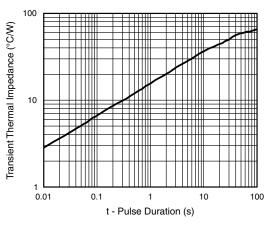


Fig. 6 - Typical Transient Thermal Impedance

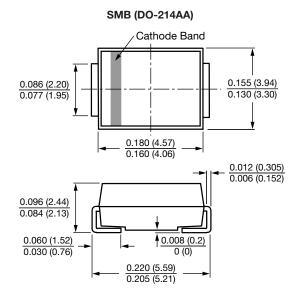
Revision: 13-May-2020 3 Document Number: 84649 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

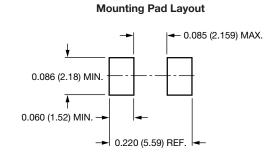


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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)







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