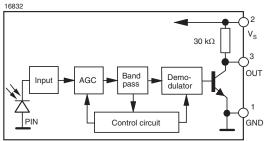


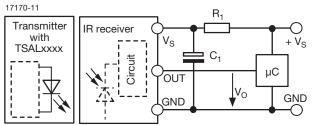
TSOP312.., TSOP314..

Vishay Semiconductors

BLOCK DIAGRAM



APPLICATION CIRCUIT



 $R_{\rm 1}$ and $C_{\rm 1}$ recommended to reduce supply ripple for $V_{\rm S}$ < 2.8 V

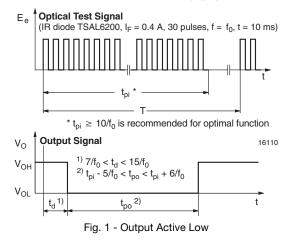
ABSOLUTE MAXIMUM RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Supply voltage (pin 2)		Vs	-0.3 to +6.0	V			
Supply current (pin 2)		I _S	3	mA			
Output voltage (pin 3)		Vo	-0.3 to (V _S + 0.3)	V			
Output current (pin 3)		Ι _Ο	5	mA			
Junction temperature		Tj	100	°C			
Storage temperature range		T _{stg}	-25 to +85	°C			
Operating temperature range		T _{amb}	-25 to +85	°C			
Power consumption	T _{amb} ≤ 85 °C	P _{tot}	10	mW			
Soldering temperature	$t \le 10$ s, 1 mm from case	T _{sd}	260	°C			

Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

ELECTRICAL AND OPTICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Supply current (pin 2)	$E_v = 0, V_S = 3.3 V$	I _{SD}	0.27	0.35	0.45	mA	
	E _v = 40 klx, sunlight	I _{SH}	-	0.45	-	mA	
Supply voltage		Vs	2.5	-	5.5	V	
Transmission distance	$E_v = 0$, test signal see Fig. 1, IR diode TSAL6200, I _F = 200 mA	d	-	45	-	m	
Output voltage low (pin 3)	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2$, test signal see Fig. 1	V _{OSL}	-	-	100	mV	
Minimum irradiance	Pulse width tolerance: t_{pi} - 5/f _o < t_{po} < t_{pi} + 6/f _o , test signal see Fig. 1	E _{e min.}	-	0.12	0.25	mW/m ²	
Maximum irradiance	t_{pi} - 5/f _o < t_{po} < t_{pi} + 6/f _o , test signal see Fig. 1	E _{e max.}	30	-	-	W/m ²	
Directivity	Angle of half transmission distance	Φ1/2	-	± 45	-	deg	

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)



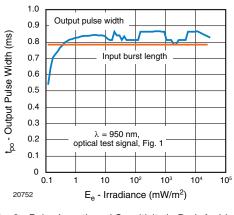


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient

Rev. 1.6, 03-Apr-18

Document Number: 82492

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



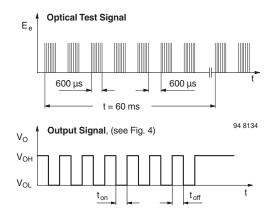


Fig. 3 - Output Function

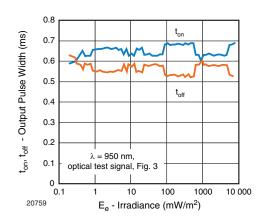


Fig. 4 - Output Pulse Diagram

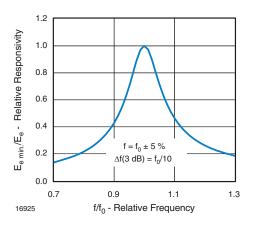


Fig. 5 - Frequency Dependence of Responsivity

Vishay Semiconductors

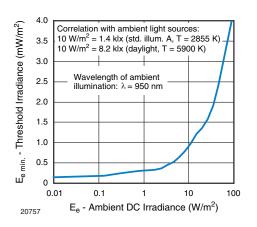


Fig. 6 - Sensitivity in Bright Ambient

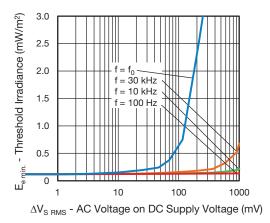


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

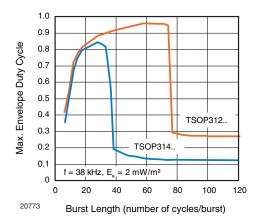


Fig. 8 - Maximum Envelope Duty Cycle vs. Burst Length

Rev. 1.6, 03-Apr-18



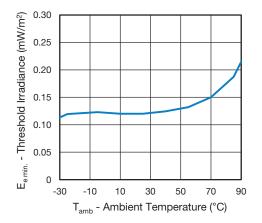


Fig. 9 - Sensitivity vs. Ambient Temperature

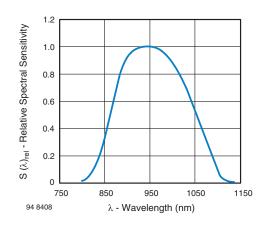


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

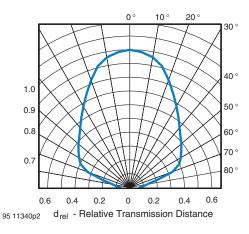
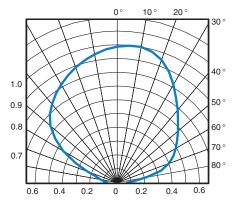


Fig. 11 - Horizontal Directivity

Vishay Semiconductors



95 11339p2 drel - Relative Transmission Distance

Fig. 12 - Vertical Directivity

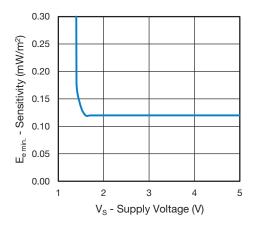


Fig. 13 - Sensitivity vs. Supply Voltage

Rev. 1.6, 03-Apr-18

4



SUITABLE DATA FORMAT

This series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the product in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output. Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- · Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see Fig. 14 or Fig. 15).

Vishay Semiconductors

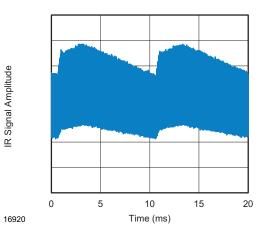


Fig. 14 - IR Disturbance from Fluorescent Lamp with Low Modulation

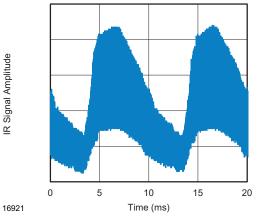


Fig. 15 - IR Disturbance from Fluorescent Lamp with High Modulation

	TSOP312	TSOP314	
Minimum burst length	10 cycles/burst	10 cycles/burst	
After each burst of length a minimum gap time is required of	10 to 70 cycles ≥ 10 cycles	10 to 35 cycles ≥ 10 cycles	
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 4 x burst length	35 cycles > 10 x burst length	
Maximum number of continuous short bursts/second	1800	1500	
NEC code	Yes	Preferred	
RC5/RC6 code	Yes	Preferred	
Thomson 56 kHz code	Yes	Preferred	
Sharp code	Yes	Preferred	
Suppression of interference from fluorescent lamps	Mild disturbance patterns are suppressed (example: signal pattern of Fig. 14)	Complex and critical disturbance patterns are suppressed (example: signal pattern of Fig. 15 or highly dimmed LCDs)	

Notes

• For data formats with short bursts please see the datasheet for TSOP311.., TSOP313..

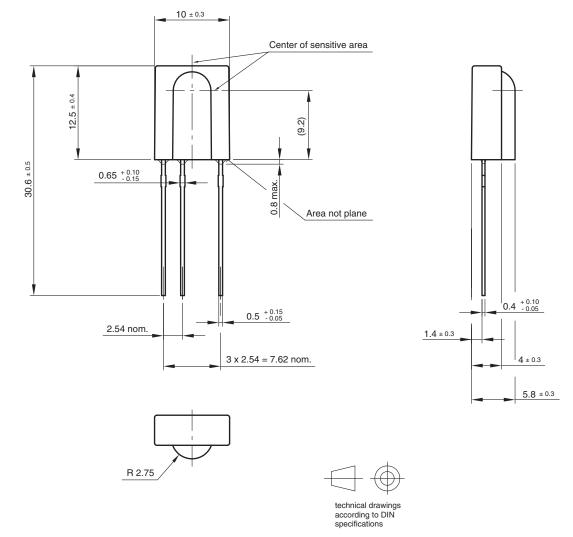
• For SIRCS 15 and 20 bit, Sony 12 bit IR codes, please see the datasheet for TSOP31S40

Rev. 1.6, 03-Apr-18



Vishay Semiconductors

PACKAGE DIMENSIONS in millimeters



Drawing-No.: 6.550-5095.01-4 Issue: 20; 15.03.10 96 12116



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Vishay:

TSOP31438 TSOP31456 TSOP31436