



SPECIFICATIONS (T _A = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 10 μA	20			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 50 μA	0.65			
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±8 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V			1	μA
		V _{DS} = 20 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 4.5 V	6			A
		V _{DS} ≥ 5 V, V _{GS} = 2.5 V	4			
Drain-Source On-Resistance ^a	r _{DS(on)}	V _{GS} = 4.5 V, I _D = 3.6 A		0.07	0.085	Ω
		V _{GS} = 2.5 V, I _D = 3.1 A		0.085	0.115	
Forward Transconductance ^a	g _{fs}	V _{DS} = 5 V, I _D = 3.6 A		10		S
Diode Forward Voltage	V _{SD}	I _S = 1.6 A, V _{GS} = 0 V		0.76	1.2	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 3.6 A		5.4	10	nC
Gate-Source Charge	Q _{gs}			0.65		
Gate-Drain Charge	Q _{gd}			1.60		
Input Capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		340		pF
Output Capacitance	C _{oss}			115		
Reverse Transfer Capacitance	C _{rss}			33		
Switching						
Turn-On Delay Time	t _{d(on)}	V _{DD} = 10 V, R _L = 5.5 Ω I _D ≅ 3.6 A, V _{GEN} = 4.5 V, R _G = 6 Ω		12	25	ns
Rise Time	t _r			36	60	
Turn-Off Delay Time	t _{d(off)}			34	60	
Fall-Time	t _f			10	25	

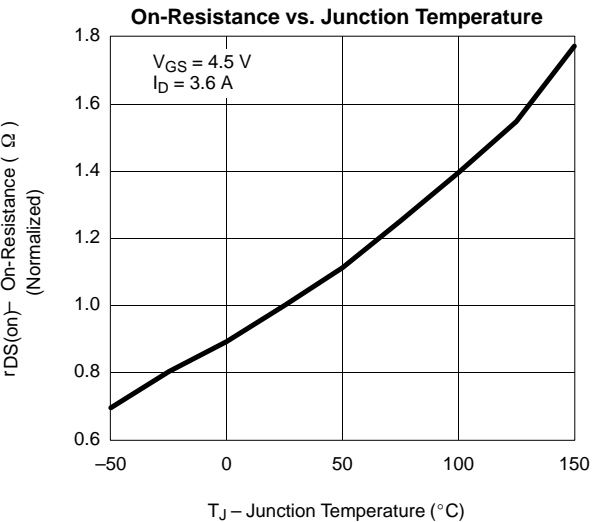
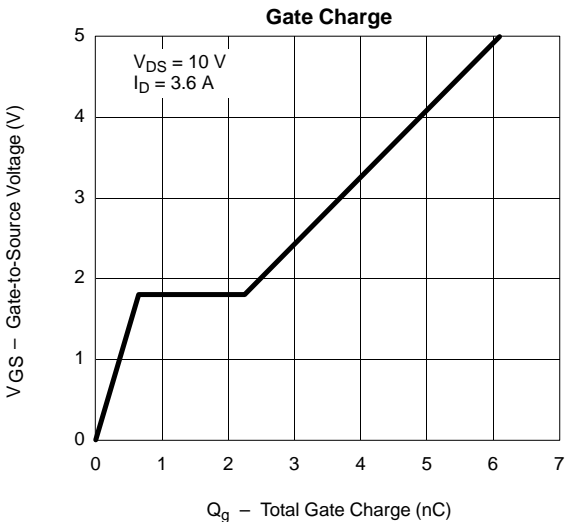
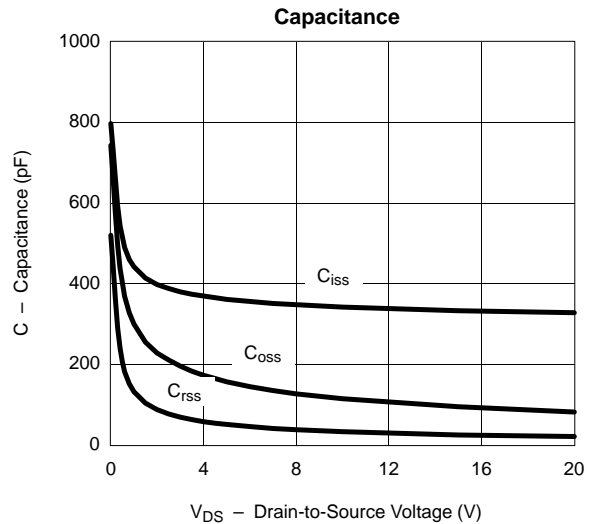
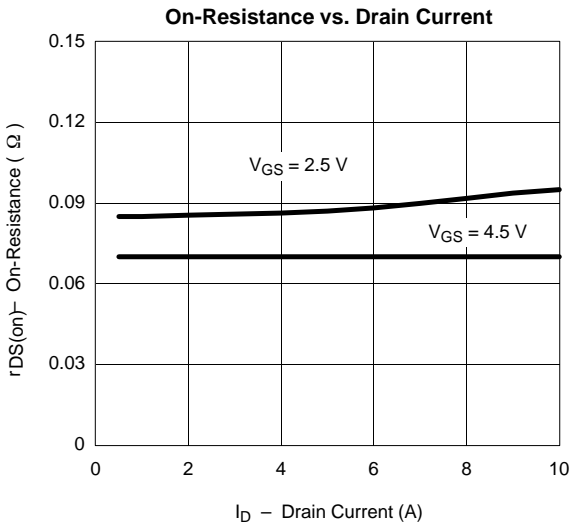
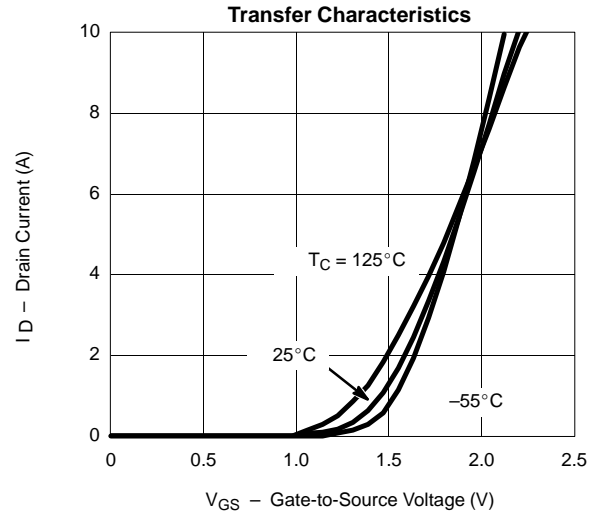
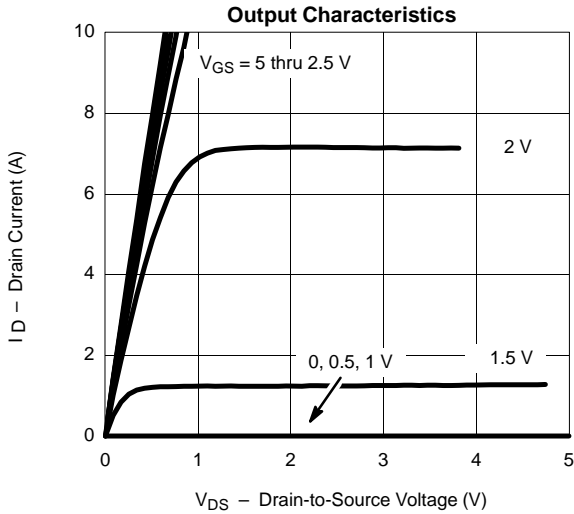
Notes

a. Pulse test: PW ≤ 300 μs duty cycle ≤ 2%.

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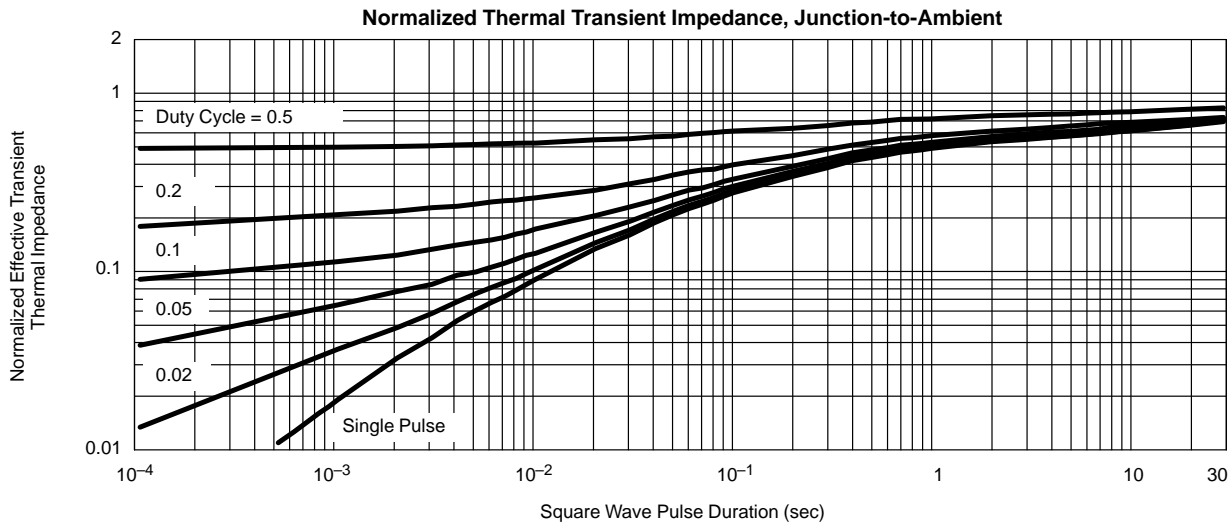
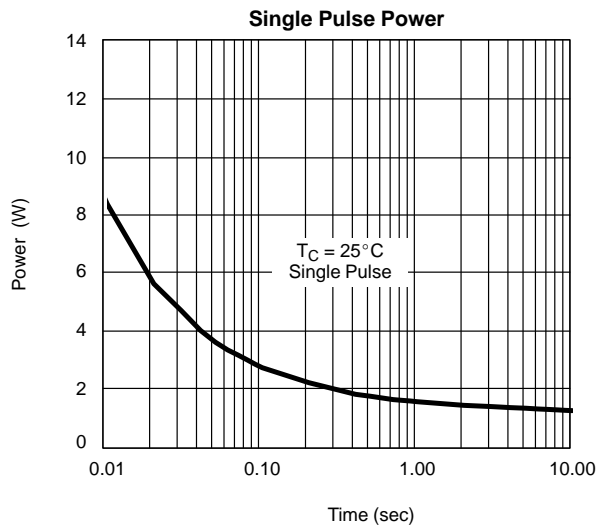
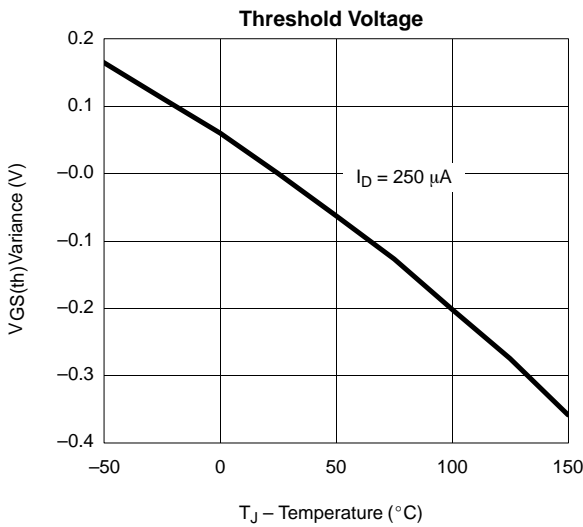
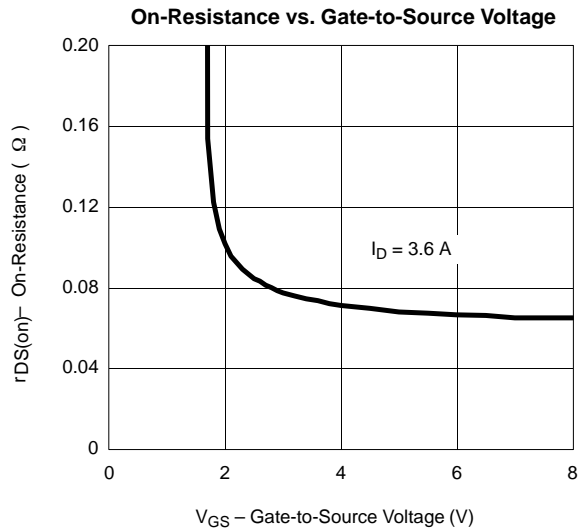
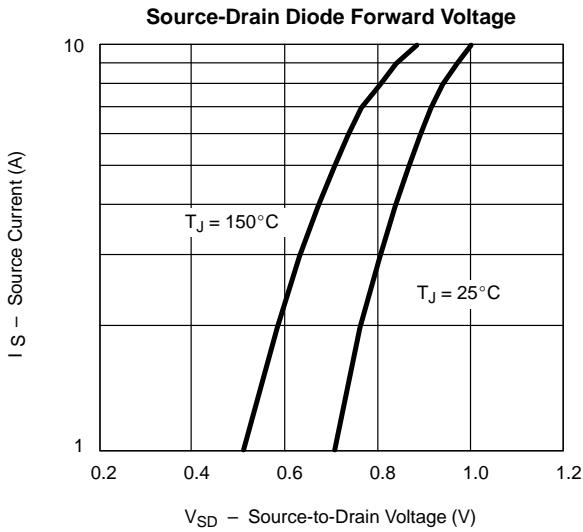


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)





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