

## **Pin Assignments**

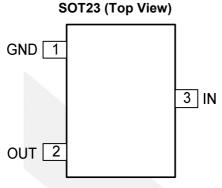


Figure 1 Pin Assignment

### **Pin Description**

Pin Name	Pin Description
OUT	Output pin, decoupled with a 10µF capacitor to GND
GND	Ground pin
IN	Input pin, decoupled with a $10\mu F$ capacitor to GND

## **Absolute Maximum Ratings**

Stresses beyond those listed under "Absolute Maximum Rating" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maxim rating conditions for extended periods may affect device reliability.

Parameter		Rating	Unit	
All pins		-0.3 to 6	V	
Package Thermal Resistance	$\theta_{JA}$ , SOT23	215	°C/W	
Junction Temperature Range		150	°C	
Lead Temperature (Soldering, 10 sec)		260	°C	
Storage Temperature Range (T <sub>STG</sub> )		-65 to 150	°C	
	HBM (Human Body Mode)	6	kV	
ESD Susceptibility	CDM (Charged Device Mode)	2		

Note: Input and output negative ratings may be exceeded if input and output diode current ratings are observed.



## **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation to ensure optimal performance to the datasheet specifications. DIOO does not recommend exceeding them or designing to Absolute Maximum Ratings.

Parameter	Rating	Unit
IN	2.7 to 5.5	V
All other pins	0 to 5.5	V
Junction Temperature Range	-40 to 125	°C
Ambient Temperature Range	-40 to 85	°C

## **Electrical Characteristics**

 $T_A=25^{\circ}C V_{IN} = 5V$ , unless otherwise noted.

	<b>_</b>	<b>A</b> 1141		_		
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>IN</sub>	Input Voltage Range		2.7		5.5	V
Ιq	Quiescent Supply Current	Open load, IC Disabled		50	88	μΑ
R <sub>DS(ON)</sub>	FET R <sub>ON</sub>	I <sub>OUT</sub> =100mA		135	176	mΩ
$V_{\text{IN}_{\text{UVLO}}}$	IN UVLO Threshold				2.5	V
VIN_HYS	IN UVLO Hysteresis			240	400	mV
I <sub>LIM</sub>	Current Limit		300	425	550	mA
los	Short Current Limit			I <sub>LIM,Typ</sub> ×1.2	I <sub>LIM,Max</sub> ×1.2	А
T <sub>ON</sub>	Turn-on Time	$R_L=10\Omega, C_{OUT}=1\mu F$		400		μs
T <sub>OFF</sub>	Turn-off Time	R <sub>L</sub> =10Ω, C <sub>OUT</sub> =1μF		20		μs
T <sub>SD</sub>	Thermal Shut down Temperature			140		°C
	Thermal Shut down Hysteresis			20		°C



# Block Diagram

## **Application Information**

#### **Power Supply Considerations**

A 10 $\mu$ F ceramic capacitor from V<sub>IN</sub> to GND to prevent the input voltage from dropping during the hot-plug condition is strongly recommended. However higher capacitance could help reduce the voltage drop. Further more, bypassing the input with a 10 $\mu$ F ceramic capacitor improves the immunity of the device to short-circuit transients, because an output short will cause ringing on the input without the input capacitor. It could destroy the internal circuitry when the input transient voltage exceeds the absolute maximum supply voltage even for a short duration.

#### Under Voltage Lockout

A voltage sense circuit monitors the input voltage. When the input voltage is below approximately 2.4V, a control signal turns off the power switch.

#### **Over-Current Protection**

The DIO7231 responds to over current conditions by limiting output current to the  $I_{LIM}$  level. When an over current condition is detected, the device maintains a constant output current and reduces the output voltage accordingly. Complete shut down occurs only if the fault is present long enough to activate thermal limit.

Two possible overload conditions can occur. In the first condition, an excessive load occurs while the device is enabled. When the excessive load occurs, very high currents may flow for a short time before the current limit circuit can react. After the current limit circuit has tripped (reached the overcurrent trip threshold) the device switches into constant current mode to limit the current close to I<sub>LIM</sub>.

In the second condition, the load is gradually increasing beyond the recommended operating current. The current is permitted to rise until the currentlimit threshold  $(I_{LIM})$  is reached or until the thermal limit of the device is exceeded. The DIO7231 is capable of delivering current up to the current limit threshold  $(I_{LIM})$  without damaging the device. Once the threshold has been reached, the device switches into its constant current mode.

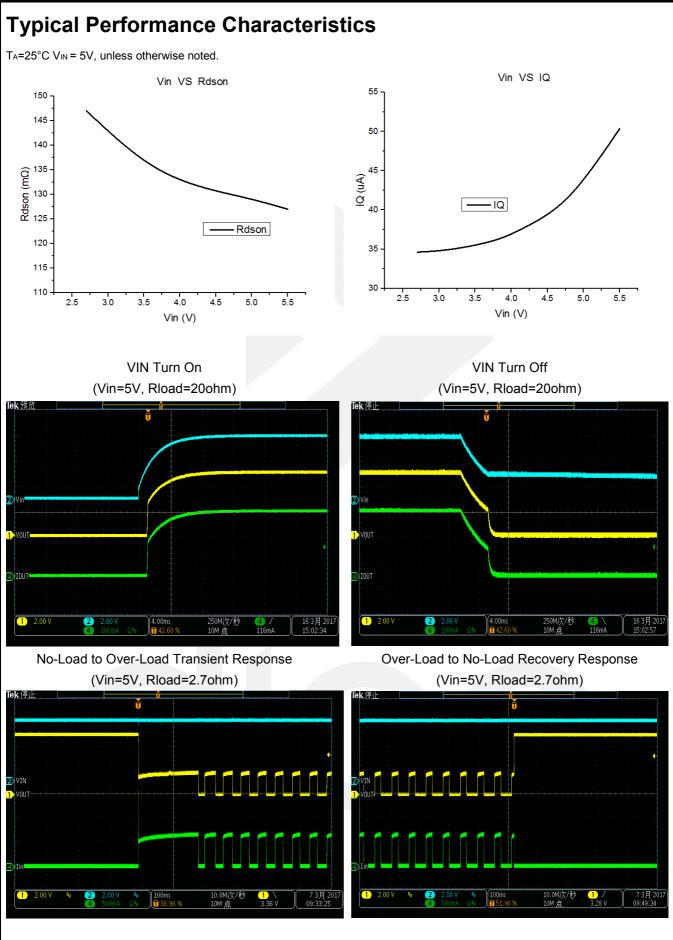
#### **Thermal Protection**

Thermal protection prevents damage to the IC when heavy overload or short circuit conditions are present for



extended periods of time. The conditions force the DIO7231 into constant current mode, and under short circuit conditions, the voltage across the switch is equal to the input voltage. The increased dissipation causes the junction temperature to rise to high levels. The protection circuit senses the junction temperature of the switch and shuts it off. Hysteresis is built into the thermal sense circuit, and after the device has cooled approximately 20 degrees, the switch turns back on. The switch continues cycle in this way until the overload or input power is removed.





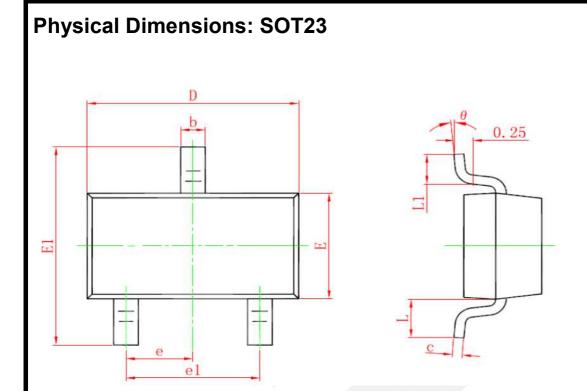
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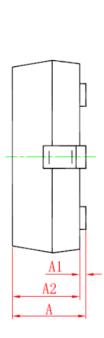
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Sumbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP.		0.037 TYP.		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF.		0.022 REF.		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	<b>0</b> °	8°	



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