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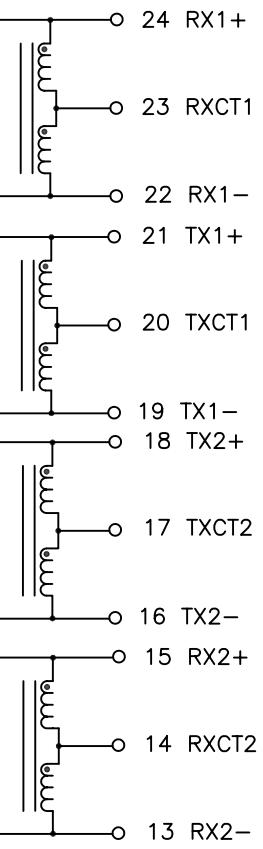

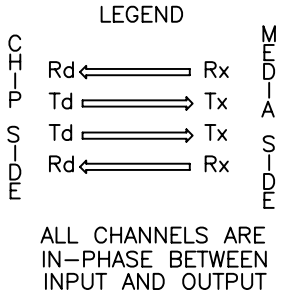

ALL CHANNELS ARE IN−PHASE BETWEEN INPUT AND OUTPUT

ELECTRICAL CHARACTERISTICS AT +25°C

PARAMETER	SPECIFICATIONS		
OPERATING TEMP	0°C − 70 °C		
TURNS RATIO	1.00 ± 2%		
POLARITY	PER SCHEMATIC		
INSERTION LOSS	100 KHz	1−100 MHz	
	−1.2 dB MAX	−0.2−0.002*f ^1.4 dB MAX	
RETURN LOSS (Z OUT = 100 OHM ±15%)	.1−30 MHz	30−60 MHz	60−80 MHz
	−16 dB MIN	$-10+20*\log_{10}$ (f/60 MHz) dB MIN	−10 dB MIN
INDUCTANCE (OCL) (MEDIA SIDE ACROSS PINS 13−15, 16−18, 19−21, 22−24), 0°C−70°C	350 uH MIN	(MEASURED AT 100 KHz, 100 mVRMS AND WITH 8 mA DC BIAS)	
CROSSTALK, ADJACENT CHANNELS	1 MHz	10−100 MHz	
	−50 dB MIN	$-55+22*\log_{10}(f/10)$ dB MIN	
DIFFERENTIAL TO COMMON MODE REJECTION	2 MHz	30−200 MHz	
	−50 dB MIN	$-43+22*\log_{10}(f/30)$ dB MIN	
DC RESISTANCE, 1/2 WINDING	.65 OHMS MAX (MEASURED AT PINS 24−23/23−22; 21−20/20−19; 18−17/17−16; 15−14/14−13)		
DC RESISTANCE IMBALANCE	PIN (24−23) − PIN (23−22) = ±.065 OHMS MAX PIN (21−20) − PIN (20−19) = ±.065 OHMS MAX PIN (18−17) − PIN (17−16) = ±.065 OHMS MAX PIN (15−14) − PIN (14−13) = ±.065 OHMS MAX		
INPUT − OUTPUT ISOLATION	1500 VRMS MIN @ 60 SECONDS		

NOTE: f IS FREQUENCY IN MHz.

T DESCRIPTION	PS DRAWING	SHEET:	DWG. NO./ PART NO.	REV.
L,100P,1:1,SM,OH	PS−0118.001−D	2	H2009NL	M17

Mouser Electronics

Authorized Distributor

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H2009 H2009T