BENEFITS

- Scalability: Single solution for mesh, fabric, and aggregated systems
- Performance: Improved system and distributed processing performance
- Power: SerDes implementation for low power solution

Specifications

- Technology: 0.13um
- Voltage: 1.2V and 3.3V
- Low power consumption
- Package: 675 ball, 27mm x 27mm, 1mm ball pitch FCBGA
- Rated for commercial and industrial temperatures
- Forward compatible with the Tsi568A Serial RapidIO Switch allowing easy migration for existing systems.

Target Markets

- Wireless Infrastructure
  - Node B, Radio Network Controller, Media Gateway
- Communications Wireline Infrastructure
  - Multiservice WAN Switches, 1 to 10 Gbit Ethernet Switches, 1 to >10 Gbit Routers, DSLAMs
- Storage
  - Storage Area Networks, Network Attached Storage, High-Performance Work Stations, Multi-service Access Nodes, Carrier-grade VoIP
- Video Infrastructure
  - Broadcast, imaging, and encoding
  - Architecture Standards
  - ATCA, MicroTCA, VXS, VPX

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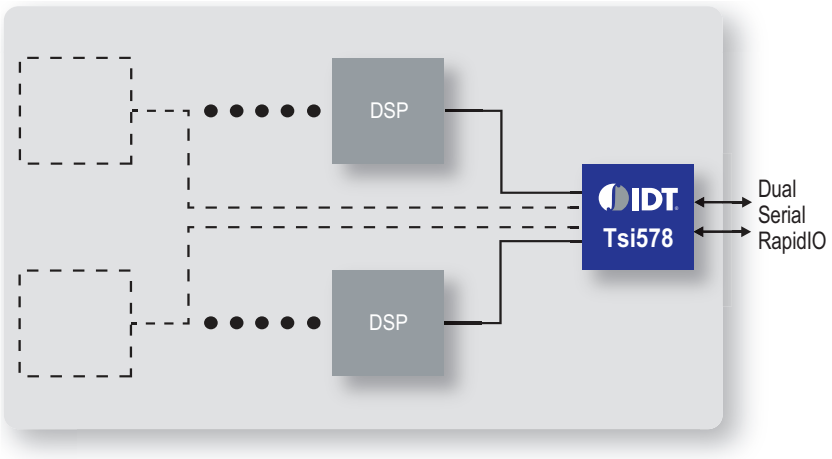
Typical Applications

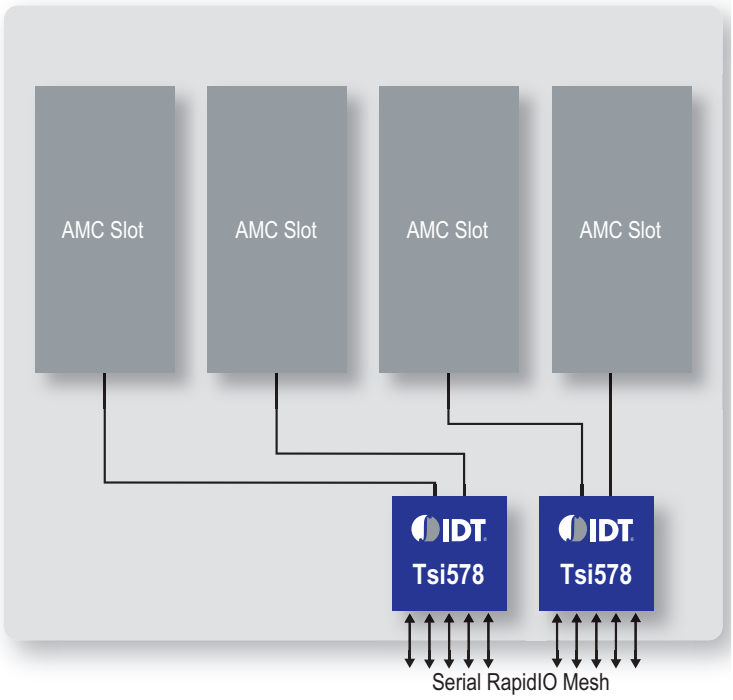
The Tsi578 can be used in many embedded communication applications. It provides chip-to-chip interconnect between I/O devices and can replace existing proprietary backplane fabrics for board-to-board interconnect which improves system cost and product time-to-market.

Processor Farm Mezzanine



The Tsi578 provides traffic aggregation through packet prioritization when it is used with RapidIO-enabled I/O devices. When it is in a system with multiple RapidIO-enabled processors it provides high performance peer-to-peer communication through its non-blocking switch fabric.

Switch Carrier Blade



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