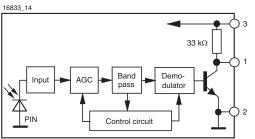


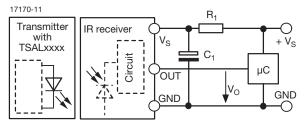
TSOP582.., TSOP584..

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BLOCK DIAGRAM



APPLICATION CIRCUIT



 R_1 and C_1 recommended to reduce supply ripple for $V_S < 2.8 V$

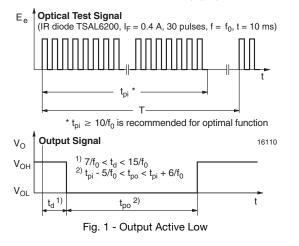
ABSOLUTE MAXIMUM RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Supply voltage		Vs	-0.3 to +6	V			
Supply current		I _S	5	mA			
Output voltage		Vo	-0.3 to 5.5	V			
Voltage at output to supply		V _S - V _O	-0.3 to (V _S + 0.3)	V			
Output current		Ι _Ο	5	mA			
Junction temperature		Тj	100	°C			
Storage temperature range		T _{stg}	-25 to +85	°C			
Operating temperature range		T _{amb}	-25 to +85	°C			
Power consumption	T _{amb} ≤ 85 °C	Ptot	10	mW			
Soldering temperature	$t \le 10$ s, 1 mm from case	T _{sd}	260	°C			

Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability

ELECTRICAL AND OPTICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Supply voltage		Vs	2.5	-	5.5	V	
Supply current	$V_{\rm S} = 5 V, E_{\rm v} = 0$	I _{SD}	0.55	0.7	0.9	mA	
	E _v = 40 klx, sunlight	I _{SH}	-	0.8	-	mA	
Transmission distance	$E_v = 0$, IR diode TSAL6200, I _F = 50 mA, test signal see Fig. 1	d	-	18	-	m	
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2$, test signal see Fig. 1	V _{OSL}	-	-	100	mV	
Minimum irradiance	ce Pulse width tolerance: $t_{pi} - 5/f_o < t_{po} < t_{pi} + 6/f_{o}$, test signal see Fig. 1		-	0.2	0.4	mW/m ²	
Maximum irradiance	t_{pi} - 5/f _o < t_{po} < t_{pi} + 6/f _o , test signal see Fig. 1	E _{e max.}	50	-	-	W/m ²	
Directivity	Angle of half transmission distance	φ1/2	-	± 45	-	deg	

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)



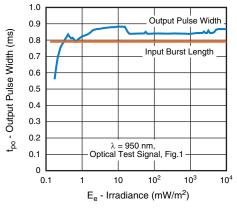


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient

Rev. 1.7, 26-Sep-2018

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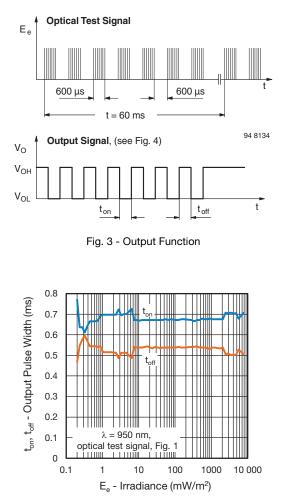


Fig. 4 - Output Pulse Diagram

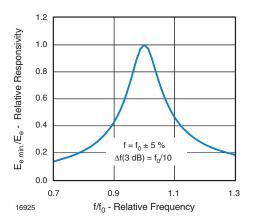


Fig. 5 - Frequency Dependence of Responsivity

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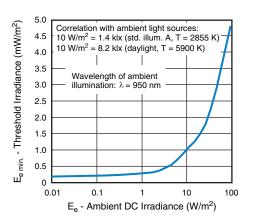


Fig. 6 - Sensitivity in Bright Ambient

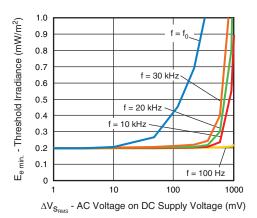


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

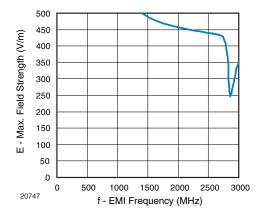


Fig. 8 - Sensitivity vs. Electric Field Disturbances

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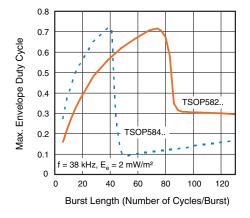


Fig. 9 - Max. Envelope Duty Cycle vs. Burst Length

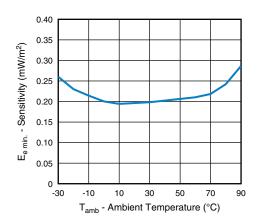


Fig. 10 - Sensitivity vs. Ambient Temperature

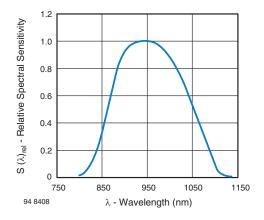


Fig. 11 - Relative Spectral Sensitivity vs. Wavelength

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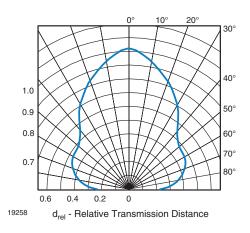


Fig. 12 - Horizontal Directivity

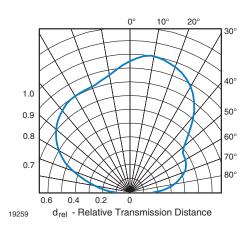


Fig. 13 - Vertical Directivity

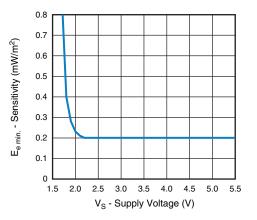


Fig. 14 - Sensitivity vs. Supply Voltage

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4



SUITABLE DATA FORMAT

This series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the product in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output.

Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- Continuous signals at any frequency
- Strongly or weakly modulated noise from fluorescent lamps with electronic ballasts (see Fig. 15 or Fig. 16)
- 2.4 GHz and 5 GHz Wi-Fi

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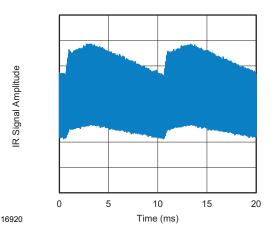


Fig. 15 - IR Disturbance from Fluorescent Lamp With Low Modulation

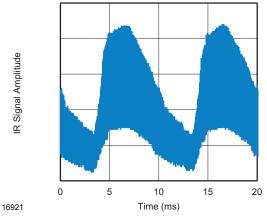


Fig. 16 - IR Disturbance from Fluorescent Lamp With High Modulation

	TSOP582	TSOP584
Minimum burst length	10 cycles/burst	10 cycles/burst
After each burst of length a minimum gap time is required of	10 to 70 cycles ≥ 12 cycles	10 to 35 cycles ≥ 12 cycles
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 4 x burst length	35 cycles > 10 x burst length
Maximum number of continuous short bursts/second	800	1300
NEC code	Yes	Preferred
RC5/RC6 code	Yes	Preferred
Thomson 56 kHz code	Yes	Preferred
Sharp code	Yes	Preferred
Suppression of interference from fluorescent lamps	Mild disturbance patterns are suppressed (example: signal pattern of Fig. 15)	Complex and critical disturbance patterns are suppressed (example: signal pattern of Fig. 16 or highly dimmed LCDs)

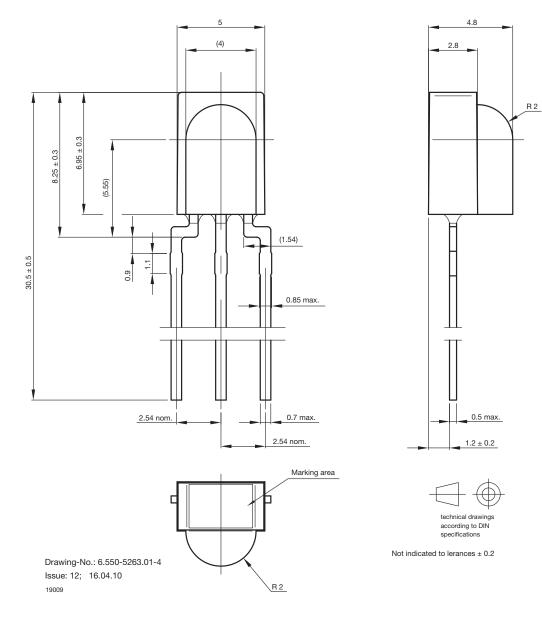
Note

• For data formats with short bursts please see the datasheet of TSOP581.., TSOP583..



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PACKAGE DIMENSIONS in millimeters





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