MADP-017015-1314 MADP-030015-1314 SURMOUNT™ 15 µm PIN Diodes

RoHS Compliant



Rev. V8

Electrical Specifications @ T_A = 25°C (unless otherwise noted)

Parameter	Conditions	Units	MA	ADP-0170	15	MADP-030015		
			Min.	Тур.	Max.	Min.	Тур.	Max.
Capacitance (C _T)	-40 V, 1 MHz ^{4,6} -40V, 1 GHz ^{4,6}	pF	_	0.32 0.31	0.3	_	0.79 0.78	0.85
Capacitance (C _T) @ 85°C	-40 V, 1 GHz ^{4,6}	pF	—	0.29	—	_	0.76	_
Resistance (R _s)	+10 mA, 1 GHz ^{5,6} +70 mA, 1 GHz ^{5,6}	Ω	_	0.72 0.51		_	0.49 0.38	_
Resistance (R _s) @ 85°C	+10 mA, 1 GHz ^{5,6} +70 mA, 1 GHz ^{5,6}	Ω	_	1.08 0.84	_	_	0.82 0.69	_
Forward Voltage (V _F)	+10 mA	V	—	0.74	0.90	—	0.72	0.90
Reverse Leakage Current (I _R)	-115 V	А			10			10
Third Order Intercept Point (IP3)	F1= 1800 MHz F2 = 1810 MHz Input Power = 0 dBm I bias = +70 mA	dBc	_	-36.8	_	_	-37.0	_
Thermal Resistance $^{7}(\theta)$	—	°C/W		30	_	_	13	
Lifetime (T _L)	+10 mA / -6 mA (50% - 90% V)	μs	_	1.3	_	_	1.6	_

4. Total capacitance, C_T, is equivalent to the sum of Junction Capacitance ,Cj, and Parasitic Capacitance, Cpar.

5. Series resistance R_s is equivalent to the total diode resistance : Rs = Rj (Junction Resistance) + Rc (Ohmic Resistance).

6. Rs and C_T are measured on an HP4291A Impedance Analyzer with the die mounted in an ODS-186 package.

7. Theta (θ) is measured with the die mounted in an ODS-186 package.

Typical Spice Parameters @ T_A = +25°C

Spice Parameter	Ν	RS	IS	IK	BV	IBV	Ct	CJO	VJ	М	FC	Cpar_Cj
Units	-	W	А	(mA)	(Volts)	(mA)	(pF)	(pF)	(Volts)	-	-	(F)
MADP-017015-1314	1.1	1.2	9.8E-15	14.7	145	10	0.46	0.10	0.29	0.50	0.34	3.5E-13
MADP-030015-1314	1.1	1.1	8.5E-15	13.9	145	10	1.12	0.29	0.18	0.50	0.19	8.2E-13

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Parameter	Absolute Maximum					
Forward Current	500 mA					
Reverse Voltage	-115 V					
Operating Temperature	-55°C to +125°C					
Storage Temperature	-55 °C to +150°C					
Junction Temperature	+175°C					
C.W. Incident Power	50 dBm					
Mounting Temperature	+280°C for 30 seconds					

Absolute Maximum Ratings⁸ @ $T_A = +25^{\circ}C$ (unless otherwise specified)

8. Exceeding these limits may cause in permanent damage.

Handling Procedures

All semiconductor chips should be handled with care to avoid damage or contamination from perspiration and skin oils. The use of plastic tipped tweezers or vacuum pickups is strongly recommended for individual components. Bulk handling should insure that abrasion and mechanical shock are minimized.

Bonding Techniques

Attachment to a circuit board is made simple through the use of surface mount technology. Mounting pads are conveniently located on the bottom surface of these devices and are removed from the active junction locations. These devices are well suited for solder attachment onto hard and soft substrates. The use of 80Au/20Sn, or RoHS compliant solders is recommended. For applications where the average power is ~1W, conductive silver epoxy may also be used. Cure per manufacturers recommended time and temperature. Typically 1 hour at 150°C.

When soldering these devices to a hard substrate, hot gas die bonding is preferred. A vacuum tip pick-up tool and a force of 60 to100 grams applied to the top surface of the device is recommended. When soldering to soft substrates, such as Duroid, it is recommended to use a soft solder at the circuit board to mounting pad interface. Position the die so that its mounting pads are aligned with the circuit board mounting pads. While applying a downward force perpendicular to the top surface of the die, apply heat near the circuit trace and diode mounting pad. The solder connection to the two pads should not be made one at a time as this will create unequal heat flow and thermal stress to the part. Solder reflow should not be performed by causing heat to flow through the top surface of the die to the back. Since the HMIC glass is transparent, the edges of the mounting pads can be visually inspected through the die after attachment is completed.

Typical re-flow profiles for Sn60/Pb40 and RoHS compliant solders is provided in Application Note M538, "Surface Mounting Instructions" and can viewed on the MACOM website @ www.macom.com

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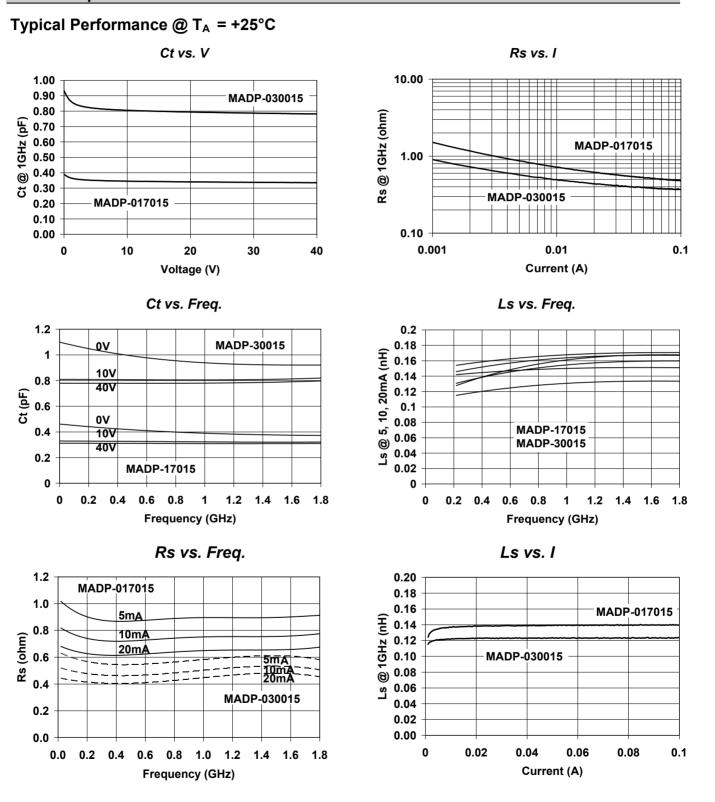
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