

Smallest Footprint

The Tsi382 BGA package has the smallest footprint of any PCIe-to-PCI device on the market. The device is offered in a 10 x 10 mm package with a standard 0.8 mm ball pitch, making it ideal for PCI ExpressCard applications or similar designs that have limited component space. For cost-sensitive applications, the Tsi382 is also available in a LQFP package.

In addition, by providing sufficient clock outputs for up to four PCI devices, board space is further reduced by eliminating the need for an external clock buffer.

Low Power Consumption

The Tsi382 has typical power consumption of less than 0.7W, and it incorporates advanced power management modes to minimize consumption during operation.

High Performance

In addition to low-latency operation, the Tsi382's superior queueing architecture and rich feature set allow designers to optimize their overall system performance. Features such as short-term caching also enable designers to tune the device's performance for different applications.

Transparent, Non-transparent, and Opaque Bridging

Transparent mode operation is available for efficient, flow through configurations. Non-transparent bridging also enables multi-host systems and is used in applications such as intelligent adapter cards. Opaque mode provides semi-transparent operation for multi-processor configurations and enhanced private device support.

Typical Applications

The Tsi382 is suited to applications that need to bridge PCIe to downstream PCI devices. Its flexibility, high performance, small footprint, and low power consumption, make it ideal for a wide range of applications, including:

- Digital video recorders
- ExpressCards for laptop computers
- Motherboards (PC, ultra-mobile PC, server, SBC, industrial PC)
- PC adapter cards (communications, graphics, imaging, and multimedia)
- Multifunction printers
- Line cards and NICs

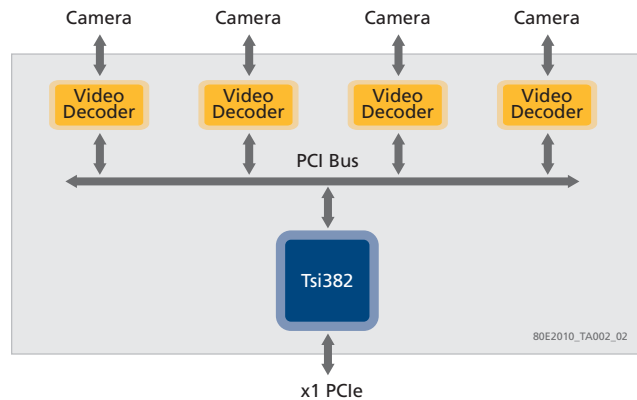


Figure 2 DVR Application

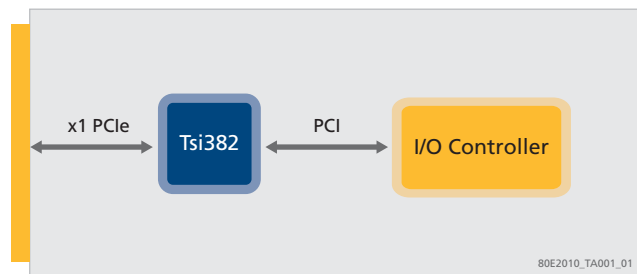


Figure 3 ExpressCard Application

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