



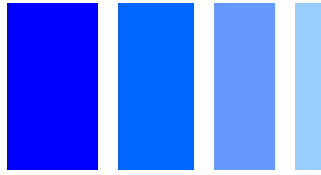
### Electrical Characteristics

Part Name	Stamp	Inductance ( $\mu\text{H}$ ) [Within]※1	D.C.R. (m $\Omega$ ) [Max.] ( 20℃)	Rated Current (A) ※2
CD43NP-1R0MC	1R0	1.0±20%	48.7	2.70
CD43NP-1R4MC	1R4	1.4±20%	56.2	2.50
CD43NP-1R8MC	1R8	1.8±20%	63.7	2.33
CD43NP-2R2MC	2R2	2.2±20%	71.2	2.25
CD43NP-2R7MC	2R7	2.7±20%	78.7	2.16
CD43NP-3R3MC	3R3	3.3±20%	86.2	2.00
CD43NP-3R9MC	3R9	3.9±20%	93.7	1.84
CD43NP-4R7MC	4R7	4.7±20%	108.7	1.62
CD43NP-5R6MC	5R6	5.6±20%	125.7	1.48
CD43NP-6R8MC	6R8	6.8±20%	131.2	1.43
CD43NP-8R2MC	8R2	8.2±20%	146.2	1.37
CD43NP-100MC	100	10±20%	182	1.04
CD43NP-120MC	120	12±20%	210	0.97
CD43NP-150MC	150	15±20%	235	0.85
CD43NP-180MC	180	18±20%	338	0.74
CD43NP-220MC	220	22±20%	378	0.68
CD43NP-270MC	270	27±20%	522	0.62
CD43NP-330KC	330	33±10%	540	0.56
CD43NP-390KC	390	39±10%	587	0.52
CD43NP-470KC	470	47±10%	844	0.44
CD43NP-560KC	560	56±10%	937	0.42
CD43NP-680KC	680	68±10%	1117	0.37

※1: Inductance measuring frequency: 1.0 $\mu\text{H}$  ~ 8.2 $\mu\text{H}$  ; at 7.96 MHz.  
10 $\mu\text{H}$  ~ 68 $\mu\text{H}$  ; at 2.52 MHz.

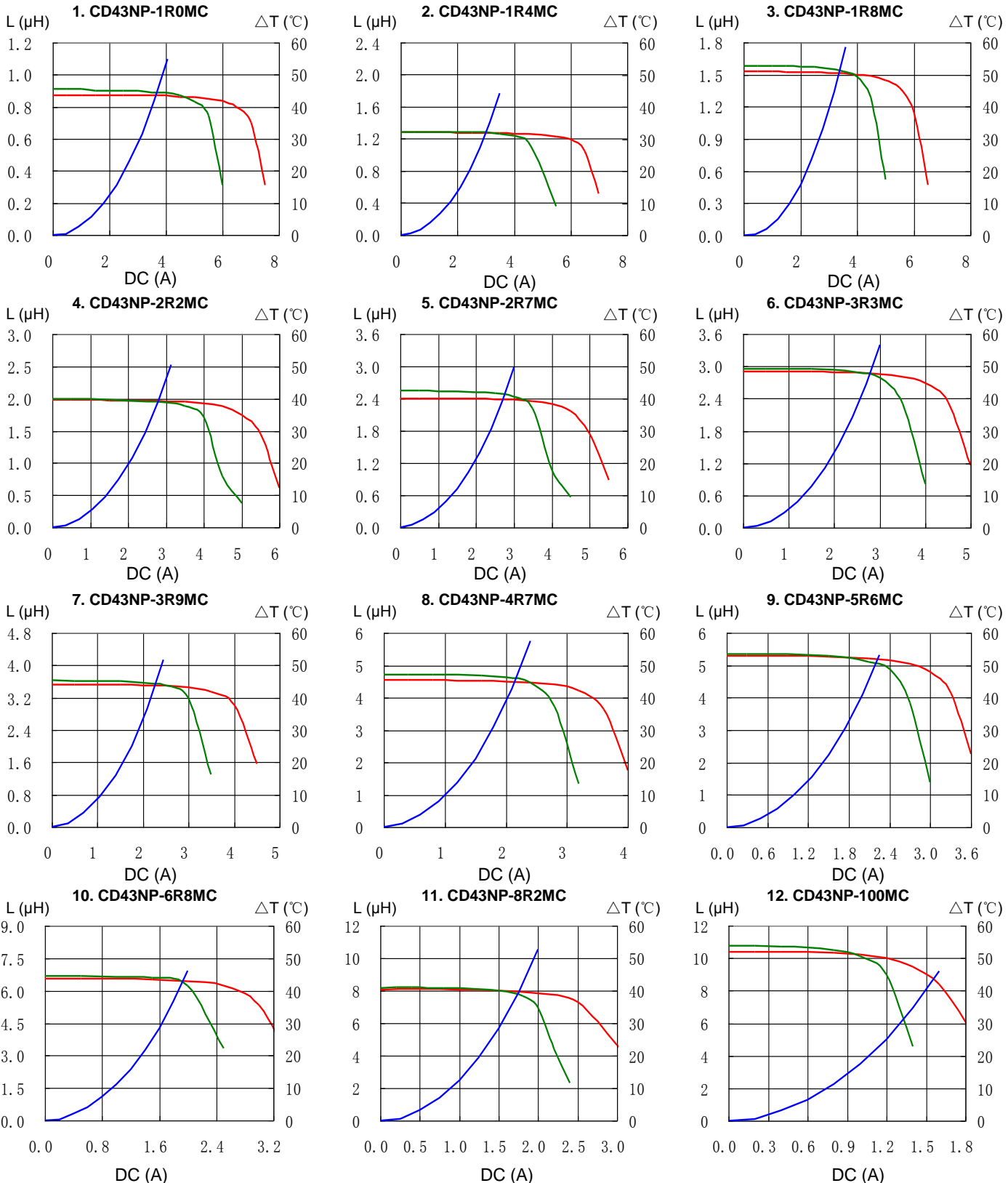
※2: Rated current: The DC current at which the inductance decreases to 90 % of it's initial value or when  $\Delta t=40^\circ\text{C}$ , whichever is lower ( $T_a=20^\circ\text{C}$ ).

# SMD Power Inductor CD43



## Saturation Current & Temperature Rise Graph

— L (20°C) — L (105°C) —  $\Delta T$

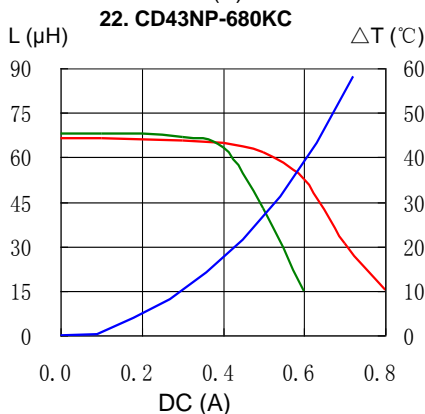
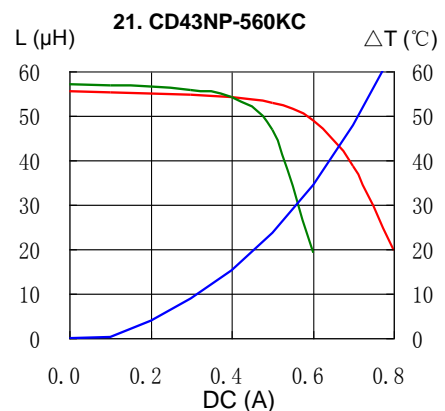
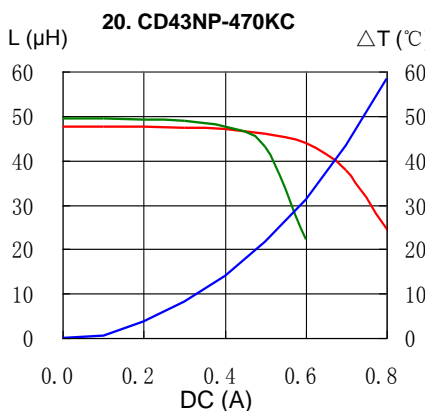
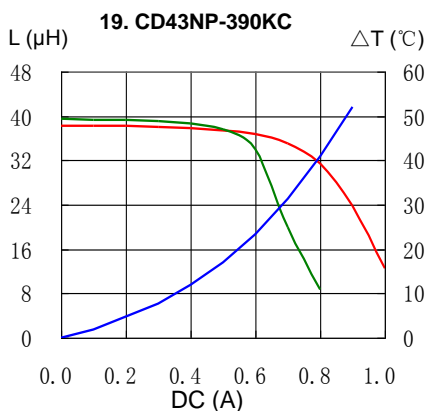
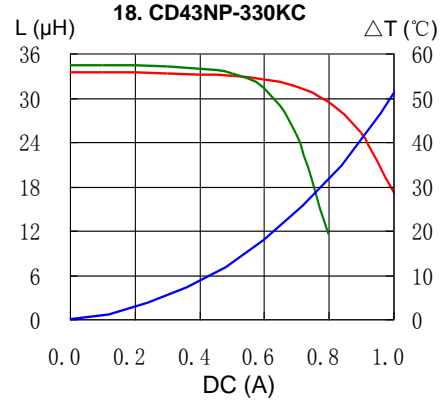
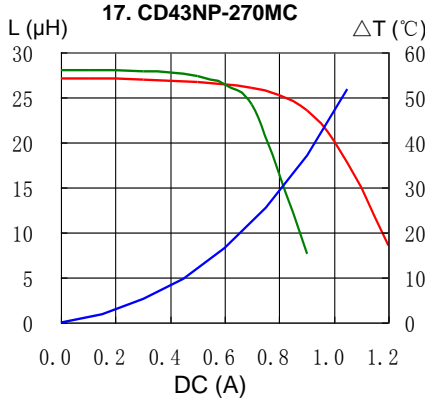
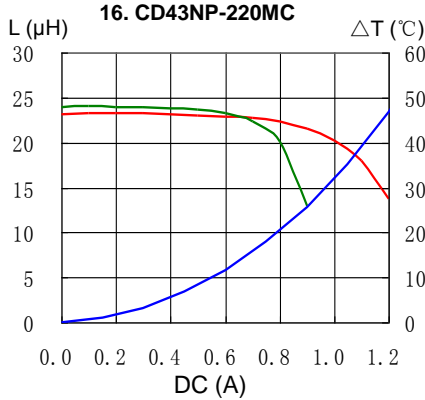
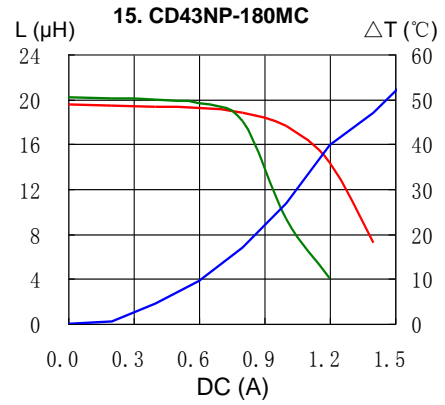
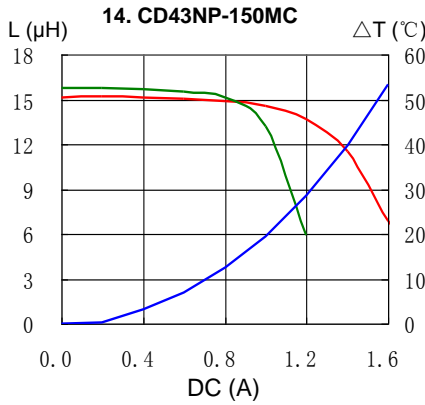
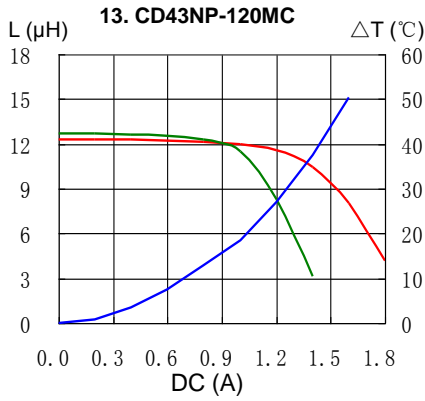


# SMD Power Inductor CD43

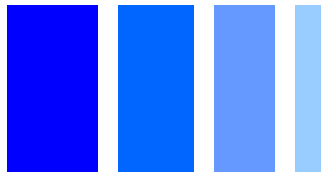


## Saturation Current & Temperature Rise Graph

— L (20°C) — L (105°C) —  $\Delta T$

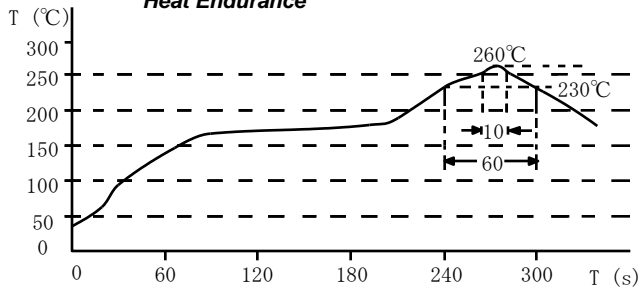


# SMD Power Inductor CD43

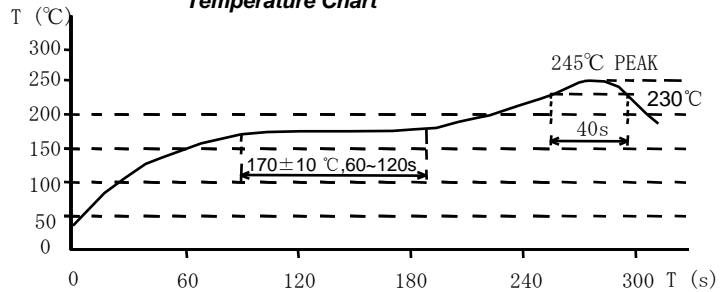


## Solder Reflow Condition

Heat Endurance



Temperature Chart



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