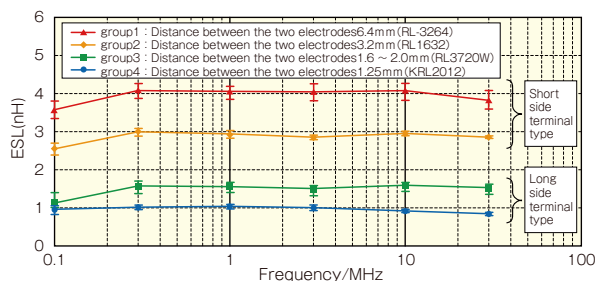


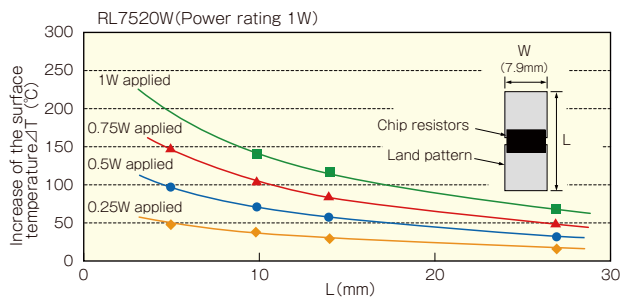
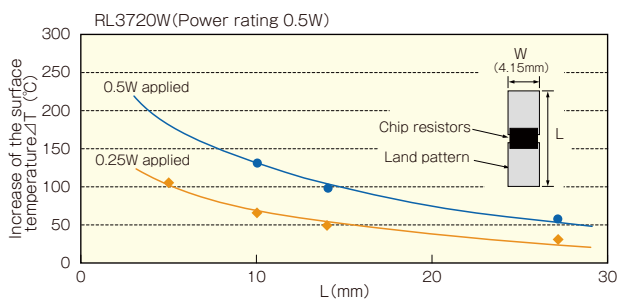
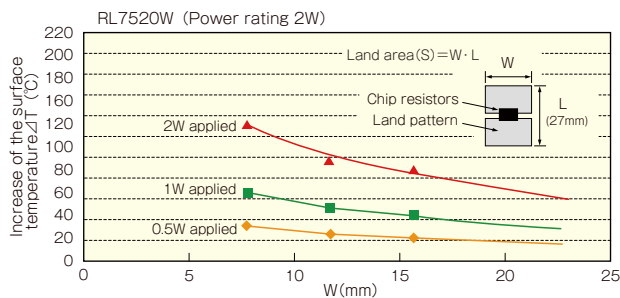
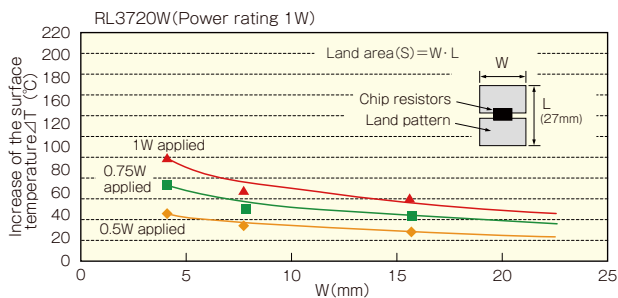
◆ESL (Equivalent series inductance)



◆Surface temperature data

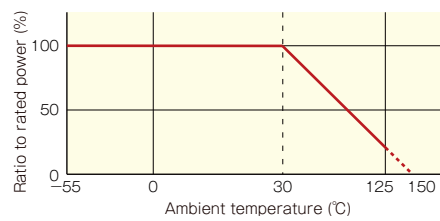
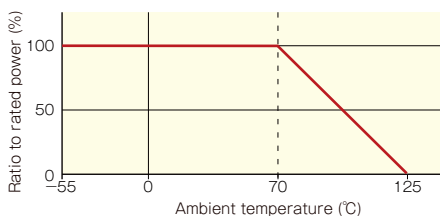
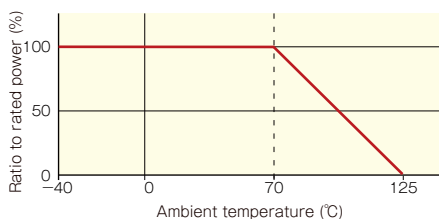
○ The high power type land pattern and surface temperature

These high-power low resistance chip resistors are designed to dissipate heat efficiently through the land patterns on circuit boards. The actual temperature of the surface of the resistor is dependent upon the dimensions and the shape of the land patterns.

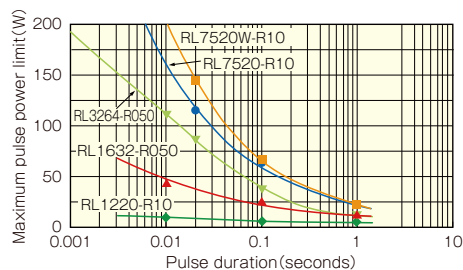


Current sensing surface mount resistors PRL / RL series

◆Derating Curve



◆Resistance to power pulse



Test procedure

Voltage pulse is applied to the test samples mounted on the test board.

After each pulse, resistance drift is measured. Pulse voltage is increased until the drift exceeds +/-0.5%.

The power at that voltage is defined as the maximum pulse power.

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