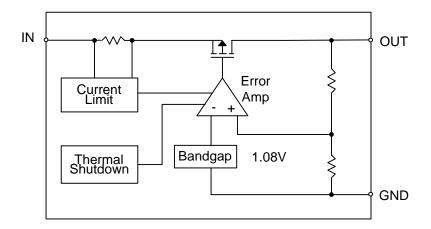


Pin Assignments

Package		NI-		D tutto	
Туре	Code	No.	Pin Name	Description	
(Top View)		1	IN		
[3]	w	2	OUT		
1 2 (SC59)		3	GND		
(Top View) ③		1	GND		
	R	2	OUT		
1 2 (SC59R)		3	IN		
(Top View)		1	IN	IN: Power Input	
	SA	2	OUT	OUT: Output Voltage	
1 2 (SOT23)		3	GND	GND: Ground	
(Top View)		1	OUT		
	Y	2	GND		
1 2 3 (SOT89-3L)		3	IN		
(Top View)		1	GND		
	YR	2	IN		
1 2 3 (SOT89R-3L)		3	OUT		



Functional Block Diagram



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
V _{CC}	Input Voltage	+6	V
T _{OP}	Operating Junction Temperature	-40 to +125	°C
T _{ST}	Storage Temperature Range	-65 to +150	°C

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V _{IN}	Input Voltage	2.7	5.5	V
lout	Output Current	0	300	mA
T _A	Operating Ambient Temperature	-40	85	°C



Electrical Characteristics

 $T_A = 25^{\circ}C$, $C_{IN} = 1\mu F$, $C_{OUT} = 10\mu F$, unless otherwise specified.

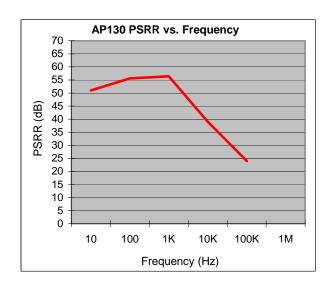
Symbol	Parameter	Conditions	Min	Тур.	Max	Unit	
V_{DROP}	Dropout Voltage (Note 2)	I _L = 300mA	-	400	500	mV	
I _{LIMIT}	Current Limit (Note 3)	$V_{IN} = 5V$, $V_{OUT} = 0V$	350	450	-	mA	
I _{short}	Short Circuit Current	V _{OUT} < 1.05V	-	150	300	mA	
ΔV_{LINE}	Line Regulation	$I_{OUT} = 1mA,$	_	0.1	0.3	%/V	
A V LINE	Ellie Regulation	$V_{IN} = (V_{OUT}+1V)$ to 5.5V		0.1	0.0	70/ V	
		F = 100Hz,				dB	
PSRR	Ripple Rejection	$C_{IN} = 1\mu F, C_O = 10uF,$	-	58	-		
		I _L = 100mA					
ΔV_{LOAD}	Load Regulation (Note 4)	$I_L = 1 \sim 300 \text{mA}, V_{IN} = 5 \text{V}$	-	30	40	mV	
	Output Voltage Accuracy	$I_L = 1 \text{mA}, V_{IN} = 5 \text{V}$	-2	-	+2	%	
ΔV _{OUT}	Output Voltage Temperature Coefficient (Note 5)		-	50	150	PPM/°C	
IQ	Quiescent Current	$I_L = 0mA$, $V_{IN} = 5V$	-	50	100	μA	
		SC59/SC59R (Note 6)	-	250	-		
θ_{JA}	Thermal Resistance Junction-to-Ambient	SOT23 (Note 7)	-	200	-	°C/W	
		SOT89-3L/SOT89R-3L (Note 8)	-	100	-		
		SC59/SC59R (Note 6)	-	79	-		
$\theta_{ m JC}$	Thermal Resistance Junction-to-Case	SOT23 (Note 7)	-	43	-	°C/W	
		SOT89-3L/SOT89R-3L (Note 8)	-	23	-		

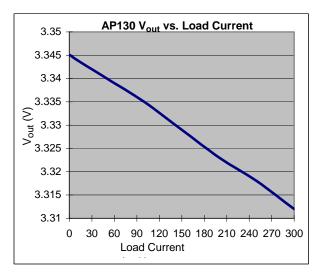
Notes:

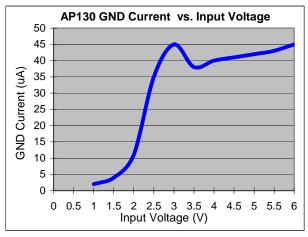
- 2. Dropout voltage is defined as the input to output differential voltage. Dropout is measured at constant junction temperature by using pulsed on time, and the criterion is V_{OUT} inside target value ±2%. This test is skipped at the condition of V_{IN}<3V.
 - 3. Current limit is measured at constant junction temperature by using pulsed testing with a low ON time.
- 4. Regulation is measured at constant junction temperature by using pulsed testing with a low ON time.
- 5. Guaranteed by design.
- 6. Test condition for SC59/SC59R: Devices mounted on FR-4 PC board, 1 MRP, 2oz copper, single sided, calibrate at T_J=125°C, T_A=25°C, with minimum recommended pad layout.
- 7. Test condition for SOT23: Devices mounted on FR-4 PC board, 1 MRP, calibrate at T_J=85°C, T_A=29°C.
- 8. Test condition for SOT89-3L/SOT89R-3L: No Heat Sink, no air flow.

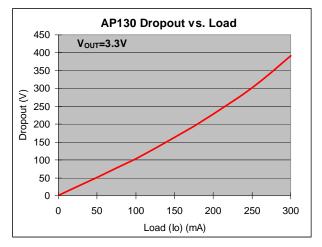


Typical Performance Characteristics







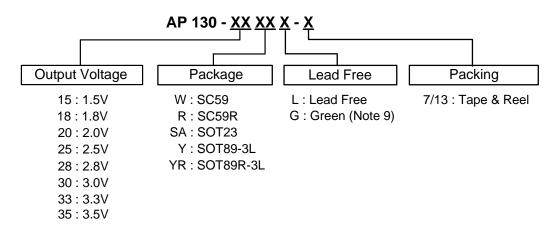


Functional Descriptions

A minimum of $10\mu F$ capacitor must be connected from OUT to ground to insure stability. Typically a large storage capacitor is connected from V_{IN} to ground to ensure that the input voltage does not sag below the minimum dropout voltage during the load transient response.



Ordering Information



	Device	Package Code	Packaging	7"/13" Tape and Reel	
	Device	Package Code	(Note 10)	Quantity	Part Number Suffix
Pb	AP130-XXWL-7	W	SC59	3000/Tape & Reel	-7
Pb ,	AP130-XXWG-7	W	SC59	3000/Tape & Reel	-7
Pb	AP130-XXRL-7	R	SC59R	3000/Tape & Reel	-7
Pb ,	AP130-XXRG-7	R	SC59R	3000/Tape & Reel	-7
Pb,	AP130-XXSAG-7	SA	SOT23	3000/Tape & Reel	-7
Pb	AP130-XXYL-13	Y	SOT89-3L	2500/Tape & Reel	-13
Pb,	AP130-XXYG-13	Y	SOT89-3L	2500/Tape & Reel	-13
Pb	AP130-XXYRL-13	YR	SOT89R-3L	2500/Tape & Reel	-13
Pb	AP130-XXYRG-13	YR	SOT89R-3L	2500/Tape & Reel	-13

Notes:

SOT23 is available in "Green" product only.
 Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.



Marking Information

(1) SC59, SC59R and SOT23

(Top View)

3

XX YWX

1

2

 \underline{XX} : Identification code \underline{Y} : Year 0~9

W: Week: A~Z: 1~26 week;

a~z: 27~52 week; z represents

52 and 53 week

 \underline{X} : A~Z: Green a~z: Lead Free

Device	Package (Note 11)	Identification Code
AP130-15W	SC59W	CA
AP130-18W	SC59W	CD
AP130-20W	SC59W	CF
AP130-25W	SC59W	СК
AP130-28W	SC59W	CN
AP130-30W	SC59W	СР
AP130-33W	SC59W	CS
AP130-35W	SC59W	CU
AP130-15R	SC59R	GO
AP130-18R	SC59R	GR
AP130-20R	SC59R	GT
AP130-25R	SC59R	GY
AP130-28R	SC59R	H1
AP130-30R	SC59R	H3
AP130-33R	SC59R	H9
AP130-35R	SC59R	НВ
AP130-15SA	SOT23	U2
AP130-18SA	SOT23	U3
AP130-20SA	SOT23	U4
AP130-25SA	SOT23	U5
AP130-28SA	SOT23	U6
AP130-30SA	SOT23	U7
AP130-33SA	SOT23	U8
AP130-35SA	SOT23	U9



Marking Information (cont.)

(2) SOT89-3L and SOT89R-3L



XX: Identification code

Y: Year: 0~9

<u>W</u>: Week: A~Z: 1~26 week; a~z: 27~52 week; z represents 52 and 53 week

X: Internal code A~Z : Green a~z : Lead Free

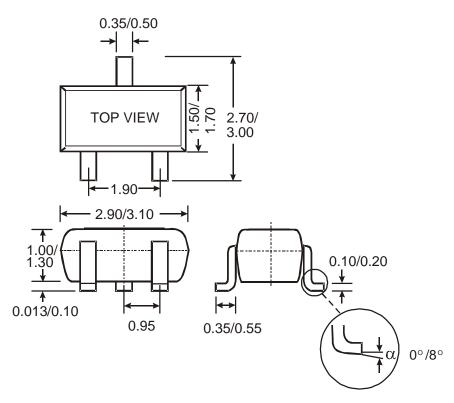
Device	Package (Note 11)	Identification Code
AP130-15Y	SOT89-3L	CA
AP130-18Y	SOT89-3L	CD
AP130-20Y	SOT89-3L	CF
AP130-25Y	SOT89-3L	CK
AP130-28Y	SOT89-3L	CN
AP130-30Y	SOT89-3L	CP
AP130-33Y	SOT89-3L	CS
AP130-35Y	SOT89-3L	CU
AP130-15YR	SOT89R-3L	GO
AP130-18YR	SOT89R-3L	GR
AP130-20YR	SOT89R-3L	GT
AP130-25YR	SOT89R-3L	GY
AP130-28YR	SOT89R-3L	H1
AP130-30YR	SOT89R-3L	H3
AP130-33YR	SOT89R-3L	H9
AP130-35YR	SOT89R-3L	HB

Notes: 11. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

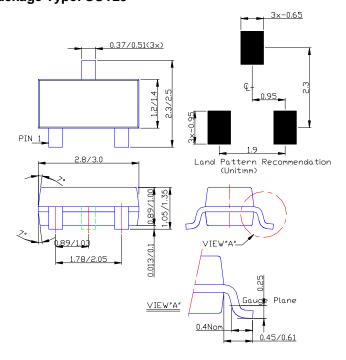


Package Outline Dimensions (All Dimensions in mm)

(1) Package Type: SC59 and SC59R



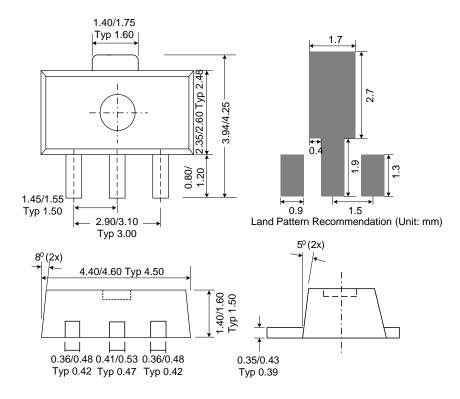
(2) Package Type: SOT23





Package Outline Dimensions (cont.)

(3) Package Type: SOT89-3L and SOT89R-3L





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