

TABLE 1. ELECTRICAL TESTS:

| TEST | Symbol | CONDITIONS | | Group A Subgroup | Device type | Limits Min | Limits Max | Units |
|-----------------------|----------------------|--|--|---------------------|-----------------------------|---------------------|-----------------|-------|
| | | -55 °C ≤ T _A ≤ +125 °C +4.5V ≤ V _{DD} ≤ 18V Unless otherwise specified | | | | | | |
| Logic 1 Input Voltage | V _{IH} | | | 1,2,3 | All | 2.4 | | V |
| Logic 0 Input Voltage | V _{IL} | | | 1,2,3 | All | | 0.8 | V |
| Input Voltage Range | V _{IN(max)} | | | 1,2,3 | All | 0 | V _{DD} | V |
| Input Current | I _{IN} | V _{IN} = 0V to 18V | | 1 2,3 | All | | ±1 ±10 | µA |
| Output High Voltage | V _{OH} | R _L = ∞ | | 1,2,3 | All | V _{DD} -25 | | mV |
| Output Low Voltage | V _{OL} | R _L = ∞ | | 1,2,3 | All | | 25 | mV |
| Output Resistance | R _{OUT} | V _{DD} = 18V, I _{OUT} = 10mA NOTE 3, NOTE 4 | | 1,2 3 | All | | 20 15 | Ω |
| Power Supply Current | I _{S1} | V _{IN} = +3V, both inputs | | 1,2 3 | All | | 8 12 | mA |
| Power Supply Current | I _{S2} | V _{IN} = 0V, both inputs | | 1,2 3 | All | | 0.4 0.6 | mA |
| Rise Time | t _r | V _{DD} = 18V NOTE 2 | | 9 10,11 10,11 | All 01,02,03 04,05,06 | | 30 40 60 | ns |
| Fall Time | t _f | V _{DD} = 18V NOTE 2 | | 9 10,11 | All | | 30 40 | ns |
| Delay Time | t _{D1} | V _{DD} = 18V NOTE 2 | | 9 10,11 | 01,02,03 | | 30 40 | ns |
| | | | | 9 10,11 | 04,05,06 | | 40 60 | |
| Delay Time | t _{D2} | V _{DD} = 18V NOTE 2 | | 9 10,11 | 01,02,03 | | 50 75 | ns |
| | | | | 9 10,11 | 04,05,06 | | 60 120 | |

NOTE 1: Guaranteed by design.

NOTE 2: Subgroups 10 and 11 are guaranteed if not tested to the limits specified in Table 1.

NOTE 3: V_{IN} = 0.8V for inverting stages, V_{IN} = 2.4V for noninverting stages.

NOTE 4: V_{IN} = 2.4V for inverting stages, V_{IN} = 0.8V for noninverting stages.

TERMINAL CONNECTIONS FOR 01, 02, 03, 04, 05, 06

| 8 PIN CERDIP | | | | 20 PIN LCC | | | | | | | |
|--------------|-----------------|-----------------|-----------------|------------|-------|-------|-------|----|-----------------|-----------------|-----------------|
| | 01,04 | 02,05 | 03,06 | | 01,04 | 02,05 | 03,06 | | 01,04 | 02,05 | 03,06 |
| 1 | NC | NC | NC | 1 | NC | NC | NC | 11 | NC | NC | NC |
| 2 | INA | INA | INA | 2 | NC | NC | NC | 12 | NC | NC | NC |
| 3 | GND | GND | GND | 3 | NC | NC | NC | 13 | NC | NC | NC |
| 4 | INB | INB | INB | 4 | INA | INA | INA | 14 | _____ | OUTB | OUTB |
| 5 | _____ | OUTB | OUTB | 5 | NC | NC | NC | 15 | NC | NC | NC |
| 6 | V _{DD} | V _{DD} | V _{DD} | 6 | GND | GND | GND | 16 | V _{DD} | V _{DD} | V _{DD} |
| 7 | _____ | OUTA | _____ | 7 | NC | NC | NC | 17 | NC | NC | NC |
| 8 | NC | NC | NC | 8 | INB | INB | INB | 18 | _____ | OUTA | _____ |
| | | | | 9 | NC | NC | NC | 19 | NC | NC | NC |
| | | | | 10 | NC | NC | NC | 20 | NC | NC | NC |

QUALITY ASSURANCE

Sampling and inspection procedures shall be in accordance with MIL-Prf-38535, Appendix A as specified in Mil-Std-883.

Screening shall be in accordance with Method 5004 of Mil-Std-883. Burn-in test Method 1015:

1. Test Condition, A, B, C, or D.
2. TA = +125°C minimum.
3. Interim and final electrical test requirements shall be specified in Table 2.

Quality conformance inspection shall be in accordance with Method 5005 of Mil-Std-883, including Groups A, B, C, and D inspection.

Group A inspection:

1. Tests as specified in Table 2.
2. Selected subgroups in Table 1, Method 5005 of Mil-Std-883 shall be omitted.

Group C and D inspections:

- a. End-point electrical parameters shall be specified in Table 1.
- b. Steady-state life test, Method 1005 of Mil-Std-883:
 1. Test condition A, B, C, D.
 2. TA = +125°C, minimum.
 3. Test duration, 1000 hours, except as permitted by Method 1005 of Mil-Std-883.

TABLE 2. ELECTRICAL TEST REQUIREMENTS

| Mil-Std-883 Test Requirements | Subgroups per Method 5005, Table 1 |
|---|------------------------------------|
| Interim Electric Parameters Method 5004 | 1 |
| Final Electrical Parameters Method 5005 | 1*, 2, 3, 9, 10**, 11** |
| Group A Test Requirements Method 5005 | 1, 2, 3, 9, 10**, 11** |
| Group C and D End-Point Electrical Parameters Method 5005 | 1 |

* PDA applies to Subgroup 1 only.

** Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits of Table 1.

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