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Electrical Characteristics (@ $T_A = 25$ °C Unless Otherwise Noted)

Symbol	Parameter		Part Number	Min.	Тур.	Max.	Unit
I _{trigger}	Current required for the device to g protected state	o from operating state to	TBU-CAxxx-050-WH TBU-CAxxx-100-WH TBU-CAxxx-200-WH TBU-CAxxx-300-WH TBU-CAxxx-300-WH	50 100 200 300 500	75 150 300 450 750	200 200 0 400 600 0 600 1000	mA
R _{device}	$ \begin{array}{l} V_{imp} = 250 \\ V_{imp} = 250 \\ V_{imp} = 250 \\ V_{imp} = 250 \\ V_{imp} = 400 \\ V_{imp} = 500 \\ V_{imp} = 650 \\ V_{imp} = 650 \\ V_{imp} = 650 \\ V_{imp} = 850 \\ V_{imp$	0 V Itrigger (min.) = 50 mA 0 V Itrigger (min.) = 100 mA 0 V Itrigger (min.) = 200 mA 0 V Itrigger (min.) = 300 mA 0 V Itrigger (min.) = 500 mA 0 V Itrigger (min.) = 200 mA 0 V Itrigger (min.) = 300 mA 0 V Itrigger (min.) = 500 mA <	TBU-CA025-050-WH TBU-CA025-100-WH TBU-CA025-200-WH TBU-CA025-200-WH TBU-CA025-300-WH TBU-CA025-500-WH TBU-CA040-050-WH TBU-CA040-050-WH TBU-CA040-000-WH TBU-CA040-200-WH TBU-CA040-200-WH TBU-CA040-200-WH TBU-CA040-500-WH TBU-CA050-050-WH TBU-CA050-050-WH TBU-CA050-200-WH TBU-CA065-050-WH TBU-CA065-050-WH TBU-CA065-050-WH TBU-CA065-000-WH TBU-CA065-000-WH TBU-CA065-000-WH TBU-CA065-000-WH TBU-CA085-000-WH TBU-CA085-050-WH TBU-CA085-050-WH TBU-CA085-050-WH TBU-CA085-000-WH TBU-CA085		$\begin{array}{c} 13.3\\ 7.1\\ 4.2\\ 3.2\\ 2.6\\ 14.3\\ 8.1\\ 5.2\\ 4.3\\ 3.6\\ 15.7\\ 9.5\\ 6.6\\ 5.6\\ 5.0\\ 17.7\\ 11.5\\ 8.6\\ 7.6\\ 7.0\\ 21.4\\ 15.2\\ 12.3\\ 11.3\\ 10.7\\ \end{array}$	15.3 8.2 4.8 3.8 3.0 16.5 9.4 6.0 5.0 4.2 18.0 10.9 7.5 6.5 5.7 20.3 13.2 9.8 8.8 8.0 24.5 17.4 14.0 13.0 12.2	Ω
t _{block}	Time for the device to go from norr	nal operating state to protect	ted state			1	μs
l _Q	Current through the triggered TBU	device with 50 Vdc circuit v	voltage	0.25	0.50	1.00	mA
V _{reset}	Voltage below which the triggered	TBU® device will transition to	o normal operating state	12	16	20	V
R _{th(j-l)}	Junction to package pads - FR4 us	ing recommended pad layou	ut		98		°C/W
R _{th(j-l)}	Junction to package pads - FR4 us	ing heat sink on board (6 cm	1 ²) (1 in ²)		40		°C/W

Environmental Characteristics

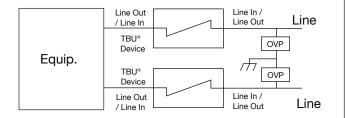
Parameter	Value
Moisture Sensitivity Level	1
ESD Classification (HBM)	1B

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Reference Application

The TBU[®] devices are general use protectors used in a wide variety of applications. The maximum voltage rating of the TBU[®] device should never be exceeded. Where necessary, an OVP should be employed to limit the maximum voltage. A costeffective protection solution combines Bourns[®] TBU[®] protection devices with a pair of Bourns[®] MOVs. For bandwidth sensitive applications, a Bourns[®] GDT may be substituted for the MOV.



Basic TBU Operation

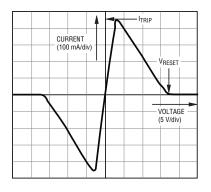
The TBU® device, constructed using MOSFET semiconductor technology, placed in the system circuit will monitor the current with the MOSFET detection circuit triggering to provide an effective barrier behind which sensitive electronics are not exposed to large voltages or currents during surge events. The TBU® device operates in approximately 1 μ s - once line current exceeds the TBU® device's trigger current l_{trigger}. When operated, the TBU® device will limit the current to less than the l_{trigger} value within the t_{block} duration. If voltage above V_{reset} is continuously sustained, the TBU® device will subsequently reduce the current to a quiescent current level within a period of time that is dependent upon the applied voltage.

After the surge, the TBU[®] device resets when the voltage across the TBU[®] device falls to the V_{reset} level. The TBU[®] device will automatically reset on lines which have no DC bias or have DC bias below V_{reset} (such as unpowered signal lines).

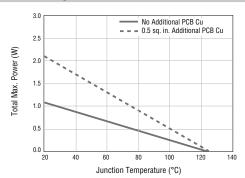
If the line has a normal DC bias above V_{reset} , the voltage across the TBU[®] device may not fall below V_{reset} after the surge. In such cases, special care needs to be taken to ensure that the TBU[®] device will reset, with software monitoring as one method used to accomplish this. Bourns application engineers can provide further assistance.

Performance Graphs

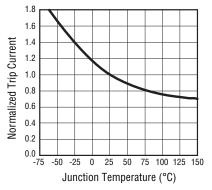
Typical V-I Characteristics (TBU-CA050-300-WH)



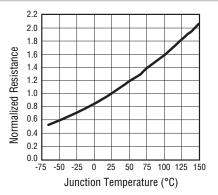
Power Derating Curve



Typical Trigger Current vs. Temperature



Typical Resistance vs. Temperature



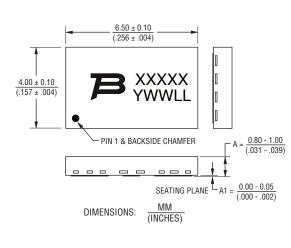
Specifications are subject to change without notice.

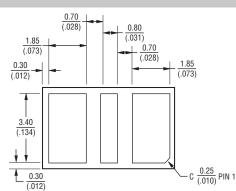
Users should verify actual device performance in their specific applications.

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Product Dimensions

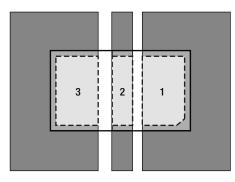




Pad Designation							
Pad #	Pin Out						
1	Line In/Out						
2	NU						
3	Line Out/In						

Recommended Pad Layout

TBU® High-Speed Protectors have a 100 % matte-tin termination finish. For improved thermal dissipation, the recommended layout uses PCB copper areas which extend beyond the exposed solder pad. The exposed solder pads should be defined by a solder mask which matches the pad layout of the TBU® device in size and spacing. It is recommended that they should be the same dimension as the TBU® pads but if smaller solder pads are used, they should be centered on the TBU® package terminal pads and not more than 0.10-0.12 mm (0.004-0.005 in.) smaller in overall width or length. Solder pad areas should not be larger than the TBU® pad sizes to ensure adequate clearance is maintained. The recommended stencil thickness is 0.10-0.12 mm (0.004-0.005 in.) with a stencil opening size 0.025 mm (0.0010 in.) less than the solder pad size. Extended copper areas beyond the solder pad significantly improve the junction to ambient thermal resistance, resulting in operation at lower junction temperatures with a corresponding benefit of reliability. All pads should soldered to the PCB, including pads marked as NC or NU but no electrical connection should be made to these pads. For minimum parasitic capacitance, it is recommended that signal, ground or power signals are not routed beneath any pad.



Dark grey areas show added PCB copper area for better thermal resistance.

Specifications are subject to change without notice.

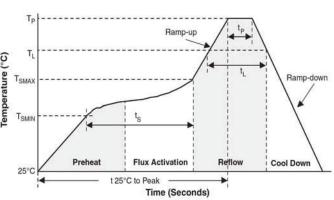
Users should verify actual device performance in their specific applications.

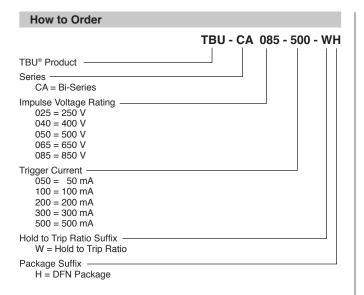
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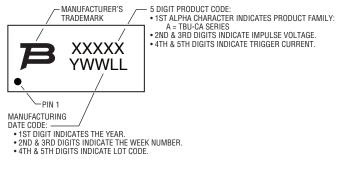
Reflow Profile

Profile Feature	Pb-Free Assembly	T
Average Ramp-Up Rate (Tsmax to Tp)	3 °C/sec. max.	Tp
Preheat - Temperature Min. (Tsmin) - Temperature Max. (Tsmax) - Time (tsmin to tsmax)	150 °C 200 °C 60-180 sec.	(°C) L ^L
Time maintained above: - Temperature (TL) - Time (tL)	217 °C 60-150 sec.	emperati
Peak/Classification Temperature (Tp)	260 °C	
Time within 5 °C of Actual Peak Temp. (tp)	20-40 sec.]
Ramp-Down Rate	6 °C/sec. max.	25°C
Time 25 °C to Peak Temperature	8 min. max.	

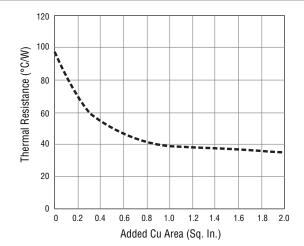




Typical Part Marking



Thermal Resistance vs Additional PCB Cu Area

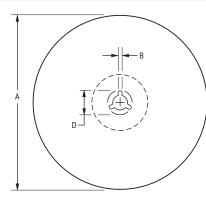


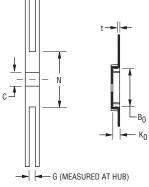
Specifications are subject to change without notice.

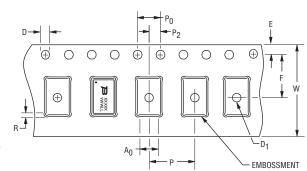
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Packaging Specifications





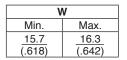


The type of corner on carrier will vary at different assembly sites.

USER DIRECTION OF FEED QUANTITY: 3000 PIECES PER REEL

ŀ	4	E	3		0		2	G	N
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Ref.	Ref.
<u>326</u> (12.835)	<u>330</u> (13.002)	<u>1.5</u> (.059)	<u>2.5</u> (.098)	<u>12.8</u> (.504)	<u>13.5</u> (.531)	<u>20.2</u> (.795)	-	<u>16.5</u> (.650)	<u>102</u> (4.016)

A	0	B	0	[D	D	1	E		F	
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	max.
<u>4.3</u> (.169)	<u>4.5</u> (.177)	<u>6.7</u> (.264)	<u>6.9</u> (.272)	<u>1.5</u> (.059)	<u>1.6</u> (.063)	<u>1.5</u> (.059)	-	<u>1.65</u> (.065)	<u>1.85</u> (.073)	<u>7.4</u> (.291)	<u>7.6</u> (.299)
K0		Р		P0		P2		R		t	
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.
1.0	1.2	7.9	8.1	3.9	4.1	$\frac{1.9}{(.075)}$	<u>2.1</u> (.083)	0 (0)	<u>0.5</u> (.020)	<u>0.25</u> (.010)	0.35



MM DIMENSIONS: (INCHES)

REV. 03/18

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 TBU-CA025

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